



# FACTORY AUTOMATION

# MELSEC iQ-F Series iQ Platform-compatible PLC





The next level of industry



# GLOBAL IMPACT OF MITSUBISHI ELECTRIC



Through Mitsubishi Electric's vision, "Changes for the Better" are possible for a brighter future.

## Changes for the Better

We bring together the best minds to create the best technologies. At Mitsubishi Electric, we understand that technology is the driving force of change in our lives. By bringing greater comfort to daily life, maximizing the efficiency of businesses and keeping things running across society, we integrate technology and innovation to bring changes for the better. Mitsubishi Electric is involved in many areas including the following

#### **Energy and Electric Systems**

A wide range of power and electrical products from generators to large-scale displays.

#### **Electronic Devices**

A wide portfolio of cutting-edge semiconductor devices for systems and products.

#### **Home Appliance**

Dependable consumer products like air conditioners and home entertainment systems.

#### Information and Communication Systems

Commercial and consumer-centric equipment, products and systems.

#### **Industrial Automation Systems**

Maximizing productivity and efficiency with cutting-edge automation technology.

# **OVERVIEW**

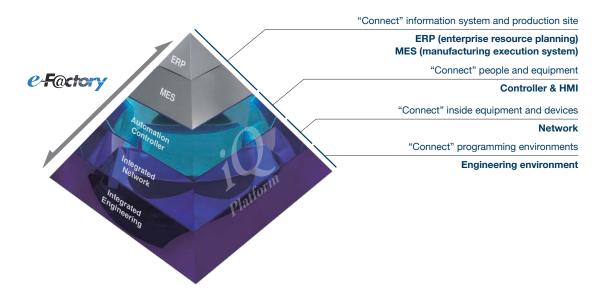
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## Concept



## "Connect" Factory Automation with iQ Platform

"iQ Platform", a solution that integrates and cooperates with controllers, HMI, engineering environments, and networks at the production site, Mitsubishi Electric has proposed along with "e-F@ctory" that information-links the high-level information system (manufacturing execution system (MES)) and production site, will integrate and optimize your system with advanced technology to reduce development, production and maintenance costs.



## Fundamentally Solving FA's Task from the Viewpoint of TCO

## **Controller & HMI**

# Improving productivity and product quality

- Significant improvement in total system performance due to high-speed MELSEC series system bus performance
- Equipped with dedicated memory for FB\*1/ label required for program standardization
- 3. Integrated, enhanced security function

#### Network

# Loss reduction with high precision and production speed

- Possible to connect to, without loss,
   Gbps high-speed communication realized by CC-Link IE Field Network
- Realizing seamless communication of various devices using SLMP\*2

#### **Engineering environment**

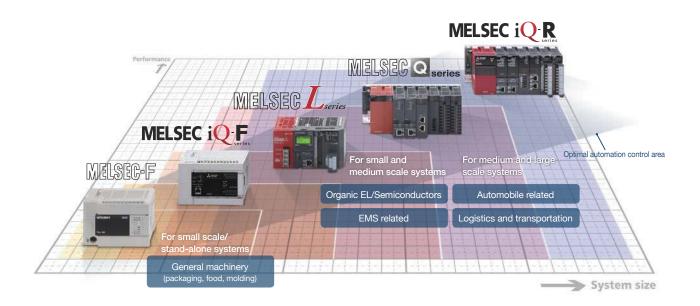
# Efficient development, operation, and maintenance

- Possible to detect and generate a largescale network configuration diagram from the actual machine
- 2. Realized mutual reflection of parameters between MELSOFT Navigator and each engineering software
- Automatically following device change of system labels held commonly between each controller and HMI



# MELSEC

The MELSEC series offer optimum automation control with a wide variety of products from compact systems to plant scale systems. Series specialized for specific functions to meet all the needs of the production site are also provided.



## For small scale/standalone systems



#### **MELSEC-F** series

Abundant functions and extendability housed in a compact body. All-in-one PLC with power supply, CPU, and I/O. Responds to various needs by connecting a wide variety of extension equipment.



#### **MELSEC iQ-F series**

Next-generation micro PLC that can support high speed of the system bus, enhanced built-in functions, and varieties of networks. A system from stand-alone to network use can be proposed, to strongly support the customer to "go one step ahead in manufacturing".

#### MELSEC-L series

Space inside the control panel saved by adopting a baseless structure. Condensed the function, performance, and operability required by the site into a compact body, realizing easy-to-use and more versatile control.

## For medium and large scale systems

For small and medium scale systems



#### MELSEC-Q series

Realized high speed control by parallel processing using the multi-CPU function, improving the performance of customer's equipment and machine.



#### MELSEC iQ-R series

An innovative next-generation controller that opens a new era of automation. Realized a substantial reduction in takt time with a newly developed high-speed system bus mounted.

# MELSEC iQ-F series

Designed on the concepts of outstanding performance, superior drive control and user centric programming, Mitsubishi's MELSEC-F series has been reborn as the **MELSEC iQ-F series.** 



From stand-alone use to networked system applications, MELSEC iQ-F series brings your business to the next level of industry.



## Function and cost performance required for small-scale/stand-alone control



## **Built-in functions**

Even easier to use with the fulfilling built-in functions. Supports the customer to "go one step ahead in manufacturing".



Analog control

Analog control suitable for the application is possible by using expansion modules in addition to the analog input/output function of the CPU module.



control

Not only built-in positioning but full positioning is also possible by extension modules.

```
For details, go to P8.
```

For details, go to P14.

For details, go to P18.

## Design concept of micro PLC

## Performance

Outstanding performance

- High-speed system bus
- Extensive built-in functions
- Enhanced security functions
- Battery-less

#### Affinity Cooperation with driving equipment

- Easy built-in positioning (4-axis 200 kpps)
- Simple interpolation functions
- 4/8-axis synchronization control (no special software required) by simple motion module



#### Programmer's workbench Improvement of programming environment

- Easy programming by drag and drop
- Reduced development time with module FB
- Parameterized setup for a variety of functions





Network/ communication

Supports the network of AnyWireASLINK system as well as CC-Link IE Field Network and CC-Link V2.



# Programming environment

Realized graphical intuitive operability, and easy programming by just "selecting".

For details, go to P22.

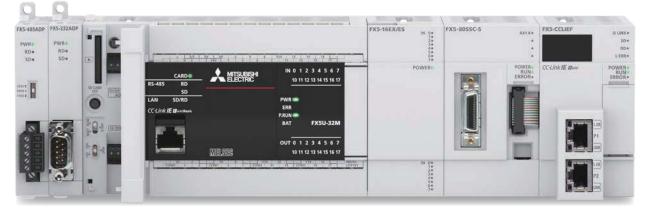
For details, go to P30.

7



The CPU module has excellent built-in functions to respond to various types of control. Ethernet port, RS-485 port, and SD memory card slot are standard equipment. The Ethernet port is compatible with CC-Link IE Field Network Basic and can be connected to a wide variety of equipment.

# **X**5U



## **CPU** Performance

The MELSEC iQ-F series has a CPU capable of high-speed processing with an instruction operation speed (LD instruction) of 34 ns\*2. In addition, the CPU supports execution of structured programs and multiple programs, ST language, FB etc.



## **High-speed System Bus Communication**

With the high-speed CPU, the MELSEC iQ-F series realizes high-speed system bus communication of 1.5 K words/ms (about 150 times compared to FX3U), and can deliver to its full potential when using an intelligent function module handling a large amount of communication data.



## **Built-in Analog Input/Output** (with alarm output)



**Battery-less and Maintenance-free** 

In the MELSEC iQ-F series, programs and devices are held in a batteryless\*3 memory such as flash ROM.

## The FX5U has built-in 12-bit 2-channel analog

voltage input and 1-channel analog voltage output.



#### \*1: Supported by FX5U/FX5UC Ver. 1.100 or later, and product number 17X\*\*\*\* (product number 178\*\*\*\* for FX5UC-32MT/DS-TS and FX5UC-32MT/DS-TS) or later. Some operation restrictions apply when 128 k steps is selected. For details, refer to the manual.

\*2: When the program capacity is 64 k steps.

\*3: Using an optional battery can increase the capacity of the device.

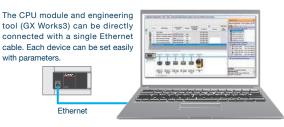


Connector type

Spring clamp terminal block type

## **Built-in Ethernet Port**

The Ethernet communication port can handle communication of up to 8 connections on the network, and can support multiple connections with personal computer and other devices. In addition, the Ethernet communication port can handle seamless SLMP communication with the upper-level device.



## Built-in RS-485 port (with MODBUS function)

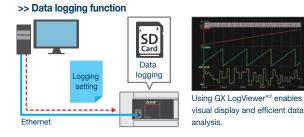
Connect to serial devices up to 50 m away with built-in RS-485 port. Control for up to 16 Mitsubishi electric inverters is possible with dedicated inverter communication instructions.

MODBUS is also supported and can connect up to 32 MODBUS devices such as PLCs, sensors and



## **Built-in SD Memory Card Slot**

A built-in SD memory card slot is convenient for updating the program and mass production of equipment. Data can be logged\*<sup>1</sup> in SD memory card, making it easy to analyze the system status and production state, etc.



## **RUN/STOP/RESET Switch**

RUN/STOP/RESET switch is built in. PLC can be rebooted without turning off the main power for efficient debugging.



\*1: Supported by FX5U/FX5UC Ver. 1.040 or later and product number 16Y\*\*\*\* or later, by GX Works3 Ver. 1.030G or later, and by CPU Module Logging Configuration Tool Ver. 1.64S or later.

## \*2: Supported by GX LogViewer Ver. 1.64S or later.

temperature controllers.

#### **Function introduction**

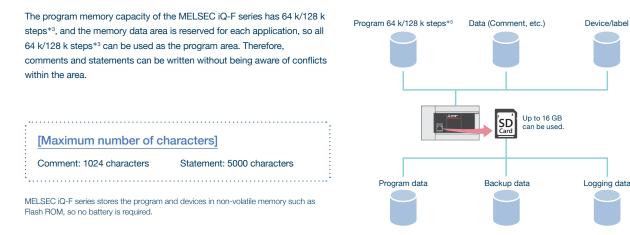




Logging Section YouTube MITSUBISHI ELECTBIC

MITSUBISHI ELECTRIC Factory Automation MELSEC iQ-F Technical Video

## Memory area for each application



## Data logging function\*1\*2

Information can be saved to the SD memory card periodically from the computer and network equipment. Using the saved data enables efficient analysis of device operating status and trouble causes. If simple settings are made with the logging setting tool, no additional program is required.

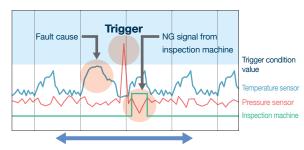
A trouble can be analyzed efficiently by [trigger logging] which logs only the situation before and after the occurrence of trouble. Important data can be selectively saved by setting conditions.

With the FTP server function\*4, logging data can be acquired from a

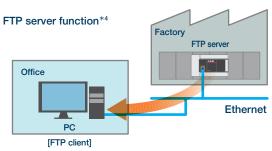
remote location without going to the site. Multiple logging files can be

managed collectively from the office computer, reducing management

and maintenance work.



# Collects data before and after occurrence of a trouble!

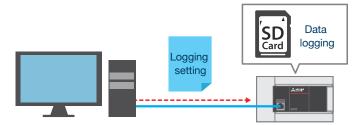


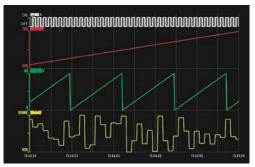
# Logs can be examined and utilized from remote locations!

- \*1: Supported by FX5U/FX5UC Ver. 1.040 or later and product number 16Y\*\*\*\* or later, by GX Works3 Ver. 1.030G or later, and by CPU Module Logging Configuration Tool Ver. 1.64S or later.
- \*2: The data logging function and memory dump function cannot be used simultaneously. There are some restrictions on the use of the backup/restore functions. For details, refer to the manual.
- \*3: Supported by FX5U/FX5UC Ver. 1.100 or later, product number 17X\*\*\*\* (product number 178\*\*\*\* for FX5UC-32MT/DS-TS) or later, and GX Works3 Ver. 1.047Z or later. Some operation restrictions apply when 128 k steps is selected. For details, refer to the manual.
- \*4: Supported by FX5U/FX5UC Ver. 1.040 or later and product number 16Y\*\*\*\* or later, and by GX Works3 Ver. 1.030G or later.

## Efficiently analyzing logging data with GX LogViewer\*1

GX LogViewer\*<sup>1</sup> is a tool to display and analyze large volumes of data collected by modules with the data logging function\*<sup>2</sup>, with easy-to-understand operations. It enables the setting of the connection destination by the same operation as the setting tool and engineering tool, and thereby enables easy checking of the logging file.



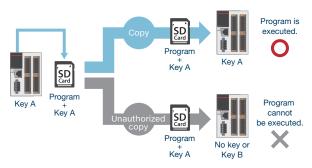


Using GX LogViewer\*1 enables visual display and efficient data analysis.

## Security

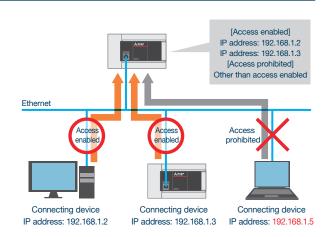
It prevents data theft, tampering, misoperation, illegal execution, etc. caused by unauthorized access from a third party with the security functions (block password, file password, remote password, security key authentication).





## IP filter function\*3

When the IP address to be permitted or blocked is set in the MELSEC iQ-F Series built-in function parameters, access from specific devices are restricted. The access source IP address can be identified to prevent accessing from illegal IP addresses.



\*1: Supported by GX LogViewer Ver. 1.64S or later.

\*2: Supported by FX5U/FX5UC Ver. 1.040 or later and product number 16Y\*\*\*\* or later.

\*3: Supported by FX5U/FX5UC Ver. 1.050 or later, and GX Works3 Ver. 1.035M or later.



## Backup/restore functions\*1 (device/label data\*2\*3, data memory\*4)

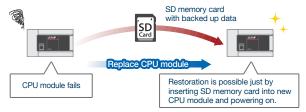
The device/label data and data memory in the CPU module can be backed up<sup>\*5</sup> to the SD memory card. Backed-up data can be restored as needed.

#### Back up data in case of an emergency!



When the SD memory card is mounted in the CPU module, the data can be backed up at an arbitrary timing. The backed up data can be restored at any timing.

#### Restoration is possible even without a PC!



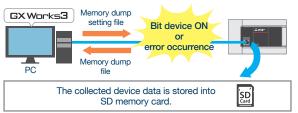
When the CPU module auto exchange function is used, the SD memory card data is automatically restored when the power is turned on or when the CPU module is reset. If the CPU module fails, it can recover promptly without a PC.

## Memory dump function\*6\*7

The CPU module device value can be saved in the SD memory card at an arbitrary timing.

By setting the trigger to be established when an error occurs, the status at error occurrence can be confirmed. This is helpful in investigating and pinpointing the cause.





Use the information when debugging systems under development, or for troubleshooting when trouble occurs at a remote location, etc.

#### Memory dump results display screen

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| RC.                     | 3491       | 1874     | 1895                 | 1000    | 1000     | 1644         | 14140               | 3416               |                   |
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The collection results can be confirmed with GX Works3. The device list can be displayed in the memory dump results display, and the memory dump conditions can be repeated on the offline monitor.

## ▲ Caution

If the data protected by the file password function exists in the CPU module, backup/restore is disabled. When setting the security key authentication function, the program cannot be executed unless the security key has been written to the CPU module.

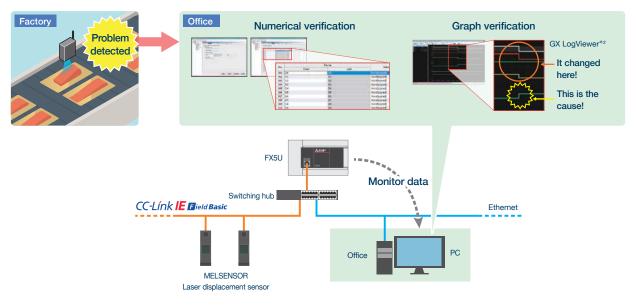
- \*1: While the backup/restore function is executed, some functions are temporarily unavailable. For details, refer to the manual.
- \*2: Supported by FX5U/FX5UC Ver. 1.045 or later.
- \*3: Excluding the buffer memory of the intelligent function module.
- \*4: Supported by FX5U/FX5UC Ver. 1.050 or later.
- \*5: Supported by FX5U/FX5UC product number 16Y\*\*\*\* or later.
- \*6: The memory dump function and data logging function are not simultaneously available. There are some restrictions on the use of the backup/restore functions. For details, refer to the manual.
- \*7: Supported by FX5U/FX5UC Ver. 1.050 or later and product number 16Y\*\*\*\* or later, and by GX Works3 Ver. 1.035M or later.

## **Real-time monitoring function**\*1

The contents of any devices can be monitored on real-time basis using GX LogViewer\*<sup>2</sup>. Because changes in device values are displayed in a trend graph, changes can be noticed at a glance!

The debugging efficiency is considerably improved at startup and troubleshooting. This function facilitates the resetting procedure, and enables graph check at a later time.

#### Real-time monitoring of data collected by CPU module using numerical values and graphs



## **Function introduction**



The FX5U CPU module has a built-in analog input/output function. In addition, it can also input and output analog quantities (voltage, current etc.) using expansion adapters and extension modules. Analog control suitable for the application is possible by using a variety of extension modules in addition to the analog input/output function of the CPU module.

#### 3 ch 4 ch 8 ch [8 ch] multi input ľ. Analog 2 ch (Selectable in channels) input Voltage · Current FX5-4AD-ADP FX5-4AD FX3U-4AD\* FX5-8AD 1 ch Analog FX5U CPU module 1 output FX5-4DA-ADP FX5-4DA FX3U-4DA\* Temperature sensor input Temperature control For thermocouple 4 ch FX5-4LC [4 ch] temperature input (Selectable in channels) 1 [4 ch] transistor output Two-position · Heating-cooling PID control [8 ch] multi input control (Selectable in channels) FX5-4AD-TC-ADP 4 ch PID control · Cascade control Temperature/ Resistance temperature temperature detector (Pt100, Ni100) For resistance temperature detector FX5-8AD · Thermocouple control 4 ch FX3U-4I C\* (K, J, T, B, R, S) [4 ch] temperature input (Selectable in channels) [4 ch] transistor output Two-position · Heating-cooling control PID control FX5-4AD-PT-ADP 4 ch · PID control · Cascade control

## List of models

## Analog input/output (with alarm output) control using built-in function

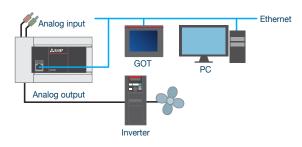


The FX5U CPU module has built-in 12-bit 2-channel analog voltage input and 1-channel analog voltage output.

It can be used with only parameter setting without programming. Numerical shift, scaling setting, and alarm output setting can also be easily set with parameters.

#### Example of inverter control using analog output

FX5U CPU module



#### New compact\*1 4 ch products capable of analog input/analog output



Analog input module FX5-4AD Analog output module FX5-4DA

#### Conversion speed "80 µs/ch" realized

#### 4AD 4DA

Both the analog input module and the analog output module have realized the conversion speed as fast as 80  $\mu$ s/ch, which has considerably improved compared with conventional modules.

#### Analog input module



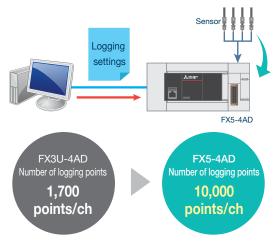
#### Analog output module

| FX3U-4DA | 1000 µs*²  |
|----------|--|
| FX5-4DA  | 80 μs/ch<br>Time reduction to<br>approx. 1/12*3! |

#### Logging function to cope with troubles

#### 4AD

By using the logging function, the operator can acquire data at a specified interval or any timing. The operator can analyze data acquired before and after occurrence of a trouble, and efficiently investigate causes of the trouble.



## Analog processing of higher accuracy



The accuracy has improved in analog inputs and analog outputs. The analog processing of higher accuracy has been enabled.



# Wave output function offering smooth wave without any program

#### 4DA

- The operator can easily create graphical wave output data expressed in arcs and straight lines using GX Works3.
- The operator can update analog output values in the D/A conversion cycle (80 µs at highest speed) without depending on the scan time.
- The operator can register the wave output data in the analog output module, and repeatedly use them to reduce the man-hours for programming.
- Analog output Drastic changes are output smoothly. Output value update interval Up to 80000 points of wave output data can be registered. Wave closer to the wave to be output can be obtained!
- With analog output using the wave output function An analog value is output at a constant interval.

\*1: When compared with Mitsubishi FX3U-4AD and FX3U-4DA.\*3: In the case of 1 ch use

\*2: 1000 µs without regard to the number of channels.

\*4: When the ambient temperature is 25±5°C, and the "-10 to +10 V" range is selected.

15



# Voltage, current, thermocouple, and resistance temperature detector inputs can be used for multiple applications with a single module!



# Providing support for various applications

Voltage, current, thermocouple (K, J, T, B, R, S), and resistance temperature detector (Pt100, Ni100) inputs are supported.

Possible to set input type per channel!

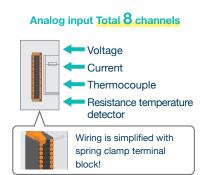
#### Multiple input module FX5-8AD

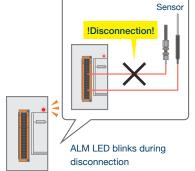
#### **Easily detect disconnection**

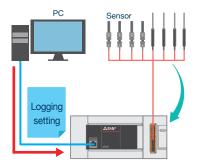
Thermocouple and resistance temperature detector disconnection can be easily detected, so downtime and maintenance cost can be reduced.

# Analyze problems with logging function

10000 points of data per channel can be logged and stored to buffer memory. If the log is saved, it can be useful in investigating the cause of the problem.







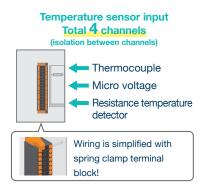


## 4-channel input/output compatible temperature control is possible!

# Various temperature sensors can be used

Supports thermocouple, resistance temperature detector, and micro voltage inputs. Possible to support a variety of applications.

#### Possible to set input type per channel!

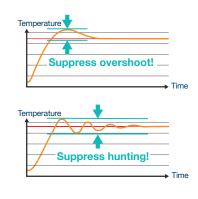


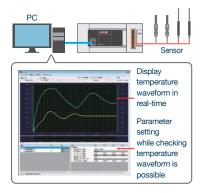
#### Temperature control module FX5-4LC

#### **PID control supported**

Overshooting where the output value exceeds the target value, and hunting phenomenon where vibration occurs around the target value can be suppressed. Supports temperature trace

Temperature change can be checked on a waveform. While checking the temperature waveform displayed in realtime, parameters can be adjusted.





memo



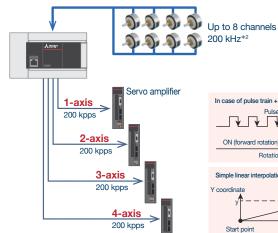
The FX5U/FX5UC CPU module has a built-in positioning function. Complex multi-axis/interpolation control is also possible by using a high-speed pulse input/output module or simple motion module.

## List of models



# Built-in positioning (200 kpps, 4 axes built in) compatible with high-speed startup of 20 µs





The FX5U/FX5UC CPU module is equipped with the high-speed counter function with 8 channels high-speed pulse input channels and the built-in positioning function by 4-axis pulse output. In addition to conventional functions, such as interrupt stop operations and variable speed operations, new functions are added, making the built-in positioning function easier to use.

FX5U/FX5UC CPU module

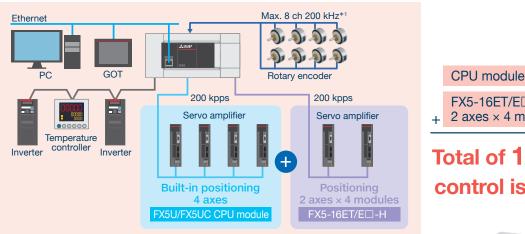
## In case of pulse train + sign Pulse output ON (forward rotation) Rotation direction Simple linear interpolation (2-axis simultaneous start) Y coordinate Target point v (x, y)



\*1: Connection with FX5U/FX5UC requires FX5-CNV-BUS or FX5-CNV-BUSC.
 \*2: 6 ch 200 kHz + 2 ch 10 kHz only for FX5U-32M and FX5UC-32M.

# Reasonably realizing multi-axis control with CPU module and high-speed pulse input/output module

## High-speed pulse input/output module FX5-16ET/ES-H, FX5-16ET/ESS-H



# CPU module4 axesFX5-16ET/E□-H2 axes × 4 modules = 8 axes

# Total of 12 axes of control is possible!

## Faster startup and 2-axis positioning for increased flexibility!

2-axis pulse train positioning module FX5-20PG-P (Transistor output) FX5-20PG-D (Differential driver output) NEW

# Introducing differential driver output positioning modules

In addition to transistor output models, a new differential driver output model has been added to the lineup.





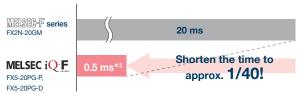
Transistor output type FX5-20PG-P

FX5-20PG-D

#### **High-speed start realized**

The high-speed normal positioning starting process speed can shorten the starting time to 0.5 ms.

#### Comparison of starting times for 1-axis linear control



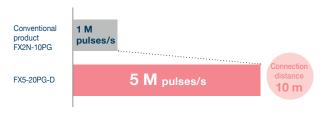
\*1: 6 ch 200 kHz + 2 ch 10 kHz only for FX5U-32M and FX5UC-32M

\*2: For FX5-20PG-P, the maximum pulse output is 200k pulses/s, and the maximum connection distance is 2 m.

- \*3: 1-axis linear control/1-axis speed control. For other controls, refer to the manual.
- \*4: Start by external command signal. 30 µs in the case of start by positioning start signal.

# The maximum output pulse is 5 M pulses/s, and the connection distance is 10 m.\*2

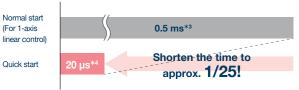
With maximum output pulses of 5 M pulses/s for the FX5-20PG-D, control is possible for devices with higher resolutions than conventional products. The maximum connection distance between servos is 10 m.



#### **Quick start function supported**

By analyzing the positioning data in advance, it is possible to start the positioning at a higher speed than the normal positioning start.

#### Comparison of starting times



## **Function introduction**



## Simple motion module (4/8-axis control module)

Simple motion module (4/8-axis control module) FX5-40SSC-S, FX5-80SSC-S

#### Positioning control with SSCNET III/H

The simple motion module is equipped with the 4/8-axis positioning function compatible with SSCNET III/H.

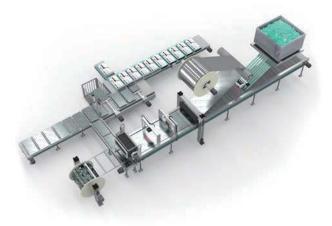
It can be used for various purposes by combining linear interpolation, 2-axis circular interpolation, constant quantity feed, and continuous path control in a table-based program.

#### Main functions

- Linear interpolation
- Circular interpolation
- Continuous path control
- S-curve acceleration/ deceleration

#### Application examples

- Sealing system
- Palletizer
- Grinding system



SSCNET III/H

## Making simple motion with compactly packed extra functions

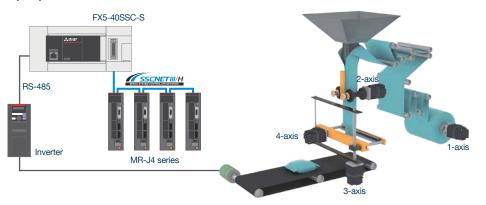
By starting with parameter settings and the sequence program, the simple motion modules can realize a variety of motion control including positioning control, advanced synchronous control, cam control and speed-torque control.

#### Synchronous control

In addition to synchronous control by replacing hardware mechanisms such as gears, shafts, transmissions, and cams with software, functions such as cam control, clutch, and cam auto generation can be easily realized. In addition, since synchronous control can be started and stopped for each axis, it is possible to mix the synchronous control axis and the positioning control axis.

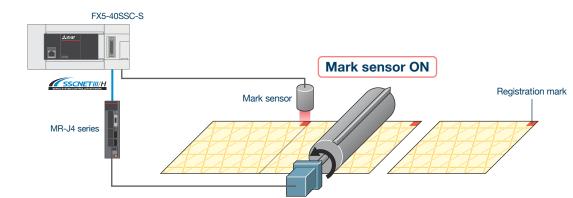
Up to four axes<sup>\*1</sup> can be synchronized to the synchronous encoder axis, enabling use with a variety of systems.

- Synchronous control and cam control can be used to build a system perfect for your equipment.
- Up to 64 types\*<sup>2</sup> of cam patterns can be registered to respond quickly to any type of contents.
- Continuous operation can be performed without stopping the workpiece.



\*1: FX5-80SSC-S: 8 axes \*2: FX5-80SSC-S: 128 types

#### **Mark detection function**



The cutter axis deviation can be compensated by detecting a mark on the workpiece so the workpiece can be cut at a constant position.

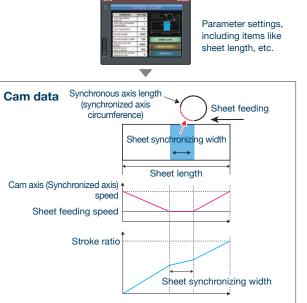
#### Cam data auto-generation

Cam data of the rotary cutter, which was conventionally difficult to create, can be automatically generated simply by inputting sheet length, synchronization width, cam resolution, etc.

Also, saving the cam data in the cam save area enables continuous use of the last cam data even after power off, and thus can shorten the start-up time of the system and realize multi-product production.

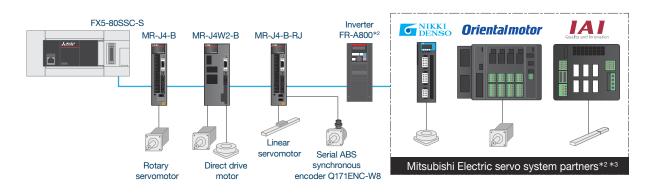
| Ite             | əm            | FX5-40SSC-S FX5-80SSC- |             |  |
|-----------------|---------------|------------------------|-------------|--|
| Memory          | Cam save area | 64 k bytes             | 128 k bytes |  |
| capacity        | Cam load area | 1024 k bytes           |             |  |
| Max. number of  | Cam save area | Up to 64               | Up to 128   |  |
| registrations*1 | Cam load area | Up to 256              |             |  |

User-created GOT screen



#### Various driving equipment

Not only rotary servomotors but also linear servomotors, direct drive motors, inverter FR-A800 series, and partner maker equipment can be connected.



\*1: The maximum number of registered cams varies depending on the memory capacity, cam resolution, and the number of coordinates. For details, refer to the manual.

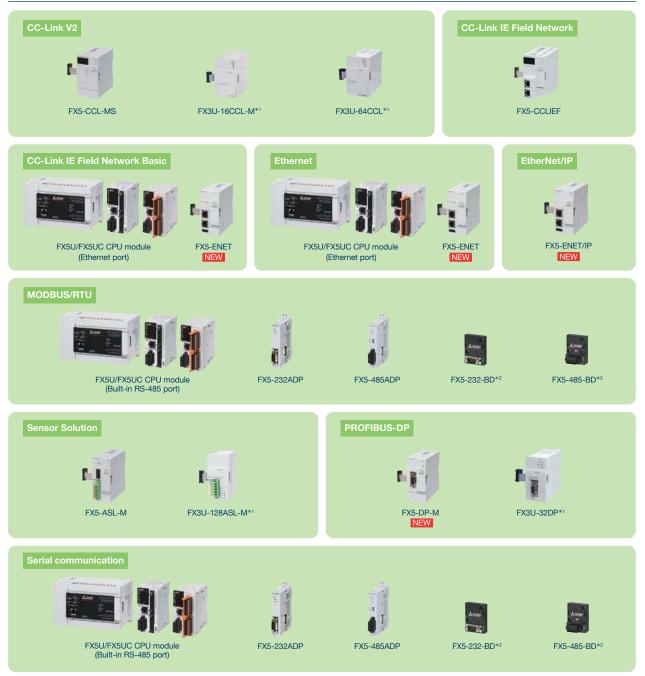
\*2: For partner products and inverter FR-A800, use the versions compatible with the simple motion module.

\*3: For details of partner products, refer to the servo system partner product catalog.



The MELSEC iQ-F series can build high-speed networks by CC-Link and other networks corresponding to the control contents such as Ethernet, MODBUS, Sensor Solution, and PROFIBUS-DP. In addition, CC-Link IE Field Network Basic is a factory automation network that utilizes general-purpose Ethernet connections to enable efficient creation of factory-wide systems.

## List of models



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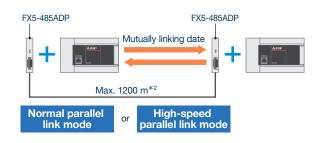
\*1: Connection with FX5U/FX5UC requires FX5-CNV-BUS or FX5-CNV-BUSC.\*2: Can be connected only to the FX5U CPU module.

## Communication using RS-485 or RS-232C equipment

#### Parallel link function\*1

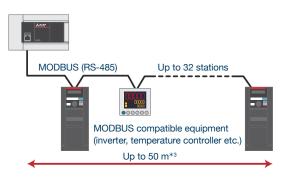
This function connects two CPU modules and automatically links mutual device data. ON/OFF status and data register values of the other station can be checked.

Normal parallel link mode/high-speed parallel link mode can be selected depending on the desired number of link points and link time. Parallel link can only be used on one channel of the CPU module.



#### **MODBUS** communications

FX5 PLC can connect, as a master or slave station of MODBUS communication, to various MODBUS communication devices.



#### **Non-protocol communication**

Non-protocol serial communication can be performed with RS-232C/RS-485 interface devices such as code readers, printers, personal computers, and measuring instruments.



#### **N:N Network**

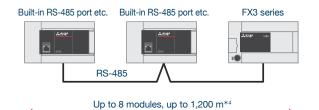
IVCK: Operation monitor
IVDR: Operation control
IVRD: Parameter read

IVWR: Parameter writeIVBWR: Parameter batch write

• IVMC: Multiple command

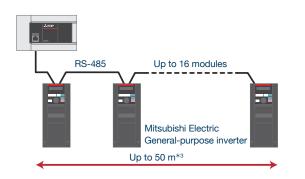
(2 types of settings and 2 types of read)

In this communication, a connection is set up with the FX5 PLC or FX3 PLC through RS-485 communication to automatically exchange data.



#### **Inverter communication**

Up to 16 inverters can be operated and controlled by RS-485 communication.



\*1: Supported by FX5U/FX5UC Ver. 1.050 or later, and GX Works3 Ver. 1.035M or later.

- \*2: 50 m or less when the built-in RS-485 port and FX5-485-BD are included.
- \*3: Built-in RS-485 or RS-485 expansion board

 $\star$ 4: When configured with FX5-485ADP. The distance varies depending on the type of communications equipment.



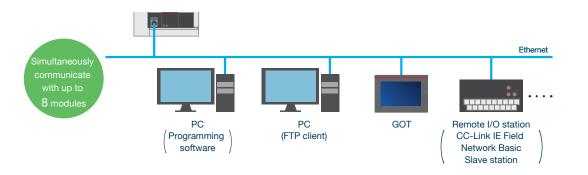
## **Network/communication**

## **Communication using Ethernet**

#### **Built-in Ethernet communication**

Compatible models: V Built-in Ethernet / Ethernet modules

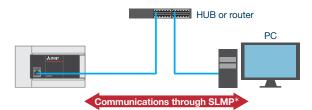
Supports CC-Link IE Field Network Basic, FTP server, and other protocols, and enables configuration of communication settings easily with parameters. Also supports various functions such as the GX Works3 diagnostic function, SLMP communication function, socket communication function, and IP address change function, and prevents unauthorized accesses from the outside by remote passwords.



#### **SLMP** communication

Compatible models: V Built-in Ethernet / Ethernet modules

Device data of a CPU module can be read from/written to the PC, etc. using SLMP\*, which is a common protocol. Because seamless communication is possible like a single network, equipment can be monitored and programs can be modified from anywhere in the office or work site.



#### **Remote maintenance**

Compatible models: V Built-in Ethernet / Ethernet modules

GX Works3 can be connected via VPN, and programs can be read/written. Troubleshooting can be performed from a remote place without going to the site, which leads to a reduction in maintenance costs.



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## Web server function\*1 NEW

Compatible models: Z Built-in Ethernet / Ethernet modules

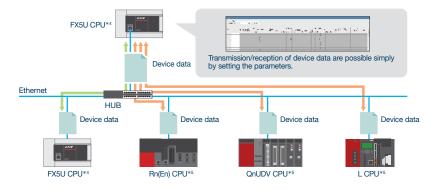
Accessing the Web server from a Web browser on a PC enables CPU module monitoring and diagnosis without any dedicated tools. User Web pages\*2 unique for each user can also be created.



## Simple CPU communication function\*3 NEW

Compatible models: Z Built-in Ethernet / Ethernet modules

Using a simple parameter setting with GX Works3, device data such as production data can be transferred without any program. Communication with existing systems using MELSEC iQ-R series, -Q series, and -L series devices can be easily performed.



## For CC-Link IE Field Network Basic, the number of connectable modules is increased to 16.

Compatible models: ✓ Built-in Ethernet / Ethernet modules

By increasing the number of connectable modules from 6 (with conventional versions) to 16, usability is improved. Because remote I/O stations connected by CC-Link IE Field Network Basic are not included in the total remote I/O points<sup>\*3</sup>, the user can expand modules without worrying about the number of remote I/O points.



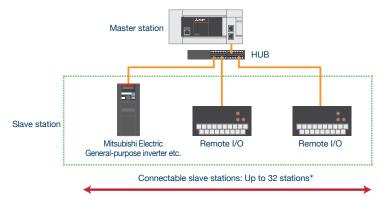
\*1: Supported by FX5U/FX5UC Ver. 1.060 or later, and GX Works3 Ver. 1.040S or later.

- \*2: Supported by FX5U/FX5UC Ver. 1.100 or later, product number 17X\*\*\*\* (product number 178\*\*\*\* for FX5UC-32MT/DS-TS and FX5UC-32MT/DSS-TS) or later, and GX Works3 Ver. 1.047Z or later.
- \*3: Supported by FX5U/FX5UC Ver. 1.110 or later, and product number 17X\*\*\*\* (product number 178\*\*\*\* for FX5UC-32MT/DS-TS and FX5UC-32MT/DSS-TS) or later, and GX Works3 Ver. 1.050C or later.
- \*4: Built-in Ethernet function
- \*5: Requires connecting device configuration.

#### **Function introduction**

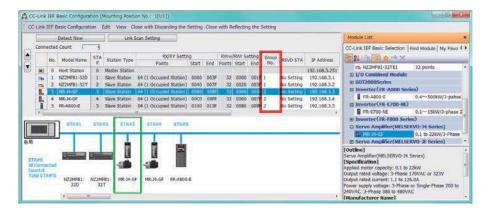


CC-Link IE Field Network Basic is a factory automation network that utilizes general-purpose Ethernet connections. Link devices are used to periodically transmit data (cyclic transmission) between the master station and slave stations. General-purpose Ethernet connections can be used to create a network that includes both the host system and production site equipment.



#### Capable of grouping of slave stations

Grouping stations according to the length of response processing time is possible. The cyclic transmission can be performed while suppressing influence by the difference in standard response times of each slave station.



#### **Socket communication function**

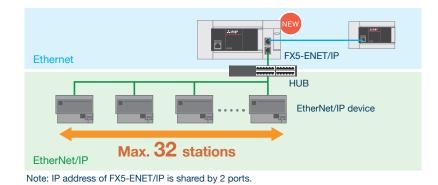


Data communication with Ethernet-connected devices is possible via TCP or UDP.





#### CIP communication protocol achieves a seamless communication with EtherNet/IP Network. EtherNet/IP and general purpose Ethernet can coexist.



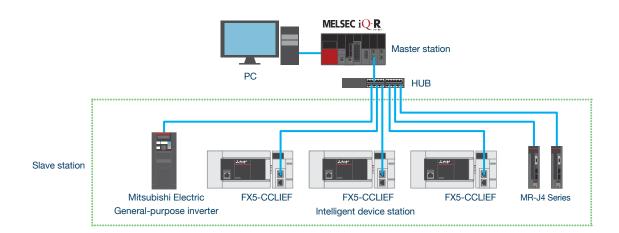
## **Connectable to CC-Link IE Field Networks**

#### CC-Link IE Field Network intelligent device module FX5-CCLIEF



#### **CC-Link IE Field Network**

CC-Link IE Field Network is a high-speed, high-capacity open field network that uses Ethernet connections. Using the FX5-CCLIEF makes it possible to connect an FX5 CPU module to the CC-Link IE Field Network as an intelligent device station. The network's flexible wiring methods—including ring, star, and line topologies—help reduce wiring costs and improve reliability.



#### **Function introduction**



## **Network/communication**

## **CC-Link communication**

#### CC-Link system master/intelligent device module FX5-CCL-MS

Enables building network systems compatible with CC-Link V2 at low cost. Since FX5-CCL-MS has both functions, the master station and intelligent device station, it can be used as either of them by switching with parameters.



#### Other station access function supported

Perform program write/read and device monitoring, etc. for another station's PLC within the same network using the GX Works3 connected to own station.

There's no need to connect GX Works3 and perform programming for each MELSEC iQ-F series module, so programming man-hours can be reduced.

#### Equipped with master station/ intelligent device station functions

The module is equipped with both the master station and intelligent device station functions, so it can be used for either type of station by changing the parameter.

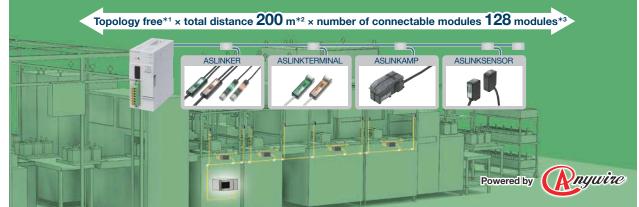


Intelligent device station

### Connection to AnyWireASLINK system

#### AnyWireASLINK system master module FX5-ASL-M

Can be connected to the AnyWireASLINK system made by AnyWire Co., Ltd. "Visualization" of sensors has been strengthened by collaboration with sensors and Mitsubishi Electric FA products. It is useful for preventive maintenance such as sensor disconnection detection.



\*1: There is no regulation about such as the specification of branching method and minimum distance between terminals.

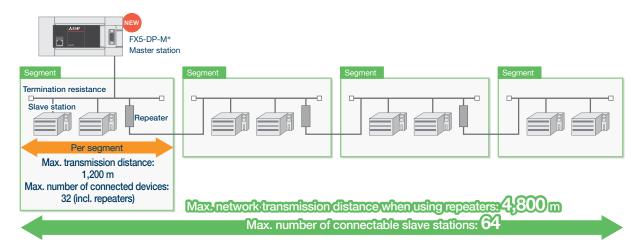
\*2: Total extension distance including branch line length

 $\star$ 3: The number varies depending on current consumption of each slave module

## **PROFIBUS-DP**

#### PROFIBUS-DP master module FX5-DP-M

PROFIBUS is an industrial fieldbus developed and maintained by the PROFIBUS & PROFINET International (PI). This protocol enables high-speed data transmission between field devices such as a remote I/O module or drive and a controller.



# Max. 12 Mbps high-speed, large-capacity communication

High-speed communication is possible at up to 12 Mbps. Up to 64 slave stations per FX5-DP-M for input/output connections. Data transmission is possible at up to 2048 bytes (with a max. of 244 bytes of I/O data per slave station).

# Obtain communication failure information from slave stations

Using the buffer memory makes it possible to obtain communications error information or extended communications error information generated by a slave station during I/O data transmission.



#### **Reading/writing I/O data**

Input/output data can be read/written between a CPU module device and the FX5-DP-M buffer memory.

NFW

To read or write I/O data, configure the refresh settings on the PROFIBUS Configuration Tool, or use MOV command or FROM/TO command programs.

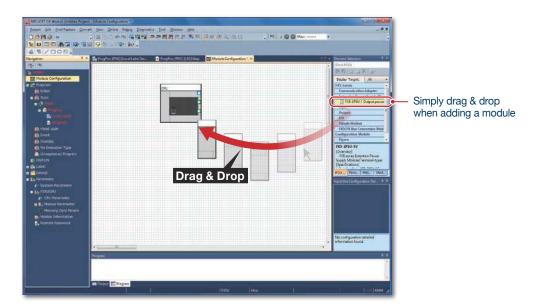


# Programming environment GXWorks3

GX Works3 is software that comprehensively supports the design and maintenance of sequence programs. Graphical intuitive operability, and easy programming by just "selecting". A diagnostic function that has a troubleshoot function realizes the reduction of engineering cost.

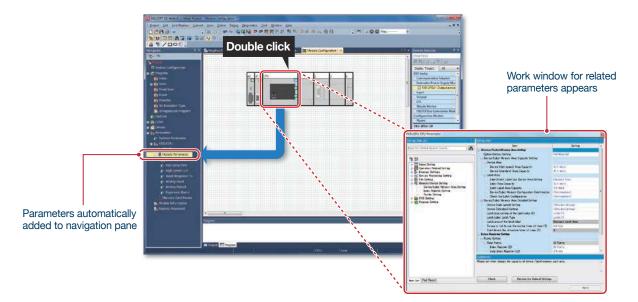
## System design with a convenient parts library

With GX Works3, designing a system is as easy as preparing the module configuration diagram by dragging and dropping selected parts.



## Auto-generation of module parameters

When preparing the module configuration diagram, simply double-click the module to automatically generate the module parameters. A window with an easy-to-use parameter settings screen opens, enabling module parameters to be modified as needed.





Ladder language edition

M M

YouTube MITSUBISHI ELECTRIC Factory Automation MELSEC iQ-F Series Quick Start Guide



You can see the basics of programming using GX Woks3 from the catalog on the left or reading the QR code. L(NA)08449ENG

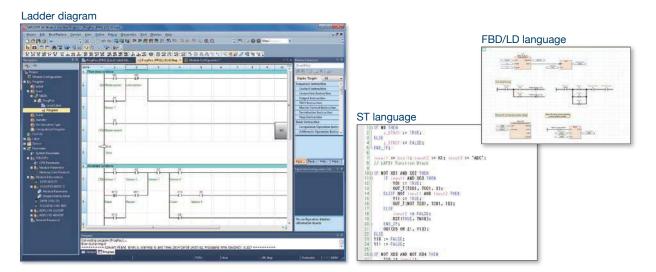
Use GX Works3 for programming with the MELSEC iQ-F Series.

FBD/LD language edition

| Software   | GX Works3          |
|------------|--------------------|
| Compatible | MELSEC iQ-R series |
| models     | MELSEC iQ-F series |

## Main programming languages supported

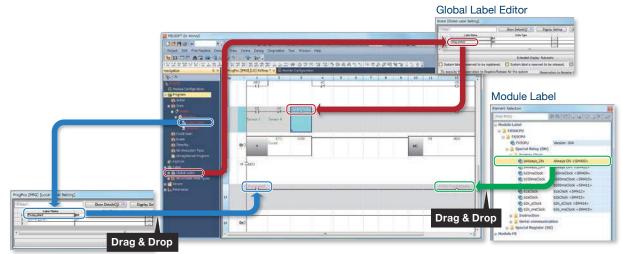
The main IEC languages are supported by GX Works3. Various different programming languages can be used within the same project simultaneously and can be viewed easily via the menu tab. The labels and devices used in each program can be shared across multiple platforms, with user defined function blocks supported.



## Reduce repetitive program tasks

With GX Works3, global labels, local labels, and module labels can be used as well as programming by devices.

Global labels can be shared between multiple programs or between other MELSOFT software. Local labels can be used in registered programs and FBs. Module labels have buffer memory information of various intelligent function modules. Therefore, programming can be done without being conscious of the buffer memory address.



Local Label Editor

### **Function introduction**



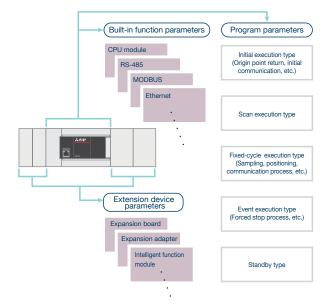
## **Programming environment**

#### Simple and convenient parameter settings

With MELSEC IQ-F series, various device settings that conventionally had to be programmed can be input in table format. Easily set the built-in functions as well as extension devices just by inputting values into the parameters. The program's execution trigger can also be set with the parameters.

#### Functions which can be set with parameters

- CPU parameter 
   Ethernet port 
   RS-485 port
- Input response time 
   Expansion board 
   Memory card 
   Security
- Expansion adapter and intelligent function module
- Program parameters



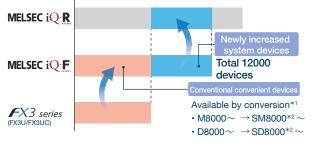
## **Flexible internal devices**

A variety of devices including new latch relays and link relays, and expanded timers and counters are available. The number of device points can be reassigned and used in the internal memory.

#### Providing the convenience of special devices

In addition to the conventional special devices, up to 12000 points of convenient system devices compatible with upper level devices are added.





#### Freely customize the latch range setting

The latch range can be set for each device, so the latch clear range can be selected during the clearing operation.

| Jtem S                    | Symbol | Device     |            | Latch      | Latch      |  |  |
|---------------------------|--------|------------|------------|------------|------------|--|--|
|                           | oympol | Points     | Range      | (1)        | (2)        |  |  |
| Input                     | x      | 1024       | @ to 1777  |            |            |  |  |
| Output                    | Y      | 1024       | 0 to 1777  |            |            |  |  |
| Internal Relay            | M      | 7690       | @ to 7679  | Setting    | No Setting |  |  |
| Link Relay                | 8      | 256        | 0-to FF    | No Setting | No Setting |  |  |
| Special Link Rel          | a SB   | 266        | @ to FF    |            |            |  |  |
| Annunciator               | F      | 128        | 0 to 127   | No Setting | No Setting |  |  |
| Step Relay                | S.     | 4096       | 0 to 4895  | Setting    |            |  |  |
| Timer                     | T      | 612        | 0 to 511   | No Setting | No Setting |  |  |
| Retentive Timer.          | ST     | 18         | 0 to 15    | Setting    | No Setting |  |  |
| Counter                   | 0      | 258        | 0 to 255   | Setting    | No Setting |  |  |
| Long Counter              | 10     | 84         | 0 to 68    | Setting    | No Setting |  |  |
| Data Register             | Ø      | 8000       | 8 to 7999  | Settine    | No Setting |  |  |
| Latch Relay               | 1      | 7600       | @ to 7679  |            |            |  |  |
| Area Capacity             |        |            | 12.0K Word |            | 11.0K W    |  |  |
| Total Device 1            |        | 11.1K Word |            | 9.6K W     |            |  |  |
| Total Word Device 10.2K W |        | 10.2K Word |            | 0.1K W     |            |  |  |
| Total Bit Device          |        | 15.7K-Bit  |            | 25 IK      |            |  |  |

#### Handy timer and counter settings

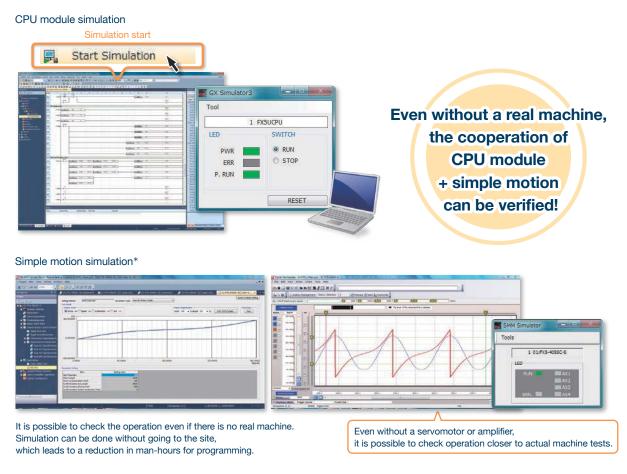
The timer and counter properties are determined by data type and how instruction is written, so programs can be created regardless of the device number.

| Timers   |                 |   | Retentive timers |                  |  |
|----------|-----------------|---|------------------|------------------|--|
| OUT TO   | 100 ms timer    |   | OUT ST0          | 100 ms retentive |  |
| OUTH TO  | 10 ms timer     | ] |                  | timer            |  |
| OUTHS TO | 1 ms timer      | 1 | OUTH STO         | 10 ms retentive  |  |
|          |                 | 1 |                  | timer            |  |
| Co       | Counters        |   |                  | 1 ms retentive   |  |
| OUT C0   | 16 bits counter | ] |                  | timer            |  |
| OUT LC0  | 32 bits counter | 1 |                  |                  |  |

\*1: When the FX3U/FX3UC project created with GX Works2 is used for the MELSEC iQ-F series, the device will be converted automatically.
 \*2: Some device names and device numbers may differ.

## **Driving simulation**

With GX Simulator3, programs can be debugged with a virtual PLC on the computer. It is convenient to be able to check before operating on the real machine.



## Integrated simple motion setup tool

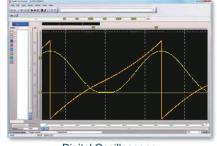
GX Works3 is equipped with a simple motion setup tool that makes it easy to change simple motion module settings such as module parameters, positioning data and servo parameters. Also, the servo adjustment is simplified using it.



System Configuration



Synchronized Control Parameter



Digital Oscilloscope

## **Function introduction**



## **Programming environment**

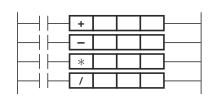
#### Dramatically more dedicated instructions

Compared with the FX3 series, a significant number of dedicated instructions have been added.



#### Intuitive and easy-to-understand arithmetic operations

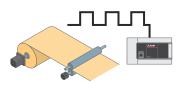
Symbols can be input in the arithmetic operations making it easy and intuitive to describe programs.



## High-performance built-in high-speed counter function

Parameter setting enables input/measurement in three modes. It is possible to set 32 high-speed comparison tables<sup>\*2</sup> and 128 multipoint output high-speed comparison tables. In addition, the DHCMOV instruction can read the latest value to the special register.

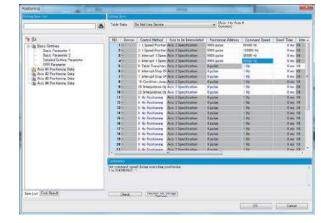
- Normal mode
- Pulse density measurement mode
- Rotation speed
- measurement mode



#### **Reinforced built-in positioning function**

Positioning can be easily performed with table operation instructions. Even advanced positioning like simple linear interpolation is possible with the multi-table operation (DRVTBL) instruction and multi-axis table operation (DRVMUL) instruction.

## Diverse table operation settings for multi-speed and interrupt positioning, etc.



\*1: When using FX5U/FX5UC Ver. 1.110.

\*2: Supported by FX5U/FX5UC Ver. 1.040 or later and product number 158\*\*\*\* or later.



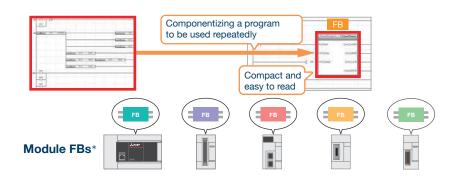
For details, refer to the catalog on the right. L(NA)08475ENG

## **MELSOFT Library useful for reducing man-hours**

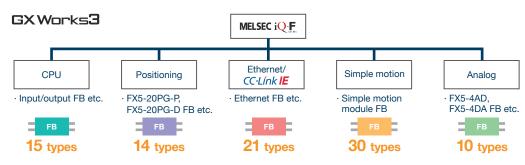
Since module FBs\* (FBs for our equipment) are all shipped with GX Works3, many libraries can be used for programming right after installation.

#### Module FBs\* to control each module are prepared.

"Module FB\*" is a componentized program that controls each module. Using the module FBs\* eliminates the need for programming the processing of each module and reduces programming man-hours.



Module FBs\* are included in GX Works3 in advance.

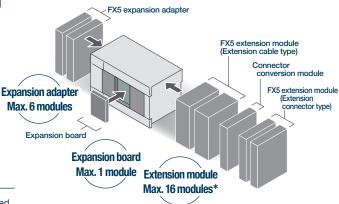


# **System Configuration**



# Flagship model equipped with advanced built-in functions and diverse expandability

FX5U is equipped with analog functions, communication and high-speed I/O, and can easily be expanded with expansion boards and adapters. The high-speed system bus communication brings out the maximum performance of extension devices equipped with intelligent functions.



\*: Up to 12 modules can be used by directly connecting a CPU module. Up to 16 modules can be used by connecting a powered I/O module or an extension power supply module. Extension power supply modules and connector conversion modules are not included in the number of connected modules.

FX5U CPU module

#### FX5 expansion adapters

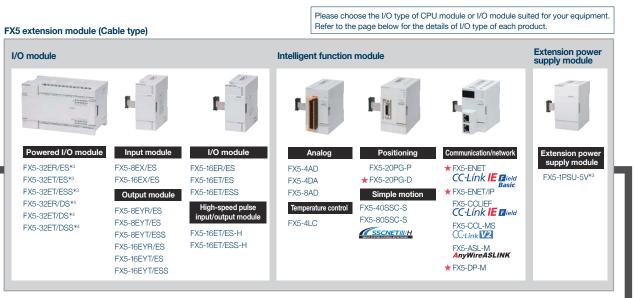


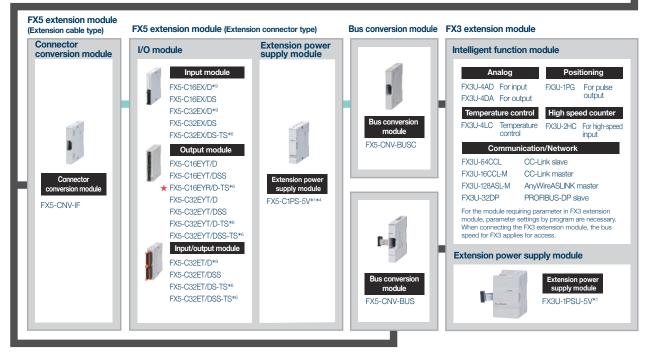
#### **Outline Specifications**

|                   | Item                                   | Outline Specifications   |  |  |  |
|-------------------|--|--|--|--|--|
|                   | Rated voltage                          | AC power supply type: 100 to 240 V AC, 50/60 Hz<br>DC power supply type: 24 V DC   |  |  |  |
|                   | Power consumption*1                    | AC power supply type: 30 W (32M), 40 W (64M), 45 W (80M)<br>DC power supply type: 30 W (32M), 40 W (64M), 45 W (80M)   |  |  |  |
| Power supply      | Rush current                           | AC power supply type: 32M: max. 25 A for 5 ms or less/100 V AC, max. 50 A for 5 ms or less/200 V AC<br>64M/80M: max. 30 A for 5 ms or less/100 V AC, max. 60 A for 5 ms or less/200 V AC<br>DC power supply type: 32M: max. 50 A for 0.5 ms or less/24 V DC<br>64M/80M: max. 65 A for 2.0 ms or less/24 V DC           |  |  |  |
|                   | 5 V DC internal power supply capacity  | AC power supply type: 900 mA (32M), 1100 mA (64M/80M)<br>DC power supply type: 900 mA (775 mA)*2 (32M), 1100 mA (975 mA*2) (64M/80M)   |  |  |  |
|                   | 24 V DC service power supply capacity  | AC power supply type: 400 mA [300 mA* <sup>3</sup> ] (32M), 600 mA [300 mA* <sup>3</sup> ] (64M/80M)<br>When an external power supply is used for the input circuit of the CPU module: 480 mA [380 mA* <sup>3</sup> ] (32M), 740mA [440 mA* <sup>3</sup> ] (64M),<br>770 mA [470 mA* <sup>3</sup> ] (80M)              |  |  |  |
|                   | 24 V DC internal power supply capacity | DC power supply type: 480 mA (360 mA)* <sup>2</sup> (32M), 740 mA (530 mA* <sup>2</sup> (64M), 770 mA (560 mA* <sup>2</sup> ) (80M)  |  |  |  |
|                   | Input specifications                   | 5.3 mA/24 V DC (X020 and later: 4.0 mA/24 V DC)  |  |  |  |
| Input/output      | Output specifications                  | Relay output type: 2 A/1 point, 8 A or less/4 points common, 8 A or less/8 points common, 30 V DC or less, 240 V AC or less (250 V AC or less in case of noncompliance with CE, UL/CLU Standards)<br>Transistor output type: 0.5 A/1 point, 0.8 A or less/4 points common, 1.6 A or less/8 points common, 5 to 30 V DC |  |  |  |
|                   | Input/output extension                 | Extension devices for FX5 can be connected: when adding an extension connector type, the connector conversion module (FX5-CNV-IF) is required.   |  |  |  |
| Built-in commu    | inication port                         | Ethernet (100BASE-TX/10BASE-T), RS-485 1 ch each   |  |  |  |
| Built-in memory   | y card slot                            | 1 slot for SD memory card  |  |  |  |
| Built-in analog i | input/output                           | Input 2 ch, output 1 ch  |  |  |  |

\*1: The values show the state where the service power of 24 V DC is consumed to the maximum level in case that its configuration has the max. no. of connections provided to CPU module. (Including the current in the input circuit) \*2: The values in the parentheses () indicate the power supply capacity to be resulted when the power supply voltage falls in the range from 16.8 to 19.2 V DC.

\*3: The values in the brackets [] will result when the ambient temperature is less than 0°C during operations





\*1: When adding the extension module, it is necessary to connect it to the front stage of extension module in case of a shortage of

\*1: When adding the extension module, it is necessary to connect it to the front stage of extension module in case of a shortage of internal power supply in CPU module.
\*2: Attach when connecting an extension cable type module to a distant location or when making two-tier connections. The connector conversion adapter (FXG-CNV-BC) is required when connected with an input/output module extension cable type), high-speed pulse input/output module, or an intelligent function module. When using also the bus conversion module in the same system, connect the FXG extension power supply module or the powered I/O module right after the extended extension cable.
\*3: Can be connected only to the AC power type system.

\*4: Can be connected only to the DC power type system.
\*5: There are restrictions on the number of extension devices and the connection order of FX5-4AD-TC-ADP. For details, refer to the manual.
\*6: Spring demp terminal block type.
\*7: For FX5-20PG-P and FX5-20PG-D.

\*8: For FX3U-2HC.
 \*9: FX2NC-100BPCB is required separately when adding to FX5U.

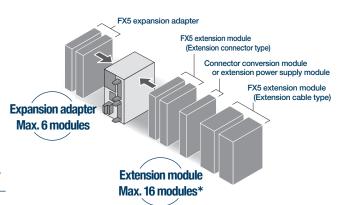
# **System Configuration**



Contributing to miniaturization of equipment by condensing various functions on a compact body

The extension module compatible with FX5UC is compact and easy-touse, and helps to downsize your system.

Easily connect to the FX5 and FX3 extension modules with the variety of conversion modules available.



\*: Up to 12 modules can be used by directly connecting a CPU module. Up to 16 modules can be used by connecting a powered I/O module or an extension power supply module. Extension power supply modules and connector conversion modules are not included in the number of connected modules.



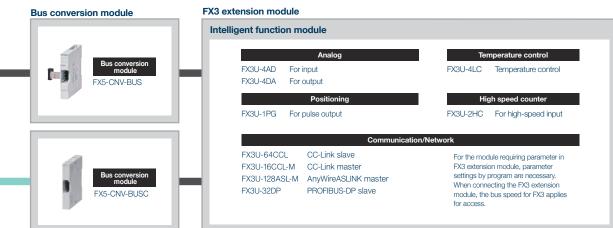
#### **Outline Specifications**

| Item                      |                              | Outline Specifications  |  |  |  |  |
|---------------------------|------------------------------|---|--|--|--|--|
|                           | Rated supply voltage         | 24 V DC   |  |  |  |  |
|                           | Power consumption*1          | 32M: 5 W/24 V DC (30 W/24 V DC +20%, -15%) 64M: 8 W/24 V DC (33 W/24 V DC +20%, -15%) 96M: 11 W/24 V DC (36 W/24 V DC +20%, -15%)                           |  |  |  |  |
| Device events             | Bush current                 | 32M: Max. 35 A 0.5 ms or less/24 V DC   |  |  |  |  |
| Power supply              | Rush current                 | 64M/96M: Max. 40 A 0.5 ms or less/24 V DC   |  |  |  |  |
|                           | 5 V DC power supply capacity | 720 mA  |  |  |  |  |
| 24 V DC power supply capa |                              | 500 mA  |  |  |  |  |
|                           | Input specifications         | 5.3 mA/24 V DC (X020 and later: 4.0 mA/24 V DC)   |  |  |  |  |
|                           |                              | Relay output type: 2 A/1 point or less, 4 A or less/8 points common*2 30 V DC or less, 240 V AC or less (250 V AC or less in case of noncompliance with CE, |  |  |  |  |
| Input/output              | Output specifications        | UL/cUL Standards)   |  |  |  |  |
| input/output              |                              | Transistor output type: Y000 to Y003 0.3 A/1 point, Y004 and later 0.1 A/1 point, 0.8 A/8 points common*3 5 to 30 V DC                                      |  |  |  |  |
|                           | Input/output extension       | Extension device for FX5 can be connected (extension power supply module (FX5-C1PS-5V) or connector conversion module (FX5-CNV-IFC) is required             |  |  |  |  |
|                           | Input/output extension       | when connecting an extension cable type)  |  |  |  |  |
| Built-in commu            | nication port                | Ethernet (100BASE-TX/10BASE-T), RS-485 1 ch each  |  |  |  |  |
| Built-in memory           | / card slot                  | 1 slot for SD memory card   |  |  |  |  |
| ,                         |                              |   |  |  |  |  |

\*1: The values show the state where the power of 24 V DC is consumed to the maximum level in case that its configuration has the max. no. of connections provided to CPU module. (Including the current in an input circuit) \*2: 8 A or less when two common terminals are connected to the external part

\*3: 1.6 A or less when two common terminals are connected to the external part.





\*1: When adding the extension module, it is necessary to connect it to the front stage of extension module in case of a shortage of internal power supply in CPU module. \*2: Next-stage extension connector of an extension power supply module can be used only for either connector connection or cable connection. In case of connector connection, an

extension connector type module can be connected.

\*3: Attach when connecting an extension cable type module to a distant location or when making two-tier connections. The connector conversion adapter (FX5-CNV-BC) is required when connected with an input/output module (extension cable type) or an intelligent function module. When using also the bus conversion module in the same system, connect the powered I/O module right after the extended extension cable.

\*4: There are restrictions on the number of extension devices and the connection order of FX5-4AD-TC-ADP. For details, refer to the manual,

\*5: Spring clamp terminal block type.
\*6: For FX5-20PG-P and FX5-20PG-D.

\*7: There are some exception models. For details, refer to the manual.

\*8: For FX3U-2HC

\*9: FX2NC-100BPCB is required separately when adding to FX5UC-□MT/DS□-TS.

# **Performance Specifications**



#### ■ FX5U/FX5UC CPU Module Performance Specifications

|                                  | Item                                 | Specifications   |
|----------------------------------|--------------------------------------|--|
| Control system                   |                                      | Stored-program repetitive operation  |
| Input/output control system      |                                      | Refresh system (Direct access input/output allowed by specification of direct access input/output [DX, DY])  |
|                                  | Programming language                 | Ladder diagram (LD), structured text (ST), function block diagram/ladder language (FBD/LD)                   |
|                                  | Programming expansion function       | Function block (FB), function (FUN), label programming (local/global)  |
|                                  | Constant scan                        | 0.2 to 2000 ms (can be set in 0.1 ms increments)   |
| Programming specifications       | Fixed cycle interrupt                | 1 to 60000 ms (can be set in 1 ms increments)  |
|                                  | Timer performance specifications     | 100 ms, 10 ms, 1 ms  |
|                                  | No. of program executions            | 32   |
|                                  | No. of FB files                      | 16 (Up to 15 for user)   |
| Operation encolfications         | Execution type                       | Standby type, initial execution type, scan execution type, fixed-cycle execution type, event execution type  |
| Operation specifications         | Interrupt type                       | Internal timer interrupt, input interruption, high-speed comparison match interrupt, interrupt from module*1 |
| Instruction pressoning time      | LD X0                                | 34 ns*2  |
| Instruction processing time      | MOV D0 D1                            | 34 ns*2  |
|                                  | Program capacity                     | 64 k/128 k steps (128 kbytes/256 kbytes, flash memory)   |
| Manager and a star               | SD memory card                       | Memory card capacity (SD/SDHC memory card: Max. 16 Gbytes)   |
| Memory capacity                  | Device/label memory                  | 120 kbytes   |
|                                  | Data memory/standard ROM             | 5 Mbytes   |
| Flash memory (Flash ROM) write   | e count                              | Max. 20000 times   |
|                                  | Device/label memory                  | 1  |
|                                  | Data memory                          |  |
| File storage capacity            | P: No. of program files              | P: 32, FB: 16  |
| 0 , ,                            | FB: No. of FB files                  |  |
|                                  | SD memory card                       | 2 Gbytes: 511*4, 4 G/8 G/16 Gbytes: 65534*4  |
|                                  | Display data                         | Year, month, day, hour, minute, second, day of week (leap year automatic detection)                          |
| Clock function                   | Precision                            | Monthly difference: ±45 sec at 25°C (77°F) (typical value)   |
|                                  | (1) No. of input/output points       | 256 points or less/384 points or less*3  |
| No. of input/output points       | (2) No. of remote I/O points         | 384 points or less/512 points or less*3  |
|                                  | Total No. of points of (1) and (2)   | 512 points or less   |
| Power failure retention          | Retention method                     | Large-capacity capacitor   |
| (Clock data*5)                   | Retention time                       | 10 days (Ambient temperature: 25°C (77°F))   |
| Power failure retention (Device) | Capacity for power failure retention | 12 K words maximum <sup>*6</sup>   |
|                                  |                                      |  |

\*1: Interrupt from the intelligent function module and high-speed pulse input/output module.

\*2: When the program capacity is 64 k steps.

\*3: Supported by FX5U/FX5UC Ver. 1.100 or later and by GX Works3 Ver. 1.047Z or later.

\*4: The value listed above indicates the number of files stored in the root folder.

\*5: Clock data is retained using the power accumulated in a large-capacity capacitor incorporated into the PLC. When voltage of the large-capacity capacitor drops, clock data is no longer accurately retained. The retention period of a fully charged capacitor (electricity is conducted across the PLC for at least 30 minutes) is 10 days (ambient temperature: 25°C (77°F)). How long the capacitor can hold the data depends on the operating ambient temperature. When the operating ambient temperature is high, the holding period is short.

\*6:All devices in the (high-speed) device area can be held against power failure. Devices in the (standard) device area can be held also when the optional battery is mounted.

#### Number of device points

| Item                         |                              |                         | Base |  | Max. number of points  |  |
|------------------------------|------------------------------|-------------------------|------|--|--|--|
|                              | Input relay (X)              |                         | 8    | 1024 points or less                            | The total number of X and Y assigned to input/output points is up to 256 points/ |  |
|                              | Output relay (Y)             |                         | 8    | 1024 points or less 384 points*1.              |  |  |
|                              | Internal relay (M)           |                         | 10   | 32768 points (can be char                      | nged with parameter)*2   |  |
|                              | Latch relay (L)              |                         | 10   | 32768 points (can be char                      | nged with parameter)*2   |  |
|                              | Link relay (B)               |                         | 16   | 32768 points (can be char                      | nged with parameter)*2   |  |
|                              | Annunciator (F)              |                         | 10   | 32768 points (can be char                      | nged with parameter)*2   |  |
|                              | Link special relay (SB)      | )                       | 16   | 32768 points (can be char                      | nged with parameter)*2   |  |
| No. of user device points    | Step relay (S)               |                         | 10   | 4096 points (fixed)                            |  |  |
| No. of user device points    | Timer system                 | Timer (T)               | 10   | 1024 points (can be chang                      | ged with parameter)*2  |  |
|                              | Accumulation timer<br>system | Accumulation timer (ST) | 10   | 1024 points (can be chang                      | ged with parameter)*2  |  |
|                              | Counter system               | Counter (C)             | 10   | 1024 points (can be chang                      | ged with parameter)*2  |  |
|                              | Counter system               | Long counter (LC)       | 10   | 1024 points (can be chang                      | ged with parameter)*2  |  |
|                              | Data register (D)            |                         | 10   | 8000 points (can be chang                      | ged with parameter)*2  |  |
|                              | Link register (W)            |                         | 16   | 32768 points (can be char                      |  |  |
|                              | Link special register (      | SW)                     | 16   | 32768 points (can be changed with parameter)*2 |  |  |
| No. of system device points  | Special relay (SM)           |                         | 10   | 10000 points (fixed)                           |  |  |
| No. of system device points  | Special register (SD)        |                         | 10   | 12000 points (fixed)                           |  |  |
| Module access device         | Intelligent function mo      | odule device            | 10   | 65536 points (designated by U□\G□)             |  |  |
| No. of index register points | Index register (Z)*3         |                         | 10   | 24 points                                      |  |  |
| No. of index register points | Long index register (L       | Z)*3                    | 10   | 12 points                                      |  |  |
| No. of file register points  | File register (R)            |                         | 10   | 32768 points (can be changed with parameter)*2 |  |  |
| Two. of the register points  | Extended file register       | (ER)                    | 10   | 32768 points (are stored in                    | n SD memory card)  |  |
| No. of nesting points        | Nesting (N)                  |                         | 10   | 15 points (fixed)                              |  |  |
| No. of pointer points        | Pointer (P)                  |                         | 10   | 4096 points                                    |  |  |
|                              | Interrupt pointer (I)        |                         | 10   | 178 points (fixed)                             |  |  |
|                              |                              | Signed                  | _    | 16 bits: -32768 to +32767                      | · · · · · · · · · · · · · · · · · · ·  |  |
|                              | Decimal constant (K)         | Olgrica                 |      | 32 bits: -2147483648 to +                      | 2147483647   |  |
|                              | Decimal constant (rt)        | Unsigned                |      | 16 bits: 0 to 65535,                           |  |  |
| Others                       |                              | Onsigned                |      | 32 bits: 0 to 4294967295                       |  |  |
| Outors                       | Hexadecimal constar          | st (Ц)                  | _    | 16 bits: 0 to FFFF,                            |  |  |
|                              | Tiexauecimai constar         |                         | _    | 32 bits: 0 to FFFFFFFF                         |  |  |
|                              | Real constant (E)            | Single precision        | -    |  | 17549435-38, 0, E1.17549435-38 to E3.40282347+38                                 |  |
|                              | Character string             |                         | -    | Shift-JIS code max. 255 si                     | ingle-byte characters (256 including NULL)                                       |  |

\*1: Supported by FX5U/FX5UC Ver. 1.100 or later and by GX Works3 Ver. 1.047Z or later.

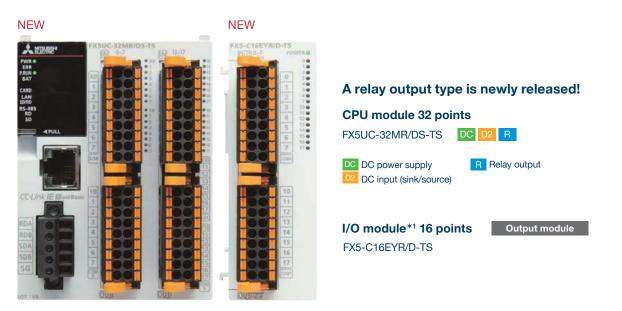
\*2: Can be changed with parameters within the capacity range of the CPU built-in memory.

\*3: Total of the index register (Z) and long index register (LZ) is maximum 24 words.

# **New products**

# New product information

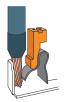
Introducing new relay output spring clamp terminal block type FX5UC CPU modules and I/O modules. They can save the labor of processing electric wires, and you can wire quickly and easily.



# What is a spring clamp terminal block type?

Spring clamp terminals hold wires in place by the force of internal springs. Constant force holds wires in place, preventing wires from falling out due to vibration.

<Internal construction> Securely fixed by elastic force!





There is no need for crimp terminals or crimp tools! Wiring is possible without extra time or cost!



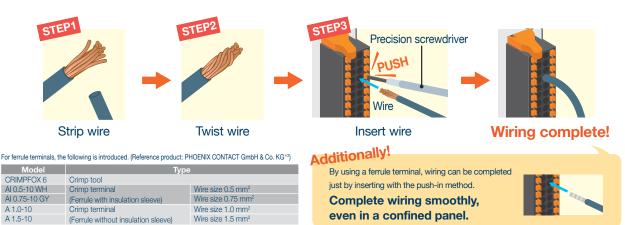
Attaching crimp terminals to cables one by one is tedious! No need for crimp terminals or crimp tools! Just prepare the cables!

No external terminal is needed! Easily detachable & securely fixed by a lock lever!



With detachable terminals, the change of wiring is not needed even when replacing the modules!

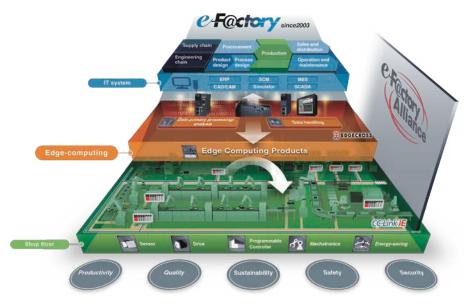
## With spring clamp terminals block type, wiring is complete in 3 steps!



\*1: When connecting to FX5U CPU module, FX5-CNV-IF is required.

\*2: If the product other than the reference product is used, the wire ferrule cannot be pulled out. Sufficiently confirm that the wire ferrule can be pulled out before use.

# FUTURE MANUFACTURING



The Future of Manufacturing as envisioned by Mitsubishi Electric, e-F@ctory: "Manufacturing" that evolves in response to environmental changes in an IoT enabled world.

Established In 2003, e-F@ctory created a Kaizen<sup>#1</sup> automation methodology to help optimize and manage the increasingly complex business of "manufacturing".

Continuously evolving itself, it also utilizes the expanded reach of IT, which has brought "cyber world" benefits of analysis, simulation and virtual engineering, and yet has also placed greater demands on the sensing, collection and communication. The continued success of e-F@ctory comes from understanding that each manufacturer has individual needs and investment plans but must still deliver; "Reduced management costs" (TCO); production flexibility to make a multitude of product in varying quantities; continuously enhanced quality. In short e-F@ctory's goal is to deliver operational performance that is "a step ahead of the times", while enabling manufacturing to evolve in

response to its environment. To do this it is supported by three key elements:

- The e-F@ctory Alliance Partners; who bring a wide range of software, devices, and system integration skills that enable the creation of the optimal e-F@ctory architecture.
- "physical" world for increased data sensing, collection and communication. The continued success of e-F@ctory comes from understanding that each manufacturer has individual needs and investment plans but must still deliver; "Reduced management costs" • Advanced communication; utilizing open network technology like CC-Link IE, and communication middleware such as OPC, to open the door to device data, including legacy systems, while supporting high speed extraction.
  - Platform thinking; to reduce the number of complex interfaces making it easier to bring together Robotics, Motion, Open programming languages (C language), PACs etc. strengthening the field of control,

yet operating on industrial strength hardware.





Kaizen#1 = continuous improvement TCO = Total Cost of Ownership

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# Selecting the FX5U model

# ◇ Product configuration



\* : For CC-Link and AnyWireASLINK

| Туре                                       | Details   | Connection details, model selection  |
|--|---|--|
| 1 CPU module                               | PLC with built-in CPU, power supply,<br>input/output and program memory.  | Various extension devices can be connected.  |
| 2 4 I/O module<br>(extension cable type)   | Product for extending I/O of extension cable type.<br>Some products are powered.  | Input/output can be extended to up to 256 points/384 points.*1<br>Up to 16 extension modules can be connected. (Extension power supply modules and<br>connector conversion modules are not included in the number of connected modules.)<br>Up to 4 high-speed pulse I/O modules can be connected.<br>For details, refer to "Rules for System Configuration" on p. 49. |
| 3 FX5 extension power supply module        | Module for extending power supply if CPU<br>module's internal power supply is insufficient.<br>Extension cable is enclosed. | Power can be supplied to I/O module, intelligent function module, and bus conversion module.<br>Up to 2 modules can be connected.  |
| 5 FX5 intelligent function module          | Module with functions other than input/output.  | Up to 16 extension modules including the I/O module can be connected (Extension power supply modules and connector conversion modules are not included in the number of connected modules.)  |
| 6 Connector conversion module              | Module for connecting FX5 Series (extension connector type) extension module  | An extension module (extension connector type) for FX5 can be connected.   |
| 7 I/O module<br>(Extension connector type) | Product for adding extension connector type inputs/outputs.   | The maximum number of points for input/output extension is 256 points/384 points*1.<br>Up to 16 extension modules can be connected. (Extension power supply modules and<br>connector conversion modules are not included in the number of connected modules.)<br>Using this type of I/O module requires the connector conversion module.                               |
| 8 Bus conversion module                    | Conversion module for connecting FX3 Series extension module.   | FX3 extension module can be connected only to the right side of the bus conversion module.<br>When using FX5-CNV-BUSC, a connector conversion module is required.  |
| 9 FX5 expansion board                      | Board connected to front of CPU module to expand functions.   | Up to 1 module can be connected to the front of the CPU module. (Expansion adapter can also be used.)  |
| 10 FX5 expansion adapter                   | Adapter connected to left side of CPU module to expand functions.   | Up to 6 modules can be connected to the left side of the CPU module.   |
| 11 FX3 extension power supply module       | Module for extending power supply if CPU<br>module's internal power supply is insufficient.<br>Extension cable is enclosed. | Up to 2 modules can be connected.<br>The bus conversion module is required for use.  |
| 12 FX3 intelligent function module         | Module with functions other than input/output.  | When using the FX3 extension power supply module, up to 8 modules* <sup>2</sup> can be used.<br>When not using the FX3 extension power supply module, up to 6 modules* <sup>2</sup> can be used.<br>The bus conversion module is required for use.   |

\*1: Supported by FX5U CPU modules Ver. 1.100 or later and by GX Works3 Ver. 1.047Z or later. \*2: Excluding some models

# 1 -1) CPU module (AC power supply, DC input type)

|               |                                      | Number of                        |                        | supply capacity                            |  | No. of          | No. of           |
|---------------|--------------------------------------|----------------------------------|------------------------|--|--|-----------------|------------------|
| Model         | Function                             | occupied input/<br>output points | 5 V DC<br>power supply | 24 V DC<br>service power supply            | I/O type                                   | input<br>points | output<br>points |
| FX5U-32MR/ES  |                                      |                                  |                        |  | DC input (sink/source)/relay output        |                 |                  |
| FX5U-32MT/ES  |                                      | 32 points                        | 900 mA                 | 400 mA (480 mA*1)<br>[300 mA (380 mA*1)]*2 | DC input (sink/source)/transistor (sink)   |                 | 16<br>points     |
| FX5U-32MT/ESS |                                      |                                  |                        |  | DC input (sink/source)/transistor (source) |                 |                  |
| FX5U-64MR/ES  |                                      | 64 points                        | 1100 mA                |  | DC input (sink/source)/relay output        |                 | 32<br>points     |
| FX5U-64MT/ES  | CPU module<br>(24 V DC service power |                                  |                        | 600 mA (740 mA*1)<br>[300 mA (440 mA*1)]*2 | DC input (sink/source)/transistor (sink)   | 32<br>points    |                  |
| FX5U-64MT/ESS | built-in)                            |                                  |                        |  | DC input (sink/source)/transistor (source) | points          |                  |
| FX5U-80MR/ES  |                                      |                                  |                        |  | DC input (sink/source)/relay output        |                 | 40<br>points     |
| FX5U-80MT/ES  |                                      | 80 points                        | 1100 mA                | 600 mA (770 mA*1)<br>[300 mA (470 mA*1)]*2 | DC input (sink/source)/transistor (sink)   | 40<br>  points  |                  |
| FX5U-80MT/ESS |                                      |                                  |                        | [  | DC input (sink/source)/transistor (source) |                 |                  |

\*1: Power supply capacity when an external power supply is used for input circuits \*2: Value inside [ ] indicates the power supply capacity when the CPU module is used at the operating ambient temperature of less than 0°C.

#### 1 -2) CPU module (DC power supply/DC input type)

|               |            |                                  |  | /                   |   |                  |                  |
|---------------|------------|----------------------------------|--|---------------------|---|------------------|------------------|
|               |            | Number of                        | Power supply capacity                    |                     |   | No. of           | No. of           |
| Model         | Function   | occupied input/<br>output points | 5 V DC 24 V DC power supply power supply |                     | I/O type  | input<br>points  | output<br>points |
| FX5U-32MR/DS  |            |                                  |  |                     | DC input (sink/source)/relay output               |                  |                  |
| FX5U-32MT/DS  |            | 32 points                        | 900 mA<br>[775 mA]*                      | 480 mA<br>[360 mA]* | DC input (sink/source)/transistor output (sink)   | ] 16<br>  points | 16<br>points     |
| FX5U-32MT/DSS |            |                                  | []                                       |                     | DC input (sink/source)/transistor output (source) |                  |                  |
| FX5U-64MR/DS  |            |                                  |  |                     | DC input (sink/source)/relay output               |                  |                  |
| FX5U-64MT/DS  | CPU module | 1.6/1 nointe                     | 1100 mA<br>[975 mA]                      | 740 mA<br>[530 mA]* | DC input (sink/source)/transistor output (sink)   | 32<br>points     | 32<br>points     |
| FX5U-64MT/DSS |            |                                  |  |                     | DC input (sink/source)/transistor output (source) |                  | pointo           |
| FX5U-80MR/DS  |            |                                  |  | 770 mA<br>[560 mA]* | DC input (sink/source)/relay output               |                  |                  |
| FX5U-80MT/DS  |            | 80 points                        | 80 points [975 mA]                       |                     | DC input (sink/source)/transistor output (sink)   | 40<br>points     | 40<br>points     |
| FX5U-80MT/DSS |            |                                  |  | 1000                | DC input (sink/source)/transistor output (source) |                  | 20110            |

\*: Value inside [] indicates the power supply capacity when the supply voltage is 16.8 to 19.2 V DC.

#### **2** -1) I/O module (AC power supply/DC input type) (extension cable type)

| Model          | Function               | Number of<br>occupied input/<br>output points | Power supply capacity5 V DC24 V DC servicepower supplypower supply |                      | I/O type                                   | No. of<br>input<br>points | No. of<br>output<br>points |
|----------------|------------------------|---|--|----------------------|--|---------------------------|----------------------------|
| FX5-32ER/ES*1  | I/O module             | 32 points                                     | 965 mA   |                      | DC input (sink/source)/relay output        |                           |                            |
| FX5-32ET/ES*1  | (24 V DC service power |   |  | 250 mA<br>(310 mA*2) | DC input (sink/source)/transistor (sink)   | 16<br>  points            | 16<br>points               |
| FX5-32ET/ESS*1 | built-in)              |   |  | (0.0                 | DC input (sink/source)/transistor (source) |                           | pointo                     |

\*1: Can be connected only to the AC power type system

\*2: Power supply capacity when an external power supply is used for input circuits

#### 2 -2) I/O module (DC power supply/DC input type) (extension cable type)

| Model         | Function                | Number of<br>occupied input/<br>output points | Power supply capacity5 V DC24 V DCpower supplypower supply |        | I/O type  | No. of<br>input<br>points           | No. of<br>output<br>points |  |
|---------------|-------------------------|---|--|--------|---|-------------------------------------|----------------------------|--|
| FX5-32ER/DS*  |                         |   | DC input (sink/source)/relay output                        |        |   | DC input (sink/source)/relay output |                            |  |
| FX5-32ET/DS*  | FX5-32ET/DS* I/O module |   | 965 mA   | 310 mA | C input (sink/source)/transistor output (sink)    |                                     | 16<br>points               |  |
| FX5-32ET/DSS* |                         |   |  |        | DC input (sink/source)/transistor output (source) |                                     | pointo                     |  |

\*: Can be connected only to the DC power type system

#### **3** FX5 extension power supply module

|               |                        | Number of       | Power supply capacity |              |  |
|---------------|------------------------|-----------------|-----------------------|--------------|--|
| Model         | Function               | occupied input/ | 5 V DC                | 24 V DC      |  |
|               |                        | output points   | power supply          | power supply |  |
| FX5-1PSU-5V*1 | Extension power supply | —               | 1200 mA*3             | 300 mA*3     |  |
| FX5-C1PS-5V*2 | Extension power supply | -               | 1200 mA*3             | 625 mA*3     |  |

\*1: Can be connected only to the AC power type system
 \*2: Can be connected only to the DC power type system
 \*3: Derating occurs when the ambient temperature exceeds 40°C. For details, refer to manuals of each product.

#### 4 I/O module (extension cable type)

|                  |   | Number of occupied  | Current consumption    |                         |  |
|------------------|---|---------------------|------------------------|-------------------------|--|
| Model            | I/O type  | input/output points | 5 V DC<br>power supply | 24 V DC<br>power supply |  |
| FX5-8EX/ES       | DC input (sink/source)                            | 8 points            | 75 mA                  | 50 mA (0 mA*2)          |  |
| FX5-16EX/ES      | DC input (sink/source)                            | 16 points           | 100 mA                 | 85 mA (0 mA*2)          |  |
| FX5-8EYR/ES      | Relay output                                      |                     |                        |                         |  |
| FX5-8EYT/ES      | Transistor output (sink)                          | 8 points            | 75 mA                  | 75 mA                   |  |
| FX5-8EYT/ESS     | Transistor output (source)                        |                     |                        |                         |  |
| FX5-16EYR/ES     | Relay output                                      |                     |                        |                         |  |
| FX5-16EYT/ES     | Transistor output (sink)                          | 16 points           | 100 mA                 | 125 mA                  |  |
| FX5-16EYT/ESS    | Transistor output (source)                        |                     |                        |                         |  |
| FX5-16ER/ES      | DC input (sink/source)/relay output               |                     |                        |                         |  |
| FX5-16ET/ES      | DC input (sink/source)/transistor output (sink)   | 16 points           | 100 mA                 | 125 mA (85 mA*2)        |  |
| FX5-16ET/ESS     | DC input (sink/source)/transistor output (source) |                     |                        |                         |  |
| FX5-16ET/ES-H*1  | DC input (sink/source)/transistor output (sink)   | 16 pointo           | 100 mA                 | 105 mA (95 mA*2)        |  |
| FX5-16ET/ESS-H*1 | DC input (sink/source)/transistor output (source) | 16 points           | TUUTIIA                | 125 mA (85 mA*²)        |  |

\*1: Supported by FX5U/FX5UC CPU modules Ver. 1.030 or later.

\*2: Current consumption when an external power supply is used for input circuits.

## 5 FX5 intelligent function module

|               |   | Number of ecoupied      | Current consumption              |        |          |
|---------------|---|-------------------------|----------------------------------|--------|----------|
| Model         | Function       Number of occupied<br>input/output points       5 V DC<br>power supply       24 VI<br>power s         4 -ch voltage/current input       8 points       100 mA       40 mA         4 -ch voltage/current output       8 points       100 mA       -         8 -ch voltage/current/thermocouple/resistance<br>temperature detector input       8 points       -       40 mA         4 -ch temperature control (resistance temperature<br>detector/thermocouple/micro voltage)       8 points       140 mA       -         Pulse output for 2-axis control (transistor output)       8 points       -       -       -         Simple motion 4-axis control (SSCNET III/H compatible)       8 points       -       -       -         Simple motion 8-axis control (SSCNET III/H compatible)       8 points       -       -       - | 24 V DC<br>power supply | 24 V DC external<br>power supply |        |          |
| FX5-4AD*1     | 4-ch voltage/current input  | 8 points                | 100 mA                           | 40 mA  | -        |
| FX5-4DA*1     | 4-ch voltage/current output   | 8 points                | 100 mA                           | -      | 150 mA   |
| FX5-8AD*1     |   | 8 points                | - 40 mA                          |        | 100 mA   |
| FX5-4LC*1     |   | 8 points                | 140 mA                           | -      | 25 mA    |
| FX5-20PG-P*1  | Pulse output for 2-axis control (transistor output)   | 8 points                | _                                | -      | 120 mA   |
| FX5-20PG-D*1  | Pulse output for 2-axis control (differential driver output)  | 8 points                | _                                | -      | 165 mA   |
| FX5-40SSC-S   | Simple motion 4-axis control (SSCNET III/H compatible)  | 8 points                | _                                | -      | 250 mA   |
| FX5-80SSC-S   | Simple motion 8-axis control (SSCNET III/H compatible)  | 8 points                | _                                | -      | 250 mA   |
| FX5-ENET*2    | Ethernet communication  | 8 points                | _                                | 110 mA | -        |
| FX5-ENET/IP*2 | EtherNet/IP communication, Ethernet communication   | 8 points                | _                                | 110 mA | -        |
| FX5-CCL-MS*1  | CC-Link system master/intelligent device station  | 8 points*3              | _                                | -      | 100 mA   |
| FX5-CCLIEF*4  | CC-Link IE field network intelligent device station   | 8 points                | 10 mA                            | -      | 230 mA   |
| FX5-ASL-M*1   | AnyWireASLINK system master   | 8 points                | 200 mA                           | -      | 100 mA*5 |
| FX5-DP-M*2    | PROFIBUS-DP master  | 8 points                | _                                | 150 mA | -        |

\*1: Supported by FX5U/FX5UC CPU modules Ver. 1.050 or later.
\*2: Supported by FX5U/FX5UC CPU modules Ver. 1.110 or later.
\*3: When using FX5-CCL-MS as a master station, the number of remote I/O points on the network increases.
\*4: Supported by FX5U/FX5UC CPU modules Ver. 1.030 or later.
\*5: This value does not include the supply current to slave modules (Max. 2 A).

#### 6 Connector conversion module

|            |  | Number of occupied  | Current consumption    |                         |  |
|------------|--|---------------------|------------------------|-------------------------|--|
| Model      | Function   | input/output points | 5 V DC<br>power supply | 24 V DC<br>power supply |  |
| FX5-CNV-IF | Connector conversion<br>(FX5 (Extension cable type) →FX5 (Extension connector type)) | -                   | -                      | -                       |  |

#### **7** I/O module (Extension connector type)

|                   |   | Number of occupied  | Current consumption    |                         |  |
|-------------------|---|---------------------|------------------------|-------------------------|--|
| Model             | I/O type  | input/output points | 5 V DC<br>power supply | 24 V DC<br>power supply |  |
| FX5-C16EX/D       | DC input (sink)                                   | - 16 points         | 100 mA                 | 65 mA (0 mA*)           |  |
| FX5-C16EX/DS      | DC input (sink/source)                            | To points           | 100 MA                 | 05 MA (0 MA ')          |  |
| FX5-C32EX/D       | DC input (sink)                                   |                     |                        |                         |  |
| FX5-C32EX/DS      | DC input (sink/source)                            | 32 points           | 120 mA                 | 130 mA (0 mA*)          |  |
| FX5-C32EX/DS-TS   | DC linput (sink/source)                           |                     |                        |                         |  |
| FX5-C16EYT/D      | Transistor output (sink)                          |                     |                        |                         |  |
| FX5-C16EYT/DSS    | Transistor output (source)                        | 16 points           | 100 mA                 | 100 mA                  |  |
| FX5-C16EYR/D-TS   | Relay output                                      |                     |                        |                         |  |
| FX5-C32EYT/D      | Transistor output (sink)                          |                     |                        |                         |  |
| FX5-C32EYT/DSS    | Transistor output (source)                        |                     | 120 mA                 |                         |  |
| FX5-C32EYT/D-TS   | Transistor output (sink)                          | 32 points           |                        | 200 mA                  |  |
| FX5-C32EYT/DSS-TS | Transistor output (source)                        |                     |                        |                         |  |
| FX5-C32ET/D       | DC input (sink)/transistor output (sink)          |                     |                        |                         |  |
| FX5-C32ET/DSS     | DC input (sink/source)/transistor output (source) | Input: 16 points    | 120 mA                 | 165 mA (100 mA*)        |  |
| FX5-C32ET/DS-TS   | DC input (sink/source)/transistor output (sink)   | Output: 16 points   |                        | 100 IIIA (100 IIIA'')   |  |
| FX5-C32ET/DSS-TS  | DC input (sink/source)/transistor output (source) |                     |                        |                         |  |

\*: Current consumption when an external power supply is used for the input circuit.

#### 8 Bus conversion module

|              |   | Number of occupied  | Current consumption    |                         |  |
|--------------|---|---------------------|------------------------|-------------------------|--|
| Model        | Function  | input/output points | 5 V DC<br>power supply | 24 V DC<br>power supply |  |
| FX5-CNV-BUSC | Bus conversion<br>FX5 (extension cable type) →FX3 extension | 9 pointo            | 150 mA                 |                         |  |
| FX5-CNV-BUS  | Bus conversion<br>FX5 (extension cable type) →FX3 extension | 8 points            | TOUTIA                 |                         |  |

#### 9 FX5 expansion board

|                |   | Number of occupied  | Current consumption    |                         |  |
|----------------|---|---------------------|------------------------|-------------------------|--|
| Model          | Function                                  | input/output points | 5 V DC<br>power supply | 24 V DC<br>power supply |  |
| FX5-232-BD     | RS-232C communication                     |                     | 20 mA                  |                         |  |
| FX5-485-BD     | RS-485 communication                      | ] -                 | 20 MA                  | _                       |  |
| FX5-422-BD-GOT | RS-422 communication (for GOT connection) |                     | 20 mA*                 |                         |  |

\*: The current consumption will increase when the 5 V type GOT is connected.

#### 10 FX5 expansion adapter

|                 |   | Number of occupied  | Number of occupied     |                         | Current consumption              |  |  |
|-----------------|---|---------------------|------------------------|-------------------------|----------------------------------|--|--|
| Model           | Function  | input/output points | 5 V DC<br>power supply | 24 V DC<br>power supply | 24 V DC external<br>power supply |  |  |
| FX5-232ADP      | RS-232C communication   |                     | 30 mA                  | 30 mA                   |                                  |  |  |
| FX5-485ADP      | RS-485 communication  |                     | 20 mA                  | 50 MA                   | _                                |  |  |
| FX5-4AD-ADP     | 4 ch voltage input/current input                                |                     |                        |                         |                                  |  |  |
| FX5-4AD-PT-ADP* | 4 ch temperature sensor (resistance temperature detector) input | -                   | 10 mA                  | 20 mA                   |                                  |  |  |
| FX5-4AD-TC-ADP* | 4 ch temperature sensor (thermocouple) input                    |                     |                        |                         |                                  |  |  |
| FX5-4DA-ADP     | 4 ch voltage output/current output                              |                     |                        | _                       | 160 mA                           |  |  |

\*: Supported by FX5U/FX5UC CPU modules Ver. 1.040 or later.

#### **11** FX3 extension power supply module

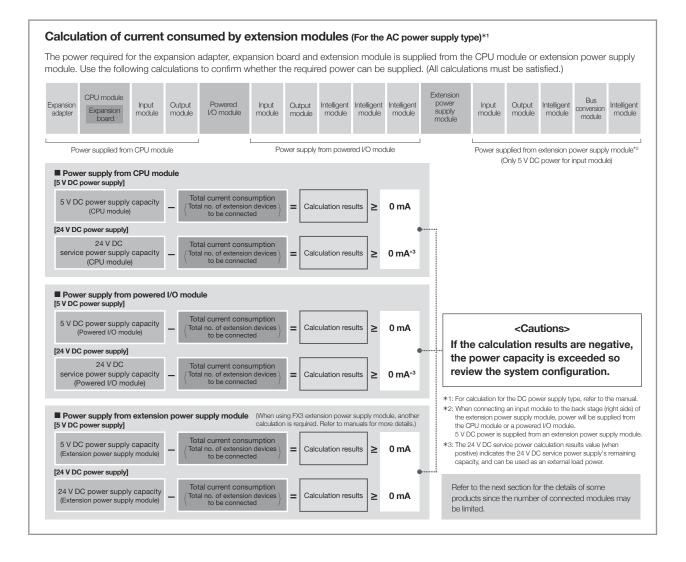
|              |                        | Number of occupied  | Current consumption    |                         |  |
|--------------|------------------------|---------------------|------------------------|-------------------------|--|
| Model        | Function               | input/output points | 5 V DC<br>power supply | 24 V DC<br>power supply |  |
| FX3U-1PSU-5V | Extension power supply | -                   | 1000 mA*               | 300 mA*                 |  |

\*: Derating occurs when the ambient temperature exceeds 40°C. For details, refer to manuals of each product.

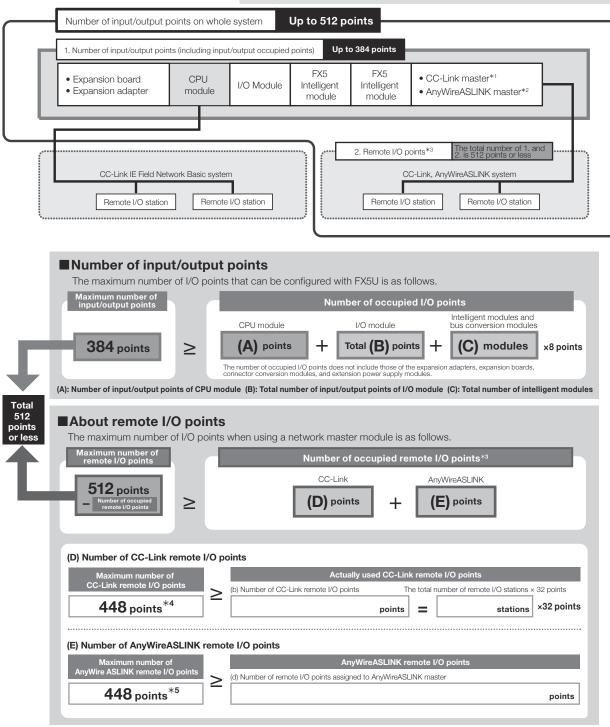
#### 12 FX3 intelligent function module

|               |   | Number of occupied  | Current consumption    |                         |                                  |  |
|---------------|---|---------------------|------------------------|-------------------------|----------------------------------|--|
| Model         | Function  | input/output points | 5 V DC<br>power supply | 24 V DC<br>power supply | 24 V DC external<br>power supply |  |
| FX3U-4AD      | 4 ch voltage input/current input  |                     | 110 mA                 |                         | 90 mA                            |  |
| FX3U-4DA      | 4 ch voltage output/current output  |                     | 120 mA                 |                         | 160 mA                           |  |
| FX3U-4LC      | 4-loop temperature control<br>(resistance thermometer/thermocouple/micro voltage) | 8 points            | 160 mA                 | -                       | 50 mA                            |  |
| FX3U-1PG      | Pulse output for 1-axis control   |                     | 150 mA                 | _                       | 40 mA                            |  |
| FX3U-2HC      | 2 ch high-speed counter   |                     | 245 mA                 |                         | -                                |  |
| FX3U-16CCL-M  | CC-Link master  | 8 points*1          |                        |                         | 240 mA                           |  |
| FX3U-64CCL    | CC-Link intelligent device station  | 8 points            |                        |                         | 220 mA                           |  |
| FX3U-128ASL-M | AnyWireASLINK system master   | 8 points*2          | 130 mA                 |                         | 100 mA*3                         |  |
| FX3U-32DP     | PROFIBUS-DP slave station   | 8 points            | —                      | 145 mA                  | -                                |  |

\*1: When using FX3U-16CCL-M as a master station, the number of remote I/O points on the network increases.
\*2: The number of input/output points set by the rotary switch is added.
\*3: This value does not include the supply current to slave modules (Max. 2 A).



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# Rules for System Configuration

The total number of I/O points and remote I/O points for the CPU module and extension devices controllable in FX5U CPU module is 512 points or less.

\*1: A bus conversion module is required when using the FX3U-16CCL-M.

\*2: A bus conversion module is required when using the FX3U-128ASL-M.

\*3: CC-Link IE Field Network Basic remote I/O stations are not calculated as remote I/O points.

\*4: 256 points when FX3U-16CCL-M is used \*5: 128 points when FX3U-128ASL-M is used

The number of points will vary if the CPU module firmware version is below 1.110. For details, refer to manuals of each product.

#### Limitation on power supply type when connecting

It is not possible to install both the AC type and the DC type in one system. The power supply type is limited for extension modules connectable to the following CPU modules. For details, refer to the manual of each product.

| Type/model/power supply type                          | Connectable extension module  |                                    |  |  |
|---|-------------------------------|------------------------------------|--|--|
| Type/model/power supply type                          | Туре                          | Model/power supply type            |  |  |
| FX5U CPU module FX5U-□M□/E□ (AC power supply type)    | Powered I/O module            | FX5-32E□/E□ (AC power supply type) |  |  |
| FASO GPO Module FASO-LIMIL/ELI (AC power supply type) | Extension power supply module | FX5-1PSU-5V (AC power supply type) |  |  |
| FX5U CPU module FX5U-□M□/D□ (DC power supply type)    | Powered I/O module            | FX5-32E□/D□ (DC power supply type) |  |  |
| FASO CPO Module FASO-LIMIL/DL (DC power supply type)  | Extension power supply module | FX5-C1PS-5V (DC power supply type) |  |  |

#### Limitation on number of modules when extending

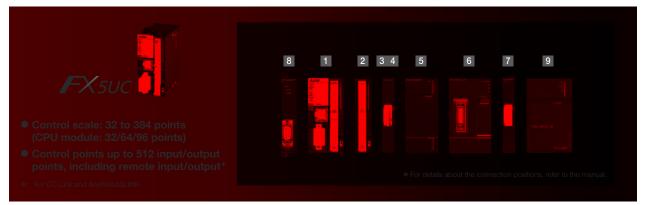
The number of connectable modules is limited for the following products. For details, refer to manuals of each product.

| Туре                              | Model/type     | Setting method/precautions   |  |  |
|-----------------------------------|----------------|--|--|--|
| I/O module (Extension cable type) | FX5-16ET/ES-H  | Line to 4 modulos can be connected for the ontire sustem   |  |  |
| 1/O Module (Extension cable type) | FX5-16ET/ESS-H | Up to 4 modules can be connected for the entire system.  |  |  |
|                                   | FX5-CCL-MS     | One module can be connected in the entire system for each station type.  Master station: 1 module*1  Intelligent device station: 1 module*2                          |  |  |
|                                   | FX5-ENET       |  |  |  |
| DVE intelligent function module   | FX5-ENET/IP    | Only 1 module can be connected in the online system  |  |  |
| FX5 intelligent function module   | FX5-CCLIEF     | Only 1 module can be connected in the entire system.   |  |  |
|                                   | FX5-DP-M       |  |  |  |
|                                   | FX5-ASL-M      | Only 1 module can be connected in the entire system. Use together with the FX3U-128ASL-M is not possible.  |  |  |
|                                   | FX3U-4AD       |  |  |  |
|                                   | FX3U-4DA       | ■When using FX3U-1PSU-5V: Up to 8 modules can be connected per system.   |  |  |
|                                   | FX3U-1PG       | ■When not using FX3U-1PSU-5V: Up to 6 modules can be connected per system.   |  |  |
|                                   | FX3U-4LC       |  |  |  |
|                                   | FX3U-128ASL-M  | Only 1 module can be connected in the entire system. It cannot be used together with the FX5-ASL-M.  |  |  |
| FX3 intelligent function module   | FX3U-16CCL-M   | Only 1 module can be connected in the entire system.<br>When using the FX5-CCL-MS as the master station, it cannot be used together with the FX5-CCL-MS.             |  |  |
|                                   | FX3U-64CCL     | Only 1 module can be connected in the entire system.<br>When using the FX5-CCL-MS as the intelligent device station, it cannot be used together with the FX5-CCL-MS. |  |  |
|                                   | FX3U-2HC       | Up to 2 modules can be connected for the entire system.<br>When not using the FX3U-1PSU-5V, connect immediately after the bus conversion module.                     |  |  |

\*1: When using the FX5-CCL-MS as the master station, it cannot be used together with the FX3U-16CCL-M. \*2: When using the FX5-CCL-MS as the intelligent device station, it cannot be used together with the FX3U-64CCL.

# Selecting the FX5UC model

# ◇ Product configuration



| Туре                                       | Details  | Connection details, model selection  |
|--|--|--|
| 1 CPU module                               | PLC with built-in CPU, power supply, input/output and program memory.  | Various extension devices can be connected.  |
| 2 I/O module<br>(extension connector type) | Product for extension I/O of extension connector type.   | Input/output can be extended to up to 256 points/384 points*1.<br>Up to 16 extension modules can be connected. (Extension power supply modules<br>and connector conversion modules are not included in the number of connected<br>modules.)<br>For details, refer to "Rules for System Configuration" on p. 55.                            |
| 3 FX5 extension power supply module        | Module for extension power supply if CPU<br>module's internal power supply is insufficient.<br>Connector conversion function is also provided. | Power can be supplied to I/O module, intelligent function module, and bus<br>conversion module.<br>Up to 2 modules can be connected.   |
| 4 Connector conversion module              | Module for connecting FX5 (extension cable type) extension module  | Extension devices (extension cable type) for FX5 can be connected.   |
| 5 I/O module<br>(extension cable type)     | Product for extending I/O of extension cable type.   | Input/output can be extended to up to 256 points/384 points*1.<br>Up to 16 extension modules can be connected. (Connector conversion modules<br>are not included in the number of connected modules.)<br>Up to 4 high-speed pulse I/O modules can be connected.<br>Using this type of I/O module requires the connector conversion module. |
| 6 FX5 intelligent function module          | Module with functions other than input/output.   | Up to 16 extension modules including I/O modules can be connected. (Connector conversion modules are not included in the number of connected modules.) Using this type of module requires the connector conversion module.   |
| 7 Bus conversion module                    | Conversion module for connecting FX3 extension module.   | FX3 Series extension modules can be connected only to the right side of the bus<br>conversion module.<br>Using the FX5-CNV-BUS requires the connector conversion module or extension<br>power supply module.   |
| 8 FX5 expansion adapter                    | Adapter connected to left side of CPU module to expand functions.  | Up to 6 modules can be connected to the left side of the CPU module.   |
| 9 FX3 intelligent function module          | Module with functions other than input/output.   | Up to 6 modules* <sup>2</sup> can be connected to the right side of the bus conversion module.<br>The bus conversion module is required for use.   |

#1: Supported by FX5U/FX5UC Ver. 1.100 or later and by GX Works3 Ver. 1.047Z or later. \*2: Excluding some models

#### 1 CPU module

|                   |            | Number of occupied  | Power sup              | ply capacity            |  | No. of  | No. of                              |   |  |
|-------------------|------------|---------------------|------------------------|-------------------------|--|---|-------------------------------------|---|--|
| Model             | Function   | input/output points | 5 V DC<br>power supply | 24 V DC<br>power supply | I/O type                                   | input<br>points                                   | output<br>points                    |   |  |
| FX5UC-32MT/D      |            |                     |                        |                         | DC input (sink)/transistor (sink)          |   |                                     |   |  |
| FX5UC-32MT/DSS    |            |                     |                        |                         | DC input (sink/source)/transistor (source) |   |                                     |   |  |
| FX5UC-32MT/DS-TS  |            | 32 points           |                        |                         | DC input (sink/source)/transistor (sink)   | ] 16<br>  points                                  | 16<br>points                        |   |  |
| FX5UC-32MT/DSS-TS |            |                     | 720 mA 50              |                         | DC input (sink/source)/transistor (source) |   | 10 0                                |   |  |
| FX5UC-32MR/DS-TS  | CPU module |                     | 720 mA 500 mA          |                         | 720 mA 500 mA                              | 720 mA 500 mA DC input (sink/source)/relay output | DC input (sink/source)/relay output | 7 |  |
| FX5UC-64MT/D      |            | O.4 m sints         |                        |                         | DC input (sink)/transistor (sink)          | 32  | 32                                  |   |  |
| FX5UC-64MT/DSS    |            | 64 points           |                        |                         | DC input (sink/source)/transistor (source) | points  | points                              |   |  |
| FX5UC-96MT/D      |            | 00 mainte           |                        |                         | DC input (sink)/transistor (sink)          | 48  | 48                                  |   |  |
| FX5UC-96MT/DSS    |            | 96 points           |                        |                         | DC input (sink/source)/transistor (source) | points  | points                              |   |  |

## **2** I/O module (extension connector type)

|                   |   |   | Current consumption    |                         |  |  |  |
|-------------------|---|---|------------------------|-------------------------|--|--|--|
| Model             | l/O type  | Number of occupied<br>input/output points | 5 V DC<br>power supply | 24 V DC<br>power supply | 24 V DC external<br>power supply<br>(24 V DC power<br>supply for input<br>circuit) |  |  |
| FX5-C16EX/D       | DC input (sink)                                   | - 16 points                               | 100 mA                 |                         | 65 mA  |  |  |
| FX5-C16EX/DS      | DC input (sink/source)                            | TO POINTS                                 | 100 111A               |                         | 05111A   |  |  |
| FX5-C32EX/D       | DC input (sink)                                   |   |                        | ] –                     | 130 mA   |  |  |
| FX5-C32EX/DS      | DC input (sink/source)                            | 32 points                                 | 120 mA                 |                         |  |  |  |
| FX5-C32EX/DS-TS   |   |   |                        |                         |  |  |  |
| FX5-C16EYT/D      | Transistor output (sink)                          | 16 points                                 | 100 mA                 |                         | _  |  |  |
| FX5-C16EYT/DSS    | Transistor output (source)                        |   |                        | 100 mA                  |  |  |  |
| FX5-C16EYR/D-TS   | Relay output                                      |   |                        |                         |  |  |  |
| FX5-C32EYT/D      | Transistor output (sink)                          |   |                        |                         |  |  |  |
| FX5-C32EYT/DSS    | Transistor output (source)                        | - 32 points                               | 120 mA                 | 200 mA                  |  |  |  |
| FX5-C32EYT/D-TS   | Transistor output (sink)                          | 32 points                                 | 120 MA                 | 200 MA                  |  |  |  |
| FX5-C32EYT/DSS-TS | Transistor output (source)                        |   |                        |                         |  |  |  |
| FX5-C32ET/D       | DC input (sink)/transistor output (sink)          |   |                        |                         |  |  |  |
| FX5-C32ET/DSS     | DC input (sink/source)/transistor output (source) | Input: 16 points                          | 120 mA                 | 100 mA                  | 65 mA  |  |  |
| FX5-C32ET/DS-TS   | DC input (sink/source)/transistor output (sink)   | Output: 16 points                         | 120 MA                 |                         | Am co  |  |  |
| FX5-C32ET/DSS-TS  | DC input (sink/source)/transistor output (source) |   |                        |                         |  |  |  |

#### **3** FX5 extension power supply module

| Model       | Function               | Number of occupied  | d Power supply capacity |                      |  |  |
|-------------|------------------------|---------------------|-------------------------|----------------------|--|--|
|             | T UNCTON               | input/output points | 5 V DC power supply     | 24 V DC power supply |  |  |
| FX5-C1PS-5V | Extension power supply | _                   | 1200 mA*                | 625 mA*              |  |  |

\*: Derating occurs when the ambient temperature exceeds 40°C. For details, refer to the manual.

#### 4 Connector conversion module

|             |  | Number of occupied  | Current consumption |                     |  |
|-------------|--|---------------------|---------------------|---------------------|--|
| Model       | Function   | input/output points | 5 V DC internal     | 24 V DC internal    |  |
|             |  | nipas carpar ponto  | current consumption | current consumption |  |
| FX5-CNV-IFC | Connector conversion (FX5 (Extension connector type) → FX5 (Extension cable type)) | -                   | _                   | -                   |  |

# 5 -1) I/O module (DC power supply/DC input type) (extension cable type)

|              |                     | Number of       | Power supply capacity |              |   |  |
|--------------|---------------------|-----------------|-----------------------|--------------|---|--|
| Model        | Function            | occupied input/ | 5 V DC                | 24 V DC      | I/O type  |  |
|              |                     | output points   | power supply          | power supply |   |  |
| FX5-32ER/DS  |                     | 32 points       |                       |              | DC input (sink/source)/relay output               |  |
| FX5-32ET/DS  | Input/output module |                 | 965 mA                | 310 mA       | DC input (sink/source)/transistor output (sink)   |  |
| FX5-32ET/DSS |                     |                 |                       |              | DC input (sink/source)/transistor output (source) |  |

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|                 |   |  | Current consumption    |                         |  |  |  |
|-----------------|---|--|------------------------|-------------------------|--|--|--|
| Model           | Function  | Number of occupied input/output points | 5 V DC<br>power supply | 24 V DC<br>power supply | 24 V DC external<br>power supply<br>(24 V DC power<br>supply for input<br>circuit) |  |  |
| FX5-8EX/ES      | DC input (sink/source)                            | 8 points                               | 75 mA                  |                         | 50 mA  |  |  |
| FX5-16EX/ES     | DC input (sink/source)                            | 16 points                              | 100 mA                 |                         | 85 mA  |  |  |
| FX5-8EYR/ES     | Relay output                                      |  |                        |                         |  |  |  |
| FX5-8EYT/ES     | Transistor output (sink)                          | 8 points                               | 75 mA                  | 75 mA                   | _  |  |  |
| FX5-8EYT/ESS    | Transistor output (source)                        |  |                        |                         |  |  |  |
| FX5-16EYR/ES    | Relay output                                      |  |                        |                         |  |  |  |
| FX5-16EYT/ES    | Transistor output (sink)                          | 16 points                              | 100 mA                 | 125 mA                  |  |  |  |
| FX5-16EYT/ESS   | Transistor output (source)                        |  |                        |                         |  |  |  |
| FX5-16ER/ES     | DC input (sink/source)/relay output               |  |                        |                         |  |  |  |
| FX5-16ET/ES     | DC input (sink/source)/transistor output (sink)   | 16 points                              | 100 mA                 | 85 mA                   | 40 mA  |  |  |
| FX5-16ET/ESS    | DC input (sink/source)/transistor output (source) |  |                        |                         |  |  |  |
| FX5-16ET/ES-H*  | DC input (sink/source)/transistor output (sink)   | 16 pointo                              | 100 mA                 | 85 mA                   | 40 mA  |  |  |
| FX5-16ET/ESS-H* | DC input (sink/source)/transistor output (source) | – 16 points                            | TOUTHA                 | AITEO                   | 40 mA  |  |  |

#### **5** -2) I/O module (extension cable type)

\*: Supported by FX5U/FX5UC CPU module Ver. 1.030 or later.

#### 6 FX5 intelligent function module

|               |   | Number of occupied  | Current consumption    |                         |                                  |  |
|---------------|---|---------------------|------------------------|-------------------------|----------------------------------|--|
| Model         | Function  | input/output points | 5 V DC<br>power supply | 24 V DC<br>power supply | 24 V DC external<br>power supply |  |
| FX5-4AD*1     | 4-ch voltage/current input  | 8 points            | 100 mA                 | 40 mA                   | -                                |  |
| FX5-4DA*1     | 4-ch voltage/current output   | 8 points            | 100 mA                 | -                       | 150 mA                           |  |
| FX5-8AD*1     | 8-ch voltage/current/thermocouple/resistance<br>temperature detector input            | 8 points            | -                      | 40 mA                   | 100 mA                           |  |
| FX5-4LC*1     | 4-ch temperature control (resistance temperature detector/thermocouple/micro voltage) | 8 points            | 140 mA —               |                         | 25 mA                            |  |
| FX5-20PG-P*1  | Pulse output for 2-axis control (transistor output)                                   | 8 points            | -                      | -                       | 120 mA                           |  |
| FX5-20PG-D*1  | Pulse output for 2-axis control (differential driver output)                          | 8 points            | -                      | _                       | 165 mA                           |  |
| FX5-40SSC-S   | Simple motion 4-axis control (SSCNET III/H compatible)                                | 8 points            | —                      | -                       | 250 mA                           |  |
| FX5-80SSC-S   | Simple motion 8-axis control (SSCNET III/H compatible)                                | 8 points            |                        |                         | 250 mA                           |  |
| FX5-ENET*2    | Ethernet communication  | 8 points            | _                      | 110 mA                  | -                                |  |
| FX5-ENET/IP*2 | EtherNet/IP communication, Ethernet communication                                     | 8 points            | _                      | 110 mA                  | -                                |  |
| FX5-CCL-MS*1  | CC-Link system master/intelligent device station                                      | 8 points*3          | -                      | -                       | 100 mA                           |  |
| FX5-CCLIEF*4  | CC-Link IE field network intelligent device station                                   | 8 points            | 10 mA                  | -                       | 230 mA                           |  |
| FX5-ASL-M*1   | AnyWireASLINK system master   | 8 points            | 200 mA                 | -                       | 100 mA*5                         |  |
| FX5-DP-M*2    | PROFIBUS-DP master  | 8 points            | —                      | 150 mA                  | -                                |  |

\*1: Supported by FX5U/FX5UC CPU module Ver. 1.050 or later.
\*2: Supported by FX5U/FX5UC CPU module Ver. 1.110 or later.
\*3: When using FX5-CCL-MS as a master station, the number of remote I/O points on the network increases.
\*4: Supported by FX5U/FX5UC CPU module Ver. 1.030 or later.
\*5: This value does not include the supply current to slave modules (Max. 2 A).

#### 7 Bus conversion module

|              |  | Number of occupied | Current consumption    |                         |  |
|--------------|--|--------------------|------------------------|-------------------------|--|
| Model        | Model Function   |                    | 5 V DC<br>power supply | 24 V DC<br>power supply |  |
| FX5-CNV-BUSC | Bus conversion<br>FX5 (extension connector type) → FX3 extension | 8 points           | 150 mA                 |                         |  |
| FX5-CNV-BUS  | Bus conversion<br>FX5 (extension cable type) → FX3 extension     | o points           | 150 MA                 | _                       |  |

#### 8 FX5 expansion adapter

|                 |   | Number of occupied  | Current consumption    |                         |                                  |  |
|-----------------|---|---------------------|------------------------|-------------------------|----------------------------------|--|
| Model           | Function  | input/output points | 5 V DC<br>power supply | 24 V DC<br>power supply | 24 V DC external<br>power supply |  |
| FX5-232ADP      | RS-232C communication   |                     | 30 mA                  | 00 m 1                  |                                  |  |
| FX5-485ADP      | RS-485 communication  |                     | 20 mA                  | 30 mA                   | _                                |  |
| FX5-4AD-ADP     | 4 ch voltage input/current input                                | -                   |                        |                         |                                  |  |
| FX5-4AD-PT-ADP* | 4 ch temperature sensor (resistance temperature detector) input |                     | 10 mA                  | 20 mA                   |                                  |  |
| FX5-4AD-TC-ADP* | 4 ch temperature sensor (thermocouple) input                    |                     |                        |                         |                                  |  |
| FX5-4DA-ADP     | 4 ch voltage output/current output                              |                     |                        | -                       | 160 mA                           |  |

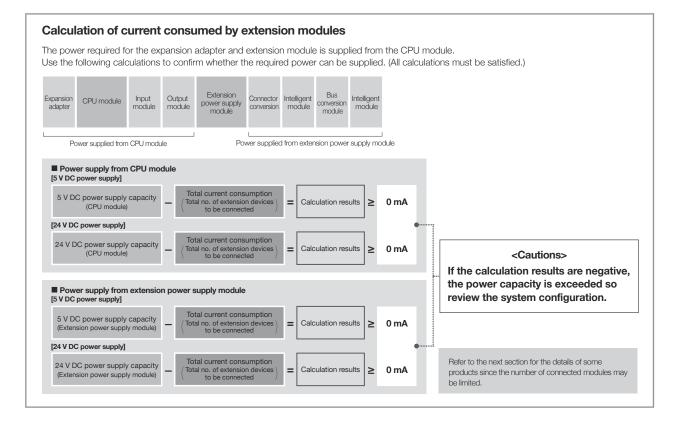
\*: Supported by FX5U/FX5UC CPU module Ver. 1.040 or later.

#### 9 FX3 intelligent function module

| Model         |   | Number of occupied  | Current consumption    |                         |                                  |  |  |
|---------------|---|---------------------|------------------------|-------------------------|----------------------------------|--|--|
|               | Function  | input/output points | 5 V DC<br>power supply | 24 V DC<br>power supply | 24 V DC external<br>power supply |  |  |
| FX3U-4AD      | 4 ch voltage input/current input  |                     | 110 mA                 |                         | 90 mA                            |  |  |
| FX3U-4DA      | 4 ch voltage output/current output  |                     | 120 mA                 |                         | 160 mA                           |  |  |
| FX3U-4LC      | 4-loop temperature control<br>(resistance thermometer/thermocouple/micro voltage) | _                   | 160 mA                 | -                       | 50 mA                            |  |  |
| FX3U-1PG      | Pulse output for 1-axis control   |                     | 150 mA                 | _                       | 40 mA                            |  |  |
| FX3U-2HC      | 2 ch high-speed counter   |                     | 245 mA                 |                         | -                                |  |  |
| FX3U-16CCL-M  | CC-Link master  | 8 points*1          |                        |                         | 240 mA                           |  |  |
| FX3U-64CCL    | CC-Link intelligent device station  | 8 points            | _                      |                         | 220 mA                           |  |  |
| FX3U-128ASL-M | AnyWireASLINK system master   | 8 points*2 130 mA   |                        | 1                       | 100 mA*3                         |  |  |
| FX3U-32DP     | PROFIBUS-DP slave station   | 8 points            | -                      | 145 mA                  | -                                |  |  |

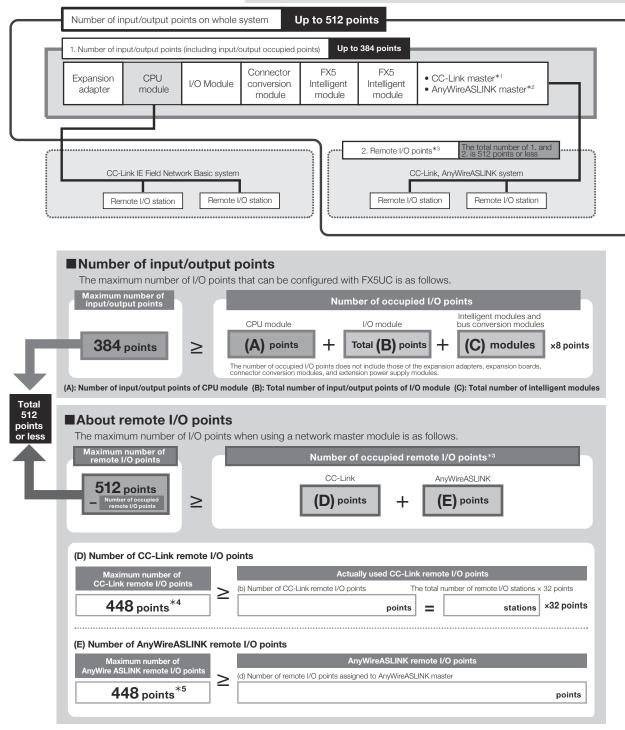
\*1: When using FX3U-16CCL-M as a master station, the number of remote I/O points on the network increases.

\*2: The number of input/output points set by the rotary switch is added.
\*3: This value does not include the supply current to slave modules.



# Rules for System Configuration

The total number of I/O points and remote I/O points for the CPU module and extension devices controllable in FX5UC CPU module is 512 points or less.



\*1: A bus conversion module is required when using the FX3U-16CCL-M.

\*2: A bus conversion module is required when using the FX3U-128ASL-M.

\*3: CC-Link IE Field Network Basic remote I/O stations are not calculated as remote I/O points.

\*4: 256 points when FX3U-16CCL-M is used

\*5: 128 points when FX3U-128ASL-M is used

#### Limitation on power supply type when connecting

It is not possible to install both the AC type and the DC type in one system.

The power supply type is limited for extension modules connectable to the following CPU modules. For details, refer to the manual of each product.

| Tupo/model/power aupply tupo                         | Connectable extension module  |                                    |  |  |
|--|-------------------------------|------------------------------------|--|--|
| Type/model/power supply type                         | Туре                          | Model/power supply type            |  |  |
| FX5UC CPU module FX5UC-IMI/DI (DC power supply type) | Powered I/O module            | FX5-32E□/D□ (DC power supply type) |  |  |
|  | Extension power supply module |                                    |  |  |

#### Limitation on number of modules when extending

The number of connectable modules is limited for the following products. For details, refer to manuals of each product.

| Туре                              | Model/type     | Setting method/precautions  |  |  |  |
|-----------------------------------|----------------|---|--|--|--|
| I/O module (Extension cable type) | FX5-16ET/ES-H  | Up to 4 modules can be connected for the entire system.   |  |  |  |
| 1/O Module (Extension cable type) | FX5-16ET/ESS-H | op to 4 modules can be connected for the entire system.   |  |  |  |
|                                   | FX5-CCL-MS     | One module can be connected in the entire system for each station type.  • Master station: 1 module*1  • Intelligent device station: 1 module*2                         |  |  |  |
|                                   | FX5-ENET       |   |  |  |  |
| EVE intelligent function module   | FX5-ENET/IP    | Only 1 module can be connected in the antice system   |  |  |  |
| FX5 intelligent function module   | FX5-CCLIEF     | Only 1 module can be connected in the entire system.  |  |  |  |
|                                   | FX5-DP-M       |   |  |  |  |
|                                   | FX5-ASL-M      | Only 1 module can be connected in the entire system. Use together with the FX3U-128ASL-M is not possible.   |  |  |  |
|                                   | FX3U-4AD       |   |  |  |  |
|                                   | FX3U-4DA       | ■When using FX3U-1PSU-5V: Up to 8 modules can be connected per system.  |  |  |  |
|                                   | FX3U-1PG       | When not using FX3U-1PSU-5V: Up to 6 modules can be connected per system.   |  |  |  |
|                                   | FX3U-4LC       |   |  |  |  |
|                                   | FX3U-128ASL-M  | Only 1 module can be connected in the entire system. It cannot be used together with the FX5-ASL-M.   |  |  |  |
| FX3 intelligent function module   | FX3U-16CCL-M   | Only 1 module can be connected in the entire system.<br>When using the FX5-CCL-MS as the master station, it cannot be used together with the FX5-CCL-MS.                |  |  |  |
|                                   | FX3U-64CCL     | Only 1 module can be connected in the entire system.<br>When using the FX5-CCL-MS as the intelligent device station, it cannot be used together with the<br>FX5-CCL-MS. |  |  |  |
|                                   | FX3U-2HC       | Up to 2 modules can be connected for the entire system.<br>Connect immediately after the bus conversion module.   |  |  |  |

\*1: When using the FX5-CCL-MS as the master station, it cannot be used together with the FX3U-16CCL-M. \*2: When using the FX5-CCL-MS as the intelligent device station, it cannot be used together with the FX3U-64CCL.

Refer to the manual for details on each model.

# I/O Module

The I/O module is a product for extending inputs/outputs. Some products are powered.

# Powered input/output modules

Powered input/output module is a powered input/output extension device.

Like with the CPU module, various I/O modules and intelligent function modules can be connected to the rear stage of extension module.

# ◇ List of powered input/output modules

| Model                |              | Total No. | No. of input/output points & Input/output ty |                          |           | output type            | ype Compatible CPU module |    | MASS (Weight): | External dimensions        |
|----------------------|--------------|-----------|--|--------------------------|-----------|------------------------|---------------------------|----|----------------|----------------------------|
| IVIOU                |              | of points | Input  |                          | Output    |                        | FX5U FX5UC                |    |                | $W \times H \times D$ (mm) |
| AC power supply type | FX5-32ER/ES  |           |  |                          |           | Relay                  |                           |    |                |                            |
|                      | FX5-32ET/ES  | 32 points | 16 points                                    | 24 V DC<br>(sink/source) | 16 points | Transistor<br>(sink)   | 0                         | ×  | Approx. 0.65   | 150 × 90 × 83              |
| -1                   | FX5-32ET/ESS |           |  |                          |           | Transistor<br>(source) |                           |    |                |                            |
| DC power supply type | FX5-32ER/DS  |           |  |                          |           | Relay                  |                           |    |                |                            |
|                      | FX5-32ET/DS  | 32 points | 16 points                                    | 24 V DC<br>(sink/source) | 16 points | Transistor<br>(sink)   | 0                         | 0* | Approx. 0.65   | 150 × 90 × 83              |
|                      | FX5-32ET/DSS |           |  |                          |           | Transistor<br>(source) |                           |    |                |                            |

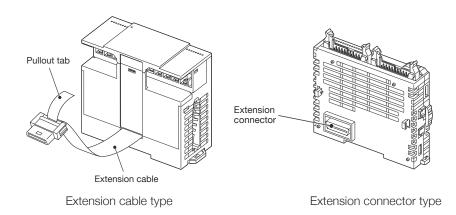
\*: Connection with FX5UC requires FX5-CNV-IFC.

# $\diamond$ Connection cable

The extension cable for connection to the right side of the front-stage device is offered as an accessory of each powered I/O module.

# I/O module

Input modules/output modules receive the power from the CPU module, and extend input/output points. Each module can be offered as the extension cable type or extension connector type.



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# $\diamond$ List of input modules (extension cable type)

| Мо   | dal         | Total No. | No. of i  | nput/output poi          | ints & Input/ | output type | Compatible | CPU module | MASS (Weight): | External dimensions        |
|------|-------------|-----------|-----------|--------------------------|---------------|-------------|------------|------------|----------------|----------------------------|
| IVIU | UEI         | of points | I         |                          | С             | Output      | FX5U       | FX5UC      | kg             | $W \times H \times D$ (mm) |
| 8    | FX5-8EX/ES  | 8 points  | 8 points  | 24 V DC<br>(sink/source) | _             | _           | 0          | 0*         | Approx. 0.2    | 40 × 00 × 92               |
| C II | FX5-16EX/ES | 16 points | 16 points | 24 V DC<br>(sink/source) | -             | _           |            |            | Approx. 0.25   | 40 × 90 × 83               |

\*: Connection with FX5UC requires FX5-CNV-IFC or FX5-C1PS-5V.

# $\diamond$ List of output modules (extension cable type)

| Mc       | odel          | Total No. | No. of i | nput/output poir | nts & Input/o | output type            | Compatible | CPU module | MASS (Weight): | External dimensions        |
|----------|---------------|-----------|----------|------------------|---------------|------------------------|------------|------------|----------------|----------------------------|
| IVIC     |               | of points |          | Input            | 0             | utput                  | FX5U       | FX5UC      | kg             | $W \times H \times D$ (mm) |
| 8        | FX5-8EYR/ES   | 8 points  |          |                  | 8 points      | Relay                  |            |            | Approx. 0.2    |                            |
| R.       | FX5-8EYT/ES   | 8 points  |          |                  | 8 points      | Transistor<br>(sink)   |            |            | Approx. 0.2    |                            |
| 8        | FX5-8EYT/ESS  | 8 points  |          |                  | 8 points      | Transistor<br>(source) | 0          | 0*         | Approx. 0.2    | 40 00 02                   |
| C.       | FX5-16EYR/ES  | 16 points | _        | _                | 16 points     | Relay                  |            |            | Approx. 0.25   | 40 × 90 × 83               |
| C.       | FX5-16EYT/ES  | 16 points |          |                  | 16 points     | Transistor<br>(sink)   |            |            | Approx. 0.25   |                            |
| <b>P</b> | FX5-16EYT/ESS | 16 points |          |                  | 16 points     | Transistor<br>(source) |            |            | Approx. 0.25   |                            |

\*: Connection with FX5UC requires FX5-CNV-IFC or FX5-C1PS-5V.

# ◇ List of Input/output modules (extension cable type)

| Model    | Total No.      | No. of i | nput/output poir         | nts & Input/o | output type            | Compatible | CPU module | MASS (Weight): | External dimensions        |
|----------|----------------|----------|--------------------------|---------------|------------------------|------------|------------|----------------|----------------------------|
|          | of points      |          | Input                    | 0             | utput                  | FX5U       | FX5UC      | kg             | $W \times H \times D$ (mm) |
| FX5-16EF | R/ES           |          |                          |               | Relay                  |            |            |                |                            |
| FX5-16ET | T/ES 16 points | 8 points | 24 V DC<br>(sink/source) | 8 points      | Transistor<br>(sink)   | 0          | 0*         | Approx. 0.25   | 40 × 90 × 83               |
| FX5-16ET | T/ESS          |          |                          |               | Transistor<br>(source) |            |            |                |                            |

\*: Connection with FX5UC requires FX5-CNV-IFC or FX5-C1PS-5V.

| Ma   | del            | Total No. | No. of i | nput/output poir | nts & Input/o | output type            | Compatible | CPU module | MASS         | External dimensions        |
|------|----------------|-----------|----------|------------------|---------------|------------------------|------------|------------|--------------|----------------------------|
| IVIC | JUEI           | of points | Input    |                  | Output        |                        | FX5U       | FX5UC      | (Weight): kg | $W \times H \times D$ (mm) |
| 1    | FX5-16ET/ES-H  | 16 points | 8 points | 24 V DC          | 8 points      | Transistor<br>(sink)   |            | 0*         | Approx. 0.25 | 40 × 90 × 83               |
|      | FX5-16ET/ESS-H |           | o points | (sink/source)    | o points      | Transistor<br>(source) |            |            | Αμμιύχ. 0.25 | 40 × 90 × 83               |

# ♦ List of high-speed pulse input/output modules (extension cable type)

\*: Connection with FX5UC requires FX5-CNV-IFC or FX5-C1PS-5V.

# **Connection cable**

Extension cable type input/output modules are equipped with the extension cable for connection to the right side of the front-stage device.

# ♦ List of input modules (extension connector type)

|   | Model           | Total No. | No. of ir | nput/output poir         | nts & Input/o | output type | Compatible | CPU module | MASS (Weight): | External dimensions        |
|---|-----------------|-----------|-----------|--------------------------|---------------|-------------|------------|------------|----------------|----------------------------|
|   | WIDLEI          | of points | I         | Input                    | 0             | utput       | FX5U       | FX5UC      |                | $W \times H \times D$ (mm) |
|   | FX5-C16EX/D     | 16 points | 16 points | 24 V DC<br>(sink)        |               |             |            |            | Approx. 0.1    | 14.6 × 90 × 87             |
| 1 | FX5-C16EX/DS    | TO POINTS | TO POINTS | 24 V DC<br>(sink/source) |               |             |            |            | Approx. 0.1    | 14.6 × 90 × 87             |
|   | FX5-C32EX/D     |           |           | 24 V DC<br>(sink)        | -             | -           | 0*         | 0          | Approx. 0.15   | 20.1 × 90 × 87             |
| 1 | FX5-C32EX/DS    | 32 points | 32 points | 24 V DC                  |               |             |            |            | Approx. 0.15   | 20.1 × 90 × 87             |
|   | FX5-C32EX/DS-TS |           |           | (sink/source)            |               |             |            |            | Approx. 0.15   | 20.1 × 90 × 93.7           |

\*: Connection with FX5U requires FX5-CNV-IF.

# ◇ List of output modules (extension connector type)

|    | Model             | Total No. | No. of i | nput/output poir | nts & Input/a | output type            | Compatible | CPU module | MASS (Weight): | External dimensions        |
|----|-------------------|-----------|----------|------------------|---------------|------------------------|------------|------------|----------------|----------------------------|
|    | IVIOUEI           | of points |          | Input            | 0             | utput                  | FX5U       | FX5UC      |                | $W \times H \times D$ (mm) |
|    | FX5-C16EYT/D      |           |          |                  |               | Transistor<br>(sink)   |            |            | Approx. 0.1    | 14.6 × 90 × 87             |
|    | FX5-C16EYT/DSS    | 16 points |          |                  | 16 points     | Transistor<br>(source) |            |            | Approx. 0.1    | 14.6 × 90 × 87             |
| 20 | FX5-C16EYR/D-TS   |           |          |                  |               | Relay                  |            |            | Approx. 0.2    | 30.7 × 90 × 93.7           |
|    | FX5-C32EYT/D      |           | _        | _                |               | Transistor<br>(sink)   | 0*         | 0          | Approx. 0.15   | 20.1 × 90 × 87             |
|    | FX5-C32EYT/DSS    | 22 points |          |                  | 22 points     | Transistor<br>(source) |            |            | Approx. 0.15   | 20.1 × 90 × 87             |
|    | FX5-C32EYT/D-TS   | 32 points |          |                  | 32 points -   | Transistor<br>(sink)   |            |            | Approx. 0.15   | 20.1 × 90 × 93.7           |
|    | FX5-C32EYT/DSS-TS |           |          |                  |               | Transistor<br>(source) |            |            | Approx. 0.15   | 20.1 × 90 × 93.7           |

\*: Connection with FX5U requires FX5-CNV-IF.

# $\diamond$ List of I/O modules (extension connector type)

|   | Model            | Total No. | No. of i  | nput/output poir         | nts & Input/o | output type            | Compatible | CPU module | MASS (Weight): | External dimensions        |
|---|------------------|-----------|-----------|--------------------------|---------------|------------------------|------------|------------|----------------|----------------------------|
|   | MODEI            | of points |           | Input                    | 0             | utput                  | FX5U       | FX5UC      | kg             | $W \times H \times D$ (mm) |
|   | FX5-C32ET/D      |           |           | 24 V DC<br>(sink)        |               | Transistor<br>(sink)   |            |            | Approx. 0.15   | 20.1 × 90 × 87             |
|   | FX5-C32ET/DSS    | 32 points | 16 points |                          | 16 points     | Transistor<br>(source) | 0*         | 0          | Approx. 0.15   | 20.1 × 90 × 87             |
| 1 | FX5-C32ET/DS-TS  | 52 points | TO POINTS | 24 V DC<br>(sink/source) | TO POINTS     | Transistor<br>(sink)   |            | 0          | Approx. 0.15   | 20.1 × 90 × 93.7           |
|   | FX5-C32ET/DSS-TS |           |           |                          |               | Transistor<br>(source) |            |            | Approx. 0.15   | 20.1 × 90 × 93.7           |

\*: Connection with FX5U requires FX5-CNV-IF.

# Examples of combinations of FX5U inputs/outputs



The table below shows examples of combinations of FX5U extension modules. The contents of combinations can be described based on the number of input points.

• In addition to the combinations shown below, various combinations can be made by changing selected I/O modules and extension modules.

| Numł<br>I/O p | oer of<br>oints | CPI             | J modı | ıle    |       | output<br>dule | input/<br>mo | rered<br>output<br>dule<br>-32E |       | output<br>dule | I/O<br>total |
|---------------|-----------------|-----------------|--------|--------|-------|----------------|--------------|---------------------------------|-------|----------------|--------------|
| Input         | Output          | Module<br>model | Input  | Output | Input | Output         | Input        | Output                          | Input | Output         |              |
| 16            | 16              | 32M             | 16     | 16     |       |                |              |                                 |       |                | 32           |
| 16            | 24              | 32M             | 16     | 16     | 0     | 8              |              |                                 |       |                | 40           |
| 16            | 32              | 32M             | 16     | 16     | 0     | 16             |              |                                 |       |                | 48           |
| 16            | 40              | 32M             | 16     | 16     | 0     | 24             |              |                                 |       |                | 56           |
| 16            | 48              | 32M             | 16     | 16     | 0     | 32             |              |                                 |       |                | 64           |
| 16            | 64              | 32M             | 16     | 16     | 0     | 48             |              |                                 |       |                | 80           |
| 24            | 16              | 32M             | 16     | 16     | 8     | 0              |              |                                 |       |                | 40           |
| 24            | 24              | 32M             | 16     | 16     | 8     | 8              |              |                                 |       |                | 48           |
| 24            | 32              | 32M             | 16     | 16     | 8     | 16             |              |                                 |       |                | 56           |
| 24            | 40              | 32M             | 16     | 16     | 8     | 24             |              |                                 |       |                | 64           |
| 32            | 16              | 32M             | 16     | 16     | 16    | 0              |              |                                 |       |                | 48           |
| 32            | 32              | 32M             | 16     | 16     | 16    | 16             |              |                                 |       |                | 64           |
| 32            | 32              | 32M             | 16     | 16     | 0     | 0              | 16           | 16                              |       |                | 64           |
| 32            | 32              | 64M             | 32     | 32     |       |                |              |                                 |       |                | 64           |
| 32            | 40              | 32M             | 16     | 16     | 0     | 8              | 16           | 16                              |       |                | 72           |
| 32            | 40              | 64M             | 32     | 32     | 0     | 8              |              |                                 |       |                | 72           |
| 32            | 48              | 32M             | 16     | 16     | 0     | 16             | 16           | 16                              |       |                | 80           |
| 32            | 48              | 64M             | 32     | 32     | 0     | 16             |              |                                 |       |                | 80           |
| 32            | 56              | 32M             | 16     | 16     | 0     | 24             | 16           | 16                              |       |                | 88           |
| 32            | 56              | 64M             | 32     | 32     | 0     | 24             |              |                                 |       |                | 88           |
| 32            | 64              | 64M             | 32     | 32     | 0     | 32             |              |                                 |       |                | 96           |
| 32            | 80              | 64M             | 32     | 32     | 0     | 48             |              |                                 |       |                | 112          |
| 32            | 80              | 64M             | 32     | 32     | 0     | 48             |              |                                 |       |                | 112          |
| 32            | 80              | 64M             | 32     | 32     | 0     | 48             |              |                                 |       |                | 112          |
| 40            | 16              | 32M             | 16     | 16     | 24    | 0              |              |                                 |       |                | 56           |
| 40            | 24              | 32M             | 16     | 16     | 24    | 8              |              |                                 |       |                | 64           |
| 40            | 32              | 32M             | 16     | 16     | 8     | 0              | 16           | 16                              |       |                | 72           |
| 40            | 40              | 32M             | 16     | 16     | 8     | 8              | 16           | 16                              |       |                | 80           |
| 40            | 40              | 80M             | 40     | 40     |       |                |              |                                 |       |                | 80           |
| 40            | 56              | 80M             | 40     | 40     | 0     | 16             |              |                                 |       |                | 96           |
| 40            | 72              | 80M             | 40     | 40     | 0     | 32             |              |                                 |       |                | 112          |
| 40            | 88              | 80M             | 40     | 40     | 0     | 48             |              |                                 |       |                | 128          |
| 48            | 16              | 32M             | 16     | 16     | 32    | 0              |              |                                 |       |                | 64           |
| 48            | 32              | 32M             | 16     | 16     | 16    | 0              | 16           | 16                              |       |                | 80           |
| 48            | 32              | 64M             | 32     | 32     | 16    | 0              |              |                                 |       |                | 80           |
| 48            | 48              | 32M             | 16     | 16     | 16    | 16             | 16           | 16                              |       |                | 96           |
| 48            | 48              | 64M             | 32     | 32     | 16    | 16             |              |                                 |       |                | 96           |
| 48            | 48              | 64M             | 32     | 32     | 0     | 0              | 16           | 16                              |       |                | 96           |
| 48            | 64              | 64M             | 32     | 32     | 16    | 32             |              |                                 |       |                | 112          |
| 48            | 64              | 64M             | 32     | 32     | 0     | 16             | 16           | 16                              |       |                | 112          |
| 48            | 80              | 64M             | 32     | 32     | 0     | 32             | 16           | 16                              |       |                | 128          |
| 48            | 96              | 64M             | 32     | 32     | 0     | 48             | 16           | 16                              |       |                | 144          |

|       | ber of<br>points | CPL             | J modi | ıle    |       | output<br>dule | input/<br>mo | vered<br>output<br>dule<br>-32E |       | output<br>dule | I/O<br>total |
|-------|------------------|-----------------|--------|--------|-------|----------------|--------------|---------------------------------|-------|----------------|--------------|
| Input | Output           | Module<br>model | Input  | Output | Input | Output         | Input        | Output                          | Input | Output         |              |
| 56    | 32               | 32M             | 16     | 16     | 24    | 0              | 16           | 16                              |       |                | 88           |
| 56    | 40               | 32M             | 16     | 16     | 24    | 8              | 16           | 16                              |       |                | 96           |
| 56    | 40               | 80M             | 40     | 40     | 16    | 0              |              |                                 |       |                | 96           |
| 56    | 56               | 80M             | 40     | 40     | 16    | 16             |              |                                 |       |                | 112          |
| 56    | 56               | 80M             | 40     | 40     | 0     | 0              | 16           | 16                              |       |                | 112          |
| 56    | 72               | 80M             | 40     | 40     | 16    | 32             |              |                                 |       |                | 128          |
| 56    | 72               | 80M             | 40     | 40     | 0     | 16             | 16           | 16                              |       |                | 128          |
| 56    | 88               | 80M             | 40     | 40     | 0     | 32             | 16           | 16                              |       |                | 144          |
| 56    | 104              | 80M             | 40     | 40     | 0     | 48             | 16           | 16                              |       |                | 160          |
| 64    | 32               | 32M             | 16     | 16     | 32    | 0              | 16           | 16                              |       |                | 96           |
| 64    | 32               | 64M             | 32     | 32     | 32    | 0              |              |                                 |       |                | 96           |
| 64    | 48               | 32M             | 16     | 16     | 0     | 0              | 16           | 16                              | 32    | 16             | 112          |
| 64    | 48               | 64M             | 32     | 32     | 16    | 0              | 16           | 16                              |       |                | 112          |
| 64    | 48               | 64M             | 32     | 32     | 32    | 16             |              |                                 |       |                | 112          |
| 64    | 56               | 32M             | 16     | 16     | 0     | 8              | 16           | 16                              | 32    | 16             | 120          |
| 64    | 56               | 64M             | 32     | 32     | 32    | 24             |              |                                 |       |                | 120          |
| 64    | 64               | 32M             | 16     | 16     | 0     | 16             | 16           | 16                              | 32    | 16             | 128          |
| 64    | 64               | 64M             | 32     | 32     | 16    | 16             | 16           | 16                              |       |                | 128          |
| 64    | 72               | 32M             | 16     | 16     | 0     | 24             | 16           | 16                              | 32    | 16             | 136          |
| 64    | 80               | 64M             | 32     | 32     | 16    | 32             | 16           | 16                              |       |                | 144          |
| 72    | 40               | 80M             | 40     | 40     | 32    | 0              |              |                                 |       |                | 112          |
| 72    | 48               | 32M             | 16     | 16     | 8     | 0              | 16           | 16                              | 32    | 16             | 120          |
| 72    | 56               | 32M             | 16     | 16     | 8     | 8              | 16           | 16                              | 32    | 16             | 128          |
| 72    | 56               | 80M             | 40     | 40     | 32    | 16             |              |                                 |       |                | 128          |
| 72    | 56               | 80M             | 40     | 40     | 16    | 0              | 16           | 16                              |       |                | 128          |
| 72    | 64               | 80M             | 40     | 40     | 32    | 24             |              |                                 |       |                | 136          |
| 72    | 72               | 80M             | 40     | 40     | 16    | 16             | 16           | 16                              |       |                | 144          |
| 72    | 88               | 80M             | 40     | 40     | 16    | 32             | 16           | 16                              |       |                | 160          |
| 80    | 32               | 64M             | 32     | 32     | 48    | 0              |              |                                 |       |                | 112          |
| 80    | 48               | 32M             | 16     | 16     | 16    | 0              | 16           | 16                              | 32    | 16             | 128          |
| 80    | 48               | 64M             | 32     | 32     | 48    | 16             |              |                                 |       |                | 128          |
| 80    | 48               | 64M             | 32     | 32     | 32    | 0              | 16           | 16                              |       |                | 128          |
| 80    | 64               | 32M             | 16     | 16     | 16    | 16             | 16           | 16                              | 32    | 16             | 144          |
| 80    | 64               | 64M             | 32     | 32     | 32    | 16             | 16           | 16                              |       |                | 144          |
| 80    | 72               | 64M             | 32     | 32     | 32    | 24             | 16           | 16                              |       |                | 152          |
| 80    | 80               | 64M             | 32     | 32     | 0     | 16             | 16           | 16                              | 32    | 16             | 160          |
| 80    | 96               | 64M             | 32     | 32     | 0     | 32             | 16           | 16                              | 32    | 16             | 176          |
| 80    | 112              | 64M             | 32     | 32     | 0     | 48             | 16           | 16                              | 32    | 16             | 192          |

# MELSEC iQ-F

|       | ber of<br>ioints | CPI             | J modi | le     |       | output<br>dule | input/<br>mo | vered<br>output<br>dule<br>-32E |       | output<br>dule | I/O<br>total |
|-------|------------------|-----------------|--------|--------|-------|----------------|--------------|---------------------------------|-------|----------------|--------------|
| Input | Output           | Module<br>model | Input  | Output | Input |                | Input        | Output                          | Input | Output         |              |
| 144   | 64               | 64M             | 32     | 32     | 64    | 0              | 16           | 16                              | 32    | 16             | 208          |
| 144   | 72               | 64M             | 32     | 32     | 64    | 0              | 16           | 16                              | 32    | 24             | 216          |
| 144   | 80               | 64M             | 32     | 32     | 64    | 0              | 16           | 16                              | 32    | 32             | 224          |
| 152   | 72               | 80M             | 40     | 40     | 64    | 0              | 16           | 16                              | 32    | 16             | 224          |
| 152   | 80               | 80M             | 40     | 40     | 64    | 0              | 16           | 16                              | 32    | 24             | 232          |

| Numl<br>I/O p | cer of<br>ioints | CPI             | J modi | ıle    |       | output<br>dule | input/<br>mo | rered<br>output<br>dule<br>-32E |       | output<br>dule | I/O<br>total |
|---------------|------------------|-----------------|--------|--------|-------|----------------|--------------|---------------------------------|-------|----------------|--------------|
| Input         | Output           | Module<br>model | Input  | Output | Input | Output         | Input        | Output                          | Input | Output         |              |
| 88            | 40               | 80M             | 40     | 40     | 48    | 0              |              |                                 |       |                | 128          |
| 88            | 48               | 32M             | 16     | 16     | 24    | 0              | 16           | 16                              | 32    | 16             | 136          |
| 88            | 56               | 32M             | 16     | 16     | 24    | 8              | 16           | 16                              | 32    | 16             | 144          |
| 88            | 56               | 80M             | 40     | 40     | 48    | 16             |              |                                 |       |                | 144          |
| 88            | 56               | 80M             | 40     | 40     | 32    | 0              | 16           | 16                              |       |                | 144          |
| 88            | 64               | 32M             | 16     | 16     | 24    | 8              | 16           | 16                              | 32    | 24             | 152          |
| 88            | 72               | 80M             | 40     | 40     | 32    | 16             | 16           | 16                              |       |                | 160          |
| 88            | 80               | 80M             | 40     | 40     | 32    | 24             | 16           | 16                              |       |                | 168          |
| 88            | 88               | 80M             | 40     | 40     | 0     | 16             | 16           | 16                              | 32    | 16             | 176          |
| 88            | 104              | 80M             | 40     | 40     | 0     | 32             | 16           | 16                              | 32    | 16             | 192          |
| 88            | 120              | 80M             | 40     | 40     | 0     | 48             | 16           | 16                              | 32    | 16             | 208          |
| 96            | 32               | 64M             | 32     | 32     | 64    | 0              |              |                                 |       |                | 128          |
| 96            | 48               | 32M             | 16     | 16     | 32    | 0              | 16           | 16                              | 32    | 16             | 144          |
| 96            | 48               | 64M             | 32     | 32     | 48    | 0              | 16           | 16                              |       |                | 144          |
| 96            | 56               | 32M             | 16     | 16     | 32    | 0              | 16           | 16                              | 32    | 24             | 152          |
| 96            | 64               | 64M             | 32     | 32     | 48    | 16             | 16           | 16                              |       |                | 160          |
| 96            | 64               | 64M             | 32     | 32     | 16    | 0              | 16           | 16                              | 32    | 16             | 160          |
| 96            | 80               | 64M             | 32     | 32     | 16    | 16             | 16           | 16                              | 32    | 16             | 176          |
| 96            | 96               | 64M             | 32     | 32     | 16    | 32             | 16           | 16                              | 32    | 16             | 192          |
| 104           | 40               | 80M             | 40     | 40     | 64    | 0              |              |                                 |       |                | 144          |
| 104           | 56               | 80M             | 40     | 40     | 48    | 0              | 16           | 16                              |       |                | 160          |
| 104           | 72               | 80M             | 40     | 40     | 48    | 16             | 16           | 16                              |       |                | 176          |
| 104           | 72               | 80M             | 40     | 40     | 16    | 0              | 16           | 16                              | 32    | 16             | 176          |
| 104           | 88               | 80M             | 40     | 40     | 16    | 16             | 16           | 16                              | 32    | 16             | 192          |
| 104           | 104              | 80M             | 40     | 40     | 16    | 32             | 16           | 16                              | 32    | 16             | 208          |
| 112           | 48               | 64M             | 32     | 32     | 64    | 0              | 16           | 16                              |       |                | 160          |
| 112           | 64               | 64M             | 32     | 32     | 32    | 0              | 16           | 16                              | 32    | 16             | 176          |
| 112           | 80               | 64M             | 32     | 32     | 32    | 16             | 16           | 16                              | 32    | 16             | 192          |
| 112           | 88               | 64M             | 32     | 32     | 32    | 24             | 16           | 16                              | 32    | 16             | 200          |
| 120           | 56               | 80M             | 40     | 40     | 64    | 0              | 16           | 16                              |       |                | 176          |
| 120           | 72               | 80M             | 40     | 40     | 32    | 0              | 16           | 16                              | 32    | 16             | 192          |
| 120           | 88               | 80M             | 40     | 40     | 32    | 16             | 16           | 16                              | 32    | 16             | 208          |
| 120           | 96               | 80M             | 40     | 40     | 32    | 24             | 16           | 16                              | 32    | 16             | 216          |
| 128           | 64               | 64M             | 32     | 32     | 48    | 0              | 16           | 16                              | 32    | 16             | 192          |
| 128           | 80               | 64M             | 32     | 32     | 48    | 16             | 16           | 16                              | 32    | 16             | 208          |
| 128           | 88               | 64M             | 32     | 32     | 48    | 16             | 16           | 16                              | 32    | 24             | 216          |
| 136           | 72               | 80M             | 40     | 40     | 48    | 0              | 16           | 16                              | 32    | 16             | 208          |
| 136           | 88               | 80M             | 40     | 40     | 48    | 16             | 16           | 16                              | 32    | 16             | 224          |
| 136           | 96               | 80M             | 40     | 40     | 48    | 16             | 16           | 16                              | 32    | 24             | 232          |

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# Examples of combinations of FX5UC inputs/outputs



The table below shows examples of combinations of FX5UC extension modules. The contents of combinations can be described based on the number of input points.

• In addition to the combinations shown below, various combinations can be made by changing selected I/O modules and extension modules.

|       | ber of<br>points | CP              | U modı | ule    |    | output<br>dule | Connector conversion |       | output<br>dule | I/O   |
|-------|------------------|-----------------|--------|--------|----|----------------|----------------------|-------|----------------|-------|
| Input | Output           | Module<br>model |        | Output |    | Output         | module               | Input | Output         | total |
| 16    | 16               | 32M             | 16     | 16     | 0  | 0              |                      |       |                | 32    |
| 16    | 24               | 32M             | 16     | 16     | 0  | 0              | •                    |       | 8              | 40    |
| 16    | 32               | 32M             | 16     | 16     | 0  | 16             |                      |       |                | 48    |
| 16    | 48               | 32M             | 16     | 16     | 0  | 32             |                      |       |                | 64    |
| 24    | 16               | 32M             | 16     | 16     | 0  | 0              | •                    | 8     |                | 40    |
| 24    | 48               | 32M             | 16     | 16     | 0  | 32             | •                    | 8     |                | 72    |
| 24    | 64               | 32M             | 16     | 16     | 0  | 48             | •                    | 8     |                | 88    |
| 24    | 80               | 32M             | 16     | 16     | 0  | 64             | •                    | 8     |                | 104   |
| 32    | 16               | 32M             | 16     | 16     | 16 | 0              |                      |       |                | 48    |
| 32    | 32               | 32M             | 16     | 16     | 16 | 16             |                      |       |                | 64    |
| 32    | 32               | 64M             | 32     | 32     | 0  | 0              |                      |       |                | 64    |
| 32    | 48               | 32M             | 16     | 16     | 16 | 32             |                      |       |                | 80    |
| 32    | 48               | 64M             | 32     | 32     | 0  | 16             |                      |       |                | 80    |
| 32    | 64               | 64M             | 32     | 32     | 0  | 32             |                      |       |                | 96    |
| 32    | 72               | 32M             | 16     | 16     | 16 | 48             | •                    |       | 8              | 104   |
| 32    | 80               | 64M             | 32     | 32     | 0  | 48             |                      |       |                | 112   |
| 40    | 16               | 32M             | 16     | 16     | 16 | 0              | •                    | 8     |                | 56    |
| 40    | 32               | 32M             | 16     | 16     | 16 | 16             | •                    | 8     |                | 72    |
| 40    | 32               | 64M             | 32     | 32     | 0  | 0              | •                    | 8     |                | 72    |
| 40    | 48               | 32M             | 16     | 16     | 16 | 32             | •                    | 8     |                | 88    |
| 40    | 64               | 64M             | 32     | 32     | 0  | 32             | •                    | 8     |                | 104   |
| 48    | 16               | 32M             | 16     | 16     | 32 | 0              |                      |       |                | 64    |
| 48    | 32               | 64M             | 32     | 32     | 16 | 0              |                      |       |                | 80    |
| 48    | 32               | 32M             | 16     | 16     | 32 | 16             |                      |       |                | 80    |
| 48    | 48               | 32M             | 16     | 16     | 32 | 32             |                      |       |                | 96    |
| 48    | 48               | 64M             | 32     | 32     | 16 | 16             |                      |       |                | 96    |
| 48    | 48               | 96M             | 48     | 48     | 0  | 0              |                      |       |                | 96    |
| 48    | 64               | 96M             | 48     | 48     | 0  | 16             |                      |       |                | 112   |
| 48    | 64               | 64M             | 32     | 32     | 16 | 32             |                      |       |                | 112   |
| 48    | 80               | 96M             | 48     | 48     | 0  | 32             |                      |       |                | 128   |
| 56    | 32               | 32M             | 16     | 16     | 32 | 16             | •                    | 8     |                | 88    |
| 56    | 48               | 32M             | 16     | 16     | 32 | 32             | •                    | 8     |                | 104   |
| 56    | 48               | 64M             | 32     | 32     | 16 | 16             | •                    | 8     |                | 104   |
| 56    | 48               | 96M             | 48     | 48     | 0  | 0              | ٠                    | 8     |                | 104   |
| 56    | 64               | 32M             | 16     | 16     | 32 | 48             | •                    | 8     |                | 120   |
| 56    | 64               | 64M             | 32     | 32     | 16 | 32             | •                    | 8     |                | 120   |
| 56    | 64               | 96M             | 48     | 48     | 0  | 16             | •                    | 8     |                | 120   |
| 56    | 80               | 64M             | 32     | 32     | 16 | 48             | •                    | 8     |                | 136   |
| 56    | 96               | 96M             | 48     | 48     | 0  | 48             | •                    | 8     |                | 152   |
| 64    | 32               | 32M             | 16     | 16     | 48 | 16             |                      |       |                | 96    |
| 64    | 48               | 64M             | 32     | 32     | 32 | 16             |                      |       |                | 112   |
| 64    | 64               | 32M             | 16     | 16     | 48 | 48             |                      |       |                | 128   |
| 64    | 64               | 96M             | 48     | 48     | 16 | 16             |                      |       |                | 128   |
| 64    | 80               | 64M             | 32     | 32     | 32 | 48             |                      |       |                | 144   |
| 64    | 96               | 96M             | 48     | 48     | 16 | 48             |                      |       |                | 160   |

|     |                 |                 |        |        |    |                |                         | _ |                |       |
|-----|-----------------|-----------------|--------|--------|----|----------------|-------------------------|---|----------------|-------|
|     | ber of<br>oints | CP              | U modi | ule    |    | output<br>dule | Connector<br>conversion |   | output<br>dule | I/O   |
|     | Output          | Module<br>model | Input  | Output |    | Output         |                         |   | Output         | total |
| 72  | 32              | 32M             | 16     | 16     | 48 | 16             | •                       | 8 |                | 104   |
| 72  | 48              | 64M             | 32     | 32     | 32 | 16             | •                       | 8 |                | 120   |
| 72  | 64              | 32M             | 16     | 16     | 48 | 48             | •                       | 8 |                | 136   |
| 72  | 64              | 96M             | 48     | 48     | 16 | 16             | •                       | 8 |                | 136   |
| 72  | 64              | 64M             | 32     | 32     | 32 | 32             | •                       | 8 |                | 136   |
| 72  | 80              | 32M             | 16     | 16     | 48 | 64             | ٠                       | 8 |                | 152   |
| 72  | 80              | 64M             | 32     | 32     | 32 | 48             | •                       | 8 |                | 152   |
| 72  | 96              | 96M             | 48     | 48     | 16 | 48             | ٠                       | 8 |                | 168   |
| 80  | 32              | 64M             | 32     | 32     | 48 | 0              |                         |   |                | 112   |
| 80  | 48              | 64M             | 32     | 32     | 48 | 16             |                         |   |                | 128   |
| 80  | 48              | 32M             | 16     | 16     | 64 | 32             |                         |   |                | 128   |
| 80  | 64              | 32M             | 16     | 16     | 64 | 48             |                         |   |                | 144   |
| 80  | 64              | 96M             | 48     | 48     | 32 | 16             |                         |   |                | 144   |
| 80  | 80              | 64M             | 32     | 32     | 48 | 48             |                         |   |                | 160   |
| 80  | 80              | 32M             | 16     | 16     | 64 | 64             |                         |   |                | 160   |
| 80  | 96              | 64M             | 32     | 32     | 48 | 64             |                         |   |                | 176   |
| 80  | 96              | 96M             | 48     | 48     | 32 | 48             |                         |   |                | 176   |
| 88  | 48              | 32M             | 16     | 16     | 64 | 32             | •                       | 8 |                | 136   |
| 88  | 48              | 64M             | 32     | 32     | 48 | 16             | •                       | 8 |                | 136   |
| 88  | 64              | 96M             | 48     | 48     | 32 | 16             | •                       | 8 |                | 152   |
| 88  | 64              | 32M             | 16     | 16     | 64 | 48             | •                       | 8 |                | 152   |
| 88  | 80              | 64M             | 32     | 32     | 48 | 48             | •                       | 8 |                | 168   |
| 88  | 80              | 96M             | 48     | 48     | 32 | 32             | •                       | 8 |                | 168   |
| 88  | 96              | 64M             | 32     | 32     | 48 | 64             | •                       | 8 |                | 184   |
| 88  | 112             | 64M             | 32     | 32     | 48 | 80             | •                       | 8 |                | 200   |
| 88  | 112             | 96M             | 48     | 48     | 32 | 64             | •                       | 8 |                | 200   |
| 88  | 128             | 96M             | 48     | 48     | 32 | 80             | •                       | 8 |                | 216   |
| 96  | 32              | 64M             | 32     | 32     | 64 | 0              |                         |   |                | 128   |
| 96  | 48              | 96M             | 48     | 48     | 48 | 0              |                         |   |                | 144   |
| 96  | 48              | 32M             | 16     | 16     | 80 | 32             |                         |   |                | 144   |
| 96  | 64              | 32M             | 16     | 16     | 80 | 48             |                         |   |                | 160   |
| 96  | 80              | 64M             | 32     | 32     | 64 | 48             |                         |   |                | 176   |
| 96  | 96              | 32M             | 16     | 16     | 80 | 80             |                         |   |                | 192   |
| 96  | 112             | 64M             | 32     | 32     | 64 | 80             |                         |   |                | 208   |
| 96  | 112             | 96M             | 48     | 48     | 48 | 64             |                         |   |                | 208   |
| 96  | 128             | 96M             | 48     | 48     | 48 | 80             |                         |   |                | 224   |
| 96  | 144             | 96M             | 48     | 48     | 48 | 96             |                         |   |                | 240   |
| 104 | 32              | 32M             | 16     | 16     | 80 | 16             | •                       | 8 |                | 136   |
| 104 | 48              | 96M             | 48     | 48     | 48 | 0              | •                       | 8 |                | 152   |
| 104 | 48              | 32M             | 16     | 16     | 80 | 32             | •                       | 8 |                | 152   |
| 104 | 48              | 64M             | 32     | 32     | 64 | 16             | •                       | 8 |                | 152   |
| 104 | 64              | 32M             | 16     | 16     | 80 | 48             | •                       | 8 |                | 168   |
| 104 | 64              | 64M             | 32     | 32     | 64 | 32             | •                       | 8 |                | 168   |
| 104 | 96              | 64M             | 32     | 32     | 64 | 64             | •                       | 8 |                | 200   |
| 104 | 112             | 96M             | 48     | 48     | 48 | 64             | •                       | 8 |                | 216   |
| 104 | 112             | 64M             | 32     | 32     | 64 | 80             | •                       | 8 |                | 216   |
| 104 | 128             | 96M             | 48     | 48     | 48 | 80             | •                       | 8 |                | 232   |
| -0+ | 1 20            | 0000            |        |        |    | 00             | -                       | 0 |                | 202   |

# MELSEC iQ-F

| Numl<br>I/O p | ber of<br>points | CP              | U modi | lle    |     | output<br>dule | Connector<br>conversion |       | output<br>dule | I/O   |
|---------------|------------------|-----------------|--------|--------|-----|----------------|-------------------------|-------|----------------|-------|
|               | Output           | Module<br>model |        | Output |     | Output         | module                  | Input | Output         | total |
| 112           | 64               | 64M             | 32     | 32     | 80  | 32             |                         |       |                | 176   |
| 112           | 80               | 96M             | 48     | 48     | 64  | 32             |                         |       |                | 192   |
| 112           | 96               | 32M             | 16     | 16     | 96  | 80             |                         |       |                | 208   |
| 112           | 112              | 64M             | 32     | 32     | 80  | 80             |                         |       |                | 224   |
| 112           | 112              | 96M             | 48     | 48     | 64  | 64             |                         |       |                | 224   |
| 112           | 128              | 32M             | 16     | 16     | 96  | 112            |                         |       |                | 240   |
| 112           | 128              | 64M             | 32     | 32     | 80  | 96             |                         |       |                | 240   |
| 112           | 144              | 96M             | 48     | 48     | 64  | 96             |                         |       |                | 256   |
| 120           | 64               | 32M             | 16     | 16     | 96  | 48             | •                       | 8     |                | 184   |
| 120           | 80               | 64M             | 32     | 32     | 80  | 48             | ٠                       | 8     |                | 200   |
| 120           | 96               | 96M             | 48     | 48     | 64  | 48             | •                       | 8     |                | 216   |
| 120           | 112              | 32M             | 16     | 16     | 96  | 96             | •                       | 8     |                | 232   |
| 120           | 112              | 64M             | 32     | 32     | 80  | 80             | •                       | 8     |                | 232   |
| 120           | 128              | 96M             | 48     | 48     | 64  | 80             | •                       | 8     |                | 248   |
| 120           | 128              | 64M             | 32     | 32     | 80  | 96             | •                       | 8     |                | 248   |
| 120           | 136              | 96M             | 48     | 48     | 64  | 80             | •                       | 8     | 8              | 256   |
| 128           | 64               | 32M             | 16     | 16     | 112 | 48             |                         |       |                | 192   |
| 128           | 96               | 96M             | 48     | 48     | 80  | 48             |                         |       |                | 224   |
| 128           | 96               | 32M             | 16     | 16     | 112 | 80             |                         |       |                | 224   |
| 128           | 96               | 64M             | 32     | 32     | 96  | 64             |                         |       |                | 224   |
| 128           | 112              | 96M             | 48     | 48     | 80  | 64             |                         |       |                | 240   |
| 128           | 112              | 64M             | 32     | 32     | 96  | 80             |                         |       |                | 240   |
| 128           | 128              | 96M             | 48     | 48     | 80  | 80             |                         |       |                | 256   |
| 136           | 48               | 32M             | 16     | 16     | 112 | 32             | •                       | 8     |                | 184   |
| 136           | 80               | 64M             | 32     | 32     | 96  | 48             | •                       | 8     |                | 216   |
| 136           | 96               | 96M             | 48     | 48     | 80  | 48             | •                       | 8     |                | 232   |
| 136           | 96               | 64M             | 32     | 32     | 96  | 64             | •                       | 8     |                | 232   |
| 136           | 112              | 64M             | 32     | 32     | 96  | 80             | •                       | 8     |                | 248   |
| 136           | 120              | 96M             | 48     | 48     | 80  | 64             | •                       | 8     | 8              | 256   |
| 144           | 64               | 32M             | 16     | 16     | 128 | 48             |                         |       |                | 208   |
| 144           | 80               | 64M             | 32     | 32     | 112 | 48             |                         |       |                | 224   |
| 144           | 96               | 96M             | 48     | 48     | 96  | 48             |                         |       |                | 240   |
| 144           | 112              | 64M             | 32     | 32     | 112 | 80             |                         |       |                | 256   |
| 144           | 112              | 96M             | 48     | 48     | 96  | 64             |                         |       |                | 256   |
| 152           | 64               | 32M             | 16     | 16     | 128 | 48             | •                       | 8     |                | 216   |
| 152           | 64               | 64M             | 32     | 32     | 112 | 32             | •                       | 8     |                | 216   |
| 152           | 96               | 96M             | 48     | 48     | 96  | 48             | •                       | 8     |                | 248   |
| 152           | 96               | 64M             | 32     | 32     | 112 | 64             | •                       | 8     |                | 248   |
| 152           | 104              | 96M             | 48     | 48     | 96  | 48             | •                       | 8     | 8              | 256   |
| 160           | 64               | 64M             | 32     | 32     | 128 | 32             |                         |       |                | 224   |
| 160           | 80               | 96M             | 48     | 48     | 112 | 32             |                         |       |                | 240   |
| 160           | 96               | 64M             | 32     | 32     | 128 | 64             |                         |       |                | 256   |
| 160           | 96               | 96M             | 48     | 48     | 112 | 48             |                         |       |                | 256   |
| 168           | 64               | 64M             | 32     | 32     | 128 | 32             | •                       | 8     |                | 232   |
| 168           | 80               | 96M             | 48     | 48     | 112 | 32             | •                       | 8     |                | 248   |
| 168           | 80               | 64M             | 32     | 32     | 128 | 48             | •                       | 8     |                | 248   |
| 168           | 88               | 96M             | 48     | 48     | 112 | 32             | •                       | 8     | 8              | 256   |

|       | oer of<br>oints | CP              | U modı | ule    |     | output<br>dule | Connector | Input/output<br>module |        | I/O   |
|-------|-----------------|-----------------|--------|--------|-----|----------------|-----------|------------------------|--------|-------|
| Input | Output          | Module<br>model | Input  | Output |     | Output         |           |                        | Output | total |
| 176   | 64              | 64M             | 32     | 32     | 144 | 32             |           |                        |        | 240   |
| 176   | 64              | 96M             | 48     | 48     | 128 | 16             |           |                        |        | 240   |
| 176   | 80              | 64M             | 32     | 32     | 144 | 48             |           |                        |        | 256   |
| 184   | 64              | 96M             | 48     | 48     | 128 | 16             | ٠         | 8                      |        | 248   |
| 184   | 64              | 64M             | 32     | 32     | 144 | 32             | ٠         | 8                      |        | 248   |
| 184   | 72              | 96M             | 48     | 48     | 128 | 16             | ٠         | 8                      | 8      | 256   |
| 192   | 48              | 64M             | 32     | 32     | 160 | 16             |           |                        |        | 240   |
| 192   | 56              | 96M             | 48     | 48     | 144 | 0              | •         |                        | 8      | 248   |
| 192   | 64              | 96M             | 48     | 48     | 144 | 16             |           |                        |        | 256   |
| 200   | 32              | 64M             | 32     | 32     | 160 | 0              | •         | 8                      |        | 232   |
| 200   | 48              | 96M             | 48     | 48     | 144 | 0              | ٠         | 8                      |        | 248   |
| 200   | 56              | 96M             | 48     | 48     | 144 | 0              | ٠         | 8                      | 8      | 256   |
| 208   | 48              | 96M             | 48     | 48     | 160 | 0              |           |                        |        | 256   |

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I/O Module

memo

# Input/output devices for voltage and current

Analog input/output devices can be used to input and output analog amount of voltage, current, etc. Analog control essential for FA control can easily be implemented by the PLC. (For supporting micro voltage input of 0 to 10 mV DC, 0 to 100 mV DC, refer to FX5-4LC for "input device for temperature sensor".)

# List of analog input/output devices

# Analog input expansion adapter (A/D conversion)

| Model                |      | Input specifica                                    | tions                                      | Isolation  | Compatible CPU module |       | Analog<br>input    |
|----------------------|------|--|--|--|-----------------------|-------|--------------------|
| (Number of channels) | Item | Input current                                      | Input voltage                              |  | FX5U                  | FX5UC | points             |
| FX5-4AD-ADP (4 ch)   |      | -20 to +20 mA DC<br>(Input resistance 250 Ω)       | -10 to +10 V DC<br>(Input resistance 1 MΩ) | Between input terminal and PLC:                                    |                       |       |                    |
| 1                    |      | $1.25 \ \mu\text{A} (0 \text{ to } 20 \text{ mA})$ | 625 μV (0 to 10 V)<br>312.5 μV (0 to 5 V)  | Photocoupler isolation<br>Between input channels:<br>Non-isolation | 0                     | 0     | 4 points<br>(4 ch) |

# ◇ Analog output expansion adapter (D/A conversion)

| Model                |                 | Output specifica  | ations                    | Isolation   | Compatible CPU module |       | Analog<br>output   |
|----------------------|-----------------|---|---------------------------|---|-----------------------|-------|--------------------|
| (Number of channels) | Items           | Output current  | Output voltage            |   | FX5U                  | FX5UC | points             |
| FX5-4DA-ADP (4 ch)   | Output<br>range | 0 to 20 mA DC<br>(External load resistance value 0 to<br>500 Ω) | $\frac{1}{1}$ kO to 1 MO) | Between output terminal and PLC:                                    |                       |       | 4 nointe           |
| 1                    | Resolution      |   | 625 µV (0 to 10 V)        | Photocoupler isolation<br>Between output channels:<br>Non-isolation | 0                     | 0     | 4 points<br>(4 ch) |

# ◇ Analog input module (A/D conversion)

| Model   |   | Input specifica  | tions  | Isolation   | Compatible CPU<br>module |                    | Analog<br>input    |
|---|---|--|--|---|--------------------------|--------------------|--------------------|
| (Number of channels)  | Items   | Input current  | Input voltage  |   | FX5U                     | FX5UC              | points             |
| FX5-4AD (4 ch)  | Input<br>range  | -20 to +20 mA DC<br>(Input resistance 250 Ω)                         | -10 to +10 V DC<br>(Input resistance 400 kΩ or more)   | Between input terminal and PLC:   |                          |                    |                    |
|   | $ \begin{array}{ c c c c c } \hline & & & & & & & & & & & & & & & & & & $ |  | Photocoupler isolation<br>Between input terminal channels:                                   | 0   | O*2                      | 4 points<br>(4 ch) |                    |
| FX5-8AD (8 ch)  | Input<br>range  | -20 to +20 mA DC<br>(Input resistance 250 Ω)                         | -10 to +10 V DC<br>(Input resistance 1 MΩ)   | Between input terminal and PLC:   |                          |                    |                    |
| F III   | Resolution  | 625 nA (0 to 20 mA)<br>500 nA (4 to 20 mA)<br>625 nA (-20 to +20 mA) | 312. 5 μV (0 to10 V)<br>156.25 μV (0 to 5 V)<br>125 μV (1 to 5 V)<br>312.5 μV (-10 to +10 V) | Photocoupler isolation<br>Between input terminal channels:<br>Non-isolation | 0                        | 0*2                | 8 points<br>(8 ch) |
| FX3U-4AD (4 ch)   | Input<br>range  | -20 to +20 mA DC, 4 to 20 mA DC<br>(Input resistance 250 Ω)          | -10 to +10 V DC<br>(Input resistance 200 kΩ)   | Between input terminal and PLC:<br>Photocoupler isolation                   | 0*3                      | 0*3                | 4 points           |
| the second se | Resolution 1.25 µA (-20 to +20 mA)  |  | 0.32 mV (-10 to +10 V)   | Between input channels:<br>Non-isolation                                    |                          | 0***               | (4 ch)             |

\*1: Maximum resolution in the user range setting. \*2: Connection with FX5UC requires FX5-CNV-IFC or FX5-C1PS-5V.

\*3: Connection with FX5U or FX5UC requires FX5-CNV-BUS or FX5-CNV-BUSC.

# ♦ Analog output module (D/A conversion)

| Model<br>(Number of channels) |                 | Output specifica   | ations   | Isolation   | Compatible CPU<br>module |       | Analog<br>output   |
|-------------------------------|-----------------|--|--|---|--------------------------|-------|--------------------|
|                               | Items           | Output current   | Output voltage   |   | FX5U                     | FX5UC | points             |
| FX5-4DA (4 ch)                | Output<br>range | 0 to 20 mA DC (External load resistance value 0 to 500 $\Omega$ )                  | -10 to +10 V DC<br>(External load resistance value<br>1 k $\Omega$ to1 M $\Omega$ )  | Between output terminal and PLC:                                    |                          |       |                    |
|                               | Resolution      | 625 nA (0 to 20 mA)<br>500 nA (4 to 20 mA)<br>500 nA*1 (User range setting)        | 312. 5 µV (0 to 10 V)<br>156.25 µV (0 to 5 V)<br>125 µV (1 to 5 V)<br>312.5 µV (-10 to +10 V)<br>312.5 µV*' (User range setting) | Photocoupler isolation<br>Between output channels:<br>Non-isolation | 0                        | 0*2   | 4 points<br>(4 ch) |
| FX3U-4DA (4 ch)               | Output<br>range | 0 to 20 mA DC, 4 to 20 mA DC (External load resistance value $500 \Omega$ or less) | -10 to +10 V DC (external load resistance value 1 k $\Omega$ to 1 M $\Omega$ )   | Between output terminal and PLC:<br>Photocoupler isolation          |                          | ○*3   | 4 points           |
| the state                     | Resolution      | 0.63 µA (0 to 20 mA)   | 0.32 mV (-10 to +10 V)   | Between output channels:<br>Non-isolation                           | 0*3                      |       | (4 ch)             |

\*1: Maximum resolution in the user range setting.
 \*2: Connection with FX5UC requires FX5-CNV-IFC or FX5-C1PS-5V.
 \*3: Connection with FX5U or FX5UC requires FX5-CNV-BUS or FX5-CNV-BUSC.

# ◇ FX5U CPU module

Built-in analog input

| Model (Number of       | Inp            | ut specifications                              | Isolation  |  |
|------------------------|----------------|--|--|--|
| channels)              | Items          | Input voltage                                  |  |  |
| FX5U CPU module (2 ch) | Input<br>range | 0 to 10 V DC<br>(Input resistance<br>115.7 kΩ) | Between analog input circuit<br>and PLC circuit:<br>No isolation |  |
|                        | Resolution     | 2.5 mV   | Between input channels:<br>No isolation                          |  |

#### Built-in analog output

| Model (Number of       | Out             | put specifications   | Isolation                                     |
|------------------------|-----------------|--|---|
| channels)              | Items           | Output voltage   | ISUIdliUiT                                    |
| FX5U CPU module (1 ch) | Output<br>range | 0 to 10 V DC<br>(External load resistance<br>value 2 kΩ to 1 MΩ) | Between analog input circuit and PLC circuit: |
|                        | Resolution      | 2.5 mV   | No isolation                                  |

# FX5-4AD-ADP type expansion adapter

## ○ Features



- High-precision analog input adapter with resolution of 14 bits binary.
- 2) 4-channel voltage input
  (-10 to +10 V DC) or current input
  (-20 to +20 mA DC) is allowed.
- 3) Voltage or current input can be specified for each channel.
- Data can be transferred programless (no dedicated instructions).

# ♦ Specifications

| Items   | Specifications    |   |  |            |  |  |  |
|---|-------------------|---|--|------------|--|--|--|
| Analog input points   | 4 points          | (4 channels)  |  |            |  |  |  |
| Analog input voltage  | -10 to +1         | 0 V DC (input resistance                                    | 1 MΩ)  |            |  |  |  |
| Analog input current  | -20 to +2         | 0 mA DC (input resistan                                     | ce 250 Ω)  |            |  |  |  |
| Digital output value  | 14-bit bir        | nary value  |  |            |  |  |  |
|   | A                 | nalog input range   | Digital output value   | Resolution |  |  |  |
|   |                   | 0 to 10 V   | 0 to 16000   | 625 µV     |  |  |  |
|   | Voltage           | 0 to 5 V  | 0 to 16000   | 312.5 µV   |  |  |  |
| Input characteristics,  | vollage           | 1 to 5 V  | 0 to 12800   | 312.5 µV   |  |  |  |
| resolution*1  |                   | -10 to +10 V  | -8000 to +8000   | 1250 μV    |  |  |  |
|   |                   | 0 to 20 mA  | 0 to 16000   | 1.25 µA    |  |  |  |
|   | Current           | 4 to 20 mA  | 0 to 12800   | 1.25 µA    |  |  |  |
|   |                   | -20 to +20 mA   | -8000 to +8000   | 2.5 µA     |  |  |  |
| Accuracy (Accuracy<br>in respect to full-scale<br>digital output value) | Ambient           |   | thin ±0.1% (±16 digits)<br>within ±0.2% (±32 digits)<br><sup>:2</sup> : within ±0.3% (±48 digits | s)         |  |  |  |
| Absolute maximum input  | Voltage:          | ±15 V, Current: ±30 mA                                      |  |            |  |  |  |
| Conversion speed  | Up to 45          | 0 µs (data refreshed ever                                   | y operation cycle)   |            |  |  |  |
| Isolation   |                   | input terminal and PLC:<br>input channels: No isola         |  |            |  |  |  |
| Power supply  |                   | 20 mA (internal power s<br>10 mA (internal power su         |  |            |  |  |  |
| Compatible CPU module   | FX5U, FX          | 5UC, compatible from ir                                     | iitial product   |            |  |  |  |
| Number of occupied input/output points                                  | 0 points (        | (no points occupied)  |  |            |  |  |  |
| Number of connectable modules   | FX5U, FX          | FX5U, FX5UC: Up to 4 modules to the left side of CPU module |  |            |  |  |  |
| External dimensions $W \times H \times D$ (mm)                          | 17.6 × 106 × 89.1 |   |  |            |  |  |  |
| MASS (Weight): kg   | Approx. (         | ).1   |  |            |  |  |  |

\*1: For the input conversion characteristics, refer to manuals of each product.
 \*2: Products manufactured earlier than June 2016 do not support this specification.

# FX5-4DA-ADP type expansion adapter

## ◇ Features



- High-precision analog output adapter with resolution of 14 bits binary.
- 2) 4-channel voltage output(-10 to +10 V DC) or current output(0 to 20 mA DC) is allowed.
- 3) Voltage or current output can be specified for each channel.
- 4) Data can be transferred programless (no dedicated instructions).

## ♦ Specifications

| ltems  |                     |   | Specifications                   |   |  |  |  |
|--|---------------------|---|----------------------------------|---|--|--|--|
| Analog output points   | 4 points (          | 4 channels)   |                                  |   |  |  |  |
| Digital input  | 14-bit bir          | nary value  |                                  |   |  |  |  |
| Analog output voltage  | -10 to +1           | 0 V DC (external load re                                    | sistance value 1 k $\Omega$ to 1 | ΜΩ)   |  |  |  |
| Analog output current  | 0 to 20 m           | nA DC (external load resi                                   | stance value 0 to 500 Ω          | 2)  |  |  |  |
|  | Analog output range |   | Digital value                    | Resolution  |  |  |  |
|  |                     | 0 to 10 V   | 0 to 16000                       | 625 µV  |  |  |  |
| 0 · · · · · · · · ·  | Valtaga             | 0 to 5 V  | 0 to 16000                       | 312.5 µV  |  |  |  |
| Output characteristics,<br>resolution*1                                | Voltage             | 1 to 5 V  | 0 to 16000                       | 250 µV  |  |  |  |
| 16301011011  |                     | -10 to +10 V  | -8000 to +8000                   | 1250 µV   |  |  |  |
|  | Current             | 0 to 20 mA  | 0 to 16000                       | 1.25 µA   |  |  |  |
|  |                     | 4 to 20 mA  | 0 to 16000                       | 1 µA  |  |  |  |
| Accuracy (Accuracy<br>in respect to full-scale<br>analog output value) |                     | temperature 25±5°C: wi<br>temperature -20 to 55°C           |                                  | ) mV, Current ±20 μA)<br>ge ±40 mV, Current ±40 μA) |  |  |  |
| Conversion speed   | Up to 950           | ) µs (data refreshed ever                                   | y operation cycle)               |   |  |  |  |
| Isolation  |                     | output terminal and PLC<br>output channels: No iso          |                                  | 1   |  |  |  |
| Power supply   |                     | +20%, -15% 160 mA (e:<br>10 mA (internal power su           |                                  |   |  |  |  |
| Compatible CPU module  | FX5U, FX            | 5UC, compatible from ir                                     | iitial product                   |   |  |  |  |
| Number of occupied input/output points                                 | 0 points (          | no points occupied)   |                                  |   |  |  |  |
| Number of connectable modules  | FX5U, FX            | FX5U, FX5UC: Up to 4 modules to the left side of CPU module |                                  |   |  |  |  |
| External dimensions $W \times H \times D$ (mm)                         | 17.6 × 10           | 17.6 × 106 × 89.1   |                                  |   |  |  |  |
| MASS (Weight): kg  | Approx. 0.1         |   |                                  |   |  |  |  |

\*1: For details on the output conversion characteristic, refer to manuals of each product.
 \*2: The ambient temperature specification is 0 to 55°C for products manufactured earlier than June 2016.

# FX5-4AD type analog input module

#### **○ Features**



- 1) High-precision analog input module with 312.5  $\mu$ V at voltage input and 625 nA at current input.
- 2) Spring clamp terminal block type with excellent vibration resistance.
- 3) Data of 10,000 points can be logged for each channel and saved in buffer memory. Leaving logs will be useful for analyzing the cause of trouble.

# ♦ Specifications

| Items   |   |  | Specifications  |           |  |  |  |
|---|---|--|---|-----------|--|--|--|
| Analog input points                                 | 4 noints (  | 4 channels)  | oposinoutiono   |           |  |  |  |
| Analog input voltage                                | <u> </u>  | 0 V DC (Input resistance   | 400 kΩ or more)   |           |  |  |  |
| Analog input current                                |   | 0 mA DC (Input resistance  | /   |           |  |  |  |
| Absolute maximum input                              |   | ±15 V, Current: ±30 mA   | 6 200 12)   |           |  |  |  |
| Digital output value                                | L   |  | 2767)   |           |  |  |  |
| Digital Output value                                | 16-bit signed binary (-32768 to +32767)<br>Analog input range Digital output value Resolution |  |   |           |  |  |  |
|   |   | 0 to 10 V  | 0 to 32000  | 312.5 µV  |  |  |  |
|   |   | 0 to 5 V   | 0 to 32000  | 156.25 µV |  |  |  |
|   | Voltage   | 1 to 5 V   | 0 to 32000  | 125 µV    |  |  |  |
| Input observatoriation                              | vollage   | -10 to +10 V   | -32000 to +32000  | 312.5 µV  |  |  |  |
| Input characteristics, resolution                   |   | User range setting   | -32000 to +32000  | 125 µV*   |  |  |  |
| 1000101011  |   | 0 to 20 mA   | 0 to 32000  | 625 nA    |  |  |  |
|   |   | 4 to 20 mA   | 0 to 32000  | 500 nA    |  |  |  |
|   | Current   | -20 to +20 mA  | -32000 to +32000  | 625 nA    |  |  |  |
|   |   | User range setting   | -32000 to +32000  | 500 nA*   |  |  |  |
| Accuracy (full scale digital output value accuracy) | Ambient   |  | nin ±0.1% (±64 digits)<br>ithin ±0.2% (±128 digits)<br>within ±0.3% (±192 digits) |           |  |  |  |
| Conversion speed                                    | 80 µs/ch  |  |   |           |  |  |  |
| Isolation   |   | input terminal and PLC: I<br>input terminal channels:  |   |           |  |  |  |
| Power supply  |   | 40 mA (internal power si<br>40 mA (internal power si   |   |           |  |  |  |
| Compatible CPU module                               |   | 5UC: Ver. 1.050 or later<br>on with FX5UC requires F   | X5-CNV-IFC or FX5-C1PS  | S-5V.     |  |  |  |
| Number of occupied I/O points                       | 8 points (  | Either input or output is a  | vailable for counting.)   |           |  |  |  |
| Number of connectable modules                       |   | FX5U: Up to 16 modules<br>FX5UC: Up to 16 modules, or up to 15 modules when using a powered I/O module |   |           |  |  |  |
| External dimensions<br>W × H × D (mm)               | 40 × 90 × 102.2   |  |   |           |  |  |  |
| MASS (Weight): kg                                   | Approx. 0.2   |  |   |           |  |  |  |
| *: Maximum resolution in t                          | he user rar   | ae settina.  |   |           |  |  |  |

lution in the user range setting

# FX5-8AD type multiple input module

## **○ Features**



- 1) High precision multi input module with 312.5  $\mu$ V at voltage input and 625 nA at current input.
- 2) Spring clamp terminal block type with excellent vibration resistance.
- 3) Data of 10,000 points can be logged for each channel and saved in buffer memory. Leaving logs will be useful for analyzing the cause of trouble.

# **○** Specifications

| Items  |  |   | Specifications   |           |  |  |
|--|--|---|------------------|-----------|--|--|
| Analog input points                                  | 8 points (8 channels)  |   |                  |           |  |  |
| Analog input voltage                                 | -10 to 10 V DC (input resistance 1 MΩ)   |   |                  |           |  |  |
| Analog input current                                 | -20 to +2  | -20 to +20 mA DC (input resistance 250 Ω) |                  |           |  |  |
| Absolute maximum input                               | Voltage:   | ±15 V, Current: ±30 m                     | A                |           |  |  |
|  | Analog input range Digital output value Resolution   |   |                  |           |  |  |
|  |  | 0 to 10 V                                 | 0 to 32000       | 312.5 µV  |  |  |
|  | Voltage  | 0 to 5 V                                  | 0 to 32000       | 156.25 µV |  |  |
| Input characteristics,                               | voltage  | 1 to 5 V                                  | 0 to 32000       | 125 µV    |  |  |
| resolution   |  | -10 to +10 V                              | -32000 to +32000 | 312.5 µV  |  |  |
|  |  | 0 to 20 mA                                | 0 to 32000       | 625 nA    |  |  |
|  | Current  | 4 to 20 mA                                | 0 to 32000       | 500 nA    |  |  |
|  |  | -20 to +20 mA                             | -32000 to +32000 | 625 nA    |  |  |
| Digital output value<br>(16-bit signed binary value) | 16-bit signed binary (-32000 to +32000)  |   |                  |           |  |  |
| Accuracy   | Ambient temperature 25±5°C: within ±0.3% (±192 digits)<br>Ambient temperature -20 to +55°C: within ±0.5% (±320 digits) |   |                  |           |  |  |
| Conversion speed                                     | 1 ms/ch  |   |                  |           |  |  |
| Isolation  | Between input terminal and PLC: Photocoupler isolation<br>Between input terminal channels: Non-isolation               |   |                  |           |  |  |
| Power supply   | 24 V DC, 40 mA (internal power supply)<br>24 V DC +20%, -15% 100 mA (external power supply)                            |   |                  |           |  |  |
| Compatible CPU module                                | FX5U, FX5UC: Ver. 1.050 or later<br>Connection with FX5UC requires FX5-CNV-IFC or FX5-C1PS-5V.                         |   |                  |           |  |  |
| Number of occupied I/O points                        | 8 points (Either input or output is available for counting.)   |   |                  |           |  |  |
| Number of connectable modules                        | FX5U: Up to 16 modules<br>FX5UC: Up to 16 modules, or up to 15 modules when using a powered I/O module                 |   |                  |           |  |  |
| External dimensions $W \times H \times D$ (mm)       | 50 × 90 × 102.2  |   |                  |           |  |  |
| MASS (Weight): kg                                    | Approx. 0.3  |   |                  |           |  |  |

# FX3U-4AD type analog input module

## ○ Features



- High-precision analog input module with resolution of 15 bits binary + 1-bit sign (voltage) and 14 bits binary + 1-bit sign (current).
- 2) 4-channel voltage input
  (-10 to +10 V DC) or current input
  (-20 to +20 mA DC, 4 to 20 mA DC) is allowed.
- 3) Voltage or current input can be specified for each channel.
- High-speed AD conversion of 500 µs/ch has been implemented.
- 5) Various functions such as digital filter function and peak value hold function have been provided.

# $\Diamond$ Specifications

| Items  | Input voltage  | Input current   |  |  |  |  |
|--|--|---|--|--|--|--|
| Analog input range                             | -10 to +10 V DC<br>(Input resistance 200 kΩ)   | -20 to +20 mA DC, 4 to 20 mA<br>(Input resistance 250 Ω)  |  |  |  |  |
| Effective digital output                       | 15 bits binary + 1-bit sign  | 14 bits binary + 1-bit sign   |  |  |  |  |
| Resolution                                     | 0.32 mV (20 V × 1/64000)   | 1.25 µA (40 mA × 1/32000)   |  |  |  |  |
| Total precision                                | [With ambient temperature 25°C±5°C]<br>±0.3% in respect to full-scale 20 V (±60 mV)<br>[With ambient temperature 0 to 55°C]<br>±0.5% in respect to full-scale 20 V (±100 mV) | [With ambient temperature 25°C±5°C]<br>With input of -20 to +20 mA<br>±0.5% (±200 µA) in respect to full-scale 40 mA<br>Same as with input 4 to 20 mA<br>[With ambient temperature 0 to 55°C]<br>With input of -20 to +20 mA<br>±1% (±400 µA) in respect to full-scale 40 mA<br>Same as with input 4 to 20 mA |  |  |  |  |
| Conversion speed                               | 500 µs × Number of channels (5 ms × Number of channels used when digital filter is used)   |   |  |  |  |  |
| Isolation                                      | Between input terminal and PLC: Photocoupler isolation<br>Between input terminal channels: Non-isolation   |   |  |  |  |  |
| Power supply                                   | 5 V DC, 110 mA (internal power supply)<br>24 V DC ±10% 90 mA/24 V DC (external power feed)   |   |  |  |  |  |
| Compatible CPU module                          | FX5U, FX5UC, compatible from initial product<br>Connection with FX5U requires FX5-CNV-BUS, and connection with FX5UC requires<br>FX5-CNV-BUS or FX5-CNV-BUSC.                |   |  |  |  |  |
| Number of occupied input/<br>output points     | 8 points (Either input or output is available for counting.)   |   |  |  |  |  |
| Communication with PLC                         | Carried out by FROM/TO instruction via buffer memory<br>(buffer memory can directly be specified)  |   |  |  |  |  |
| Number of connectable modules                  | FX5U : Up to 8 modules when FX3U extension power supply modules are used<br>Up to 6 modules when FX3U extension power supply modules are not used<br>FX5UC: Up to 6 modules  |   |  |  |  |  |
| External dimensions $W \times H \times D$ (mm) | 55 × 90 × 87   |   |  |  |  |  |
| MASS (Weight): kg                              | Approx. 0.2  |   |  |  |  |  |

# FX5-4DA type analog output module

#### ◇ Features



- High-precision analog output module with 312.5 µV at voltage output and 625 nA at current output.
- 2) Spring clamp terminal block type with excellent vibration resistance.
- Built-in waveform output function for continuous analog output at a set conversion cycle by registering prepared waveform data (digital value) to the module extension parameter. Faster and smoother output than with programming, and program-free control for reduced overall programming work.

## ♦ Specifications

| Items  | Specifications   |   |                  |           |  |  |
|--|--|---|------------------|-----------|--|--|
| Analog output points                                     | 4 points (4 channels)  |   |                  |           |  |  |
| Analog output voltage                                    | -10 to +10 V DC (external load resistance 1 k $\Omega$ to 1 M $\Omega$ )   |   |                  |           |  |  |
| Analog output current                                    | 0 to 20 r  | 0 to 20 mA DC (external load resistance 0 to 500 Ω) |                  |           |  |  |
| Digital input  | 16-bit si  | 16-bit signed binary (-32768 to +32767)             |                  |           |  |  |
|  | Analog output range Digital value Resolution   |   |                  |           |  |  |
|  |  | 0 to 10 V   | 0 to 32000       | 312.5 µV  |  |  |
|  |  | 0 to 5 V  | 0 to 32000       | 156.3 µV  |  |  |
|  | Voltage  | 1 to 5 V  | 0 to 32000       | 125 µV    |  |  |
| Output characteristics, resolution                       |  | -10 to +10 V  | -32000 to +32000 | 312.5 µV  |  |  |
| resolution   |  | User range setting                                  | -32000 to +32000 | 312.5 µV* |  |  |
|  |  | 0 to 20 mA  | 0 to 32000       | 625 nA    |  |  |
|  | Current  | 4 to 20 mA  | 0 to 32000       | 500 nA    |  |  |
|  |  | User range setting                                  | -32000 to +32000 | 500 nA*   |  |  |
| Accuracy (full scale<br>analog output value<br>accuracy) | Ambient temperature 25±5°C: within ±0.1% (Voltage ±20 mV, Current ±20 μA)<br>Ambient temperature 0 to 55°C: within ±0.2% (Voltage ±40 mV, Current ±40 μA)<br>Ambient temperature -20 to 0°C: within ±0.3% (Voltage ±60 mV, Current ±60 μA) |   |                  |           |  |  |
| Conversion speed   | 80 µs/ch   |   |                  |           |  |  |
| Isolation  | Between output terminal and PLC: Photocoupler isolation<br>Between output channels: Non-isolation  |   |                  |           |  |  |
| Power supply   | 5 V DC, 100 mA (internal power supply)<br>24 V DC +20%, -15% 150 mA (external power supply)  |   |                  |           |  |  |
| Compatible CPU module                                    | FX5U, FX5UC: Ver. 1.050 or later<br>Connection with FX5UC requires FX5-CNV-IFC or FX5-C1PS-5V.   |   |                  |           |  |  |
| Number of occupied I/O points                            | 8 points (Either input or output is available for counting.)   |   |                  |           |  |  |
| Number of connectable modules                            | FX5U: Up to 16 modules<br>FX5UC: Up to 16 modules, or up to 15 modules when using a powered I/O module   |   |                  |           |  |  |
| External dimensions<br>W × H × D (mm)                    | 40 × 90 × 102.2  |   |                  |           |  |  |
| MASS (Weight): kg  | Approx. 0.2  |   |                  |           |  |  |

\*: Maximum resolution in the user range setting.

# FX3U-4DA type analog output module

#### ◇ Features



- High-precision analog output module with resolution of 15 bits binary + 1-bit sign (voltage) and 15 bits binary (current).
- 4-channel voltage output (-10 to + 10 V DC) or current output (0 to 20 mA DC, 4 to 20 mA DC) is allowed.
- 3) Voltage or current output can be specified for each channel.
- Various functions such as table output function and upper-limit/ lower-limit value function have been provided.

# ♦ Specifications

| Items  | Output voltage  | Output current  |  |  |
|--|---|---|--|--|
| Analog output range                            | -10 to +10 V DC<br>(External load 1 kΩ to 1 MΩ)   | 0 to 20 mA DC, 4 to 20 mA DC<br>(External load 500 Ω or less) |  |  |
| Effective digital input                        | 15 bits binary + 1-bit sign   | 15-bit binary value   |  |  |
| Resolution                                     | 0.32 mV (20 V × 1/64000)  | 0.63 µA (20 mA × 1/32000)                                     |  |  |
| Total precision                                | Ambient temperature 25±5°C         Ambient temperature 25±5°C           ±0.3% (±60 mV) in respect to full-scale 20 V         ±0.3% (±60 μA) in respect to full-scale 20 V           Ambient temperature 0 to 55°C         ±0.5% (±100 μA) in respect to full-scale 20 V |   |  |  |
| Conversion speed                               | 1 ms (unrelated to the number of channels   | used)   |  |  |
| Isolation                                      | Between output terminal and PLC: Photocoupler isolation<br>Between output terminal channels: Non-isolation  |   |  |  |
| Power supply                                   | 5 V DC, 120 mA (internal power supply)<br>24 V DC ±10% 160 mA/24 V DC (external power feed)   |   |  |  |
| Compatible CPU module                          | FX5U, FX5UC, compatible from initial product<br>Connection with FX5U requires FX5-CNV-BUS, and connection with FX5UC requires<br>FX5-CNV-BUS or FX5-CNV-BUSC.   |   |  |  |
| Number of occupied input/<br>output points     | 8 points (Either input or output is available for counting.)  |   |  |  |
| Communication with PLC                         | Carried out by FROM/TO instruction via buffer memory<br>(buffer memory can directly be specified)   |   |  |  |
| Number of connectable modules                  | FX5U : Up to 8 modules when FX3U extension power supply modules are used<br>Up to 6 modules when FX3U extension power supply modules are not used<br>FX5UC: Up to 6 modules   |   |  |  |
| External dimensions $W \times H \times D$ (mm) | 55 × 90 × 87  |   |  |  |
| MASS (Weight): kg                              | Approx. 0.2   |   |  |  |

# Built-in analog input/output function of FX5U CPU module

## 



 FX5U CPU module has built-in analog input/output. It contains 2-channel analog input and 1-channel analog output.

# ♦ Specifications (built-in analog input/output only)

| Items          |  | Specificat  | ions  |  |  |  |  |
|----------------|--|---|---|--|--|--|--|
| Analog input   |  | 0 to 10 V DC (Input resistance 115.7 Ω)   |   |  |  |  |  |
|                | Absolute maximum input                         | -0.5 V, +15 V   |   |  |  |  |  |
|                | Digital output value                           | 0 to 4000   |   |  |  |  |  |
| A/D part       | Digital output                                 | Unsigned 12-bit binary  |   |  |  |  |  |
| AVD part       | Maximum resolution                             | 2.5 mV  |   |  |  |  |  |
|                | Precision                                      | At ambient temperature of 25°C±5°C, within ±0.5% (±20 digit*1)<br>At ambient temperature of 0 to 55°C, within ±1.0% (±40 digit*1)<br>At ambient temperature of -20 to 0°C*2, within ±1.5% (±60 digit*1) |   |  |  |  |  |
|                | Conversion speed                               | 30 µs/channels (data refreshed every ope  | 30 µs/channels (data refreshed every operation cycle) |  |  |  |  |
|                | Items  | Specificat  | ione  |  |  |  |  |
|                | Analog output                                  |   |   |  |  |  |  |
|                | Digital input value                            | 0 to 10 V DC (External load resistance value 2 kΩ to 1 MΩ)<br>0 to 4000   |   |  |  |  |  |
|                | Digital input                                  | Unsigned 12-bit binary  |   |  |  |  |  |
| -              | Maximum resolution                             | 2.5 mV  |   |  |  |  |  |
| D/A part       | Precision                                      | At ambient temperature of 25°C±5°C, within ±0.5% (±20 digit*1)<br>At ambient temperature of 0 to 55°C, within ±1.0% (±40 digit*1)<br>At ambient temperature of -20 to 0°C*2, within ±1.5% (±60 digit*1) |   |  |  |  |  |
|                | Conversion speed                               | 30 µs (data refreshed every operation cyc   |   |  |  |  |  |
|                |  |   |   |  |  |  |  |
|                | Items  | Input specifications  | Output specifications                                 |  |  |  |  |
|                | Isolation                                      | Inside the PLC: Non-isolation<br>Between input terminal channels: Inside the PLC: Non-isolat<br>Non-isolation   |   |  |  |  |  |
| 0              | Number of occupied input/output points         | 0 points (no points occupied)   |   |  |  |  |  |
| Common<br>part | External dimensions $W \times H \times D$ (mm) | FX5U-32M⊡: 150 × 90 × 83<br>FX5U-64M⊡: 220 × 90 × 83<br>FX5U-80M⊡: 285 × 90 × 83  |   |  |  |  |  |
|                | MASS (Weight): kg                              | FX5U-32M <sup>-</sup> : Approx. 0.70<br>FX5U-64M <sup>-</sup> : Approx. 1.00<br>FX5U-80M <sup>-</sup> : Approx. 1.20  |   |  |  |  |  |

\*1: Digit refers to digital values.

\*2: Products manufactured earlier than June 2016 do not support this specification.

# Input device for temperature sensor

Platinum resistance thermometer sensor (Pt100) or thermocouple temperature sensors can be connected. FX5-4LC type temperature control module, which provides PID control function with auto tuning, can use a function of intelligent function module to perform temperature control.

# $\diamond$ List of input devices for temperature sensor

| Model<br>(Number of channels) | Compatible sensor   |             | Input specifications  | Insulation method  | Compatible<br>CPU module |       | Number<br>of |
|-------------------------------|---|-------------|---|--|--------------------------|-------|--------------|
|                               |   | Items       | Temperature input   |  | FX5U                     | FX5UC | channels     |
| FX5-4AD-PT-ADP                | Resistance temperature detector   | Input range | Pt100: -200 to 850°C<br>Ni100: -60 to 250°C   | Between input terminal and PLC:<br>Photocoupler insulation<br>Between input terminal channels<br>Non-isolation   |                          |       |              |
| 1                             | Pt100, Ni100  | Resolution  | 0.1°C   |  |                          |       |              |
| FX5-4AD-TC-ADP                | Thermocouple  | Input range | [Typical example]<br>K type: -200 to 1200°C<br>J type: -40 to 750°C   |  | 0                        | 0     | 4 ch         |
| 1                             | K, J, T, B, R, S  | Resolution  | 0.1°C to 0.3°C<br>(depending on the sensor used)  |  |                          |       |              |
| FX5-8AD                       | Resistance temperature detector   | Input range | Pt100: -200 to 850°C<br>Ni100: -60 to 250°C   |  |                          |       |              |
| r 11                          | Pt100, Ni100  | Resolution  | 0.1°C   | Between input terminal and PLC:  |                          |       |              |
|                               | Thermocouple<br>K, J, T, B, R, S  | Input range | [Typical example]<br>K type: -200 to 1200°C<br>J type: -40 to 750°C   | Photocoupler insulation<br>Between input terminal channels:<br>Non-isolation   | 0                        | 0*    | 8 ch         |
|                               | n, J, I, D, n, J  | Resolution  | 0.1°C to 0.3°C<br>(depending on the sensor used)  |  |                          |       |              |
| FX5-4LC                       | Resistance temperature detector<br>3-wire type Pt100<br>3-wire type JPt100<br>2-wire/3-wire type Pt1000 | Input range | 3-wire type Pt100: -200 to 600°C<br>3-wire type JPt100: -200 to 500°C<br>2-wire/3-wire type Pt1000: -200 to 650°C | Between analog input part and PLC:<br>Photocoupler insulation<br>Between transistor output part and PLC:<br>Photocoupler insulation<br>Between analog input part and power<br>supply:<br>Insulation by the DC-DC converter<br>Between transistor output part and power<br>supply:<br>Insulation by the DC-DC converter<br>Between channels: Isolated |                          |       |              |
|                               |   | Resolution  | 0.1°C or 1°C<br>(depends on the sensor used)  |  |                          |       |              |
|                               | Thermocouple<br>K, J, T, B, R, S, N,<br>PLII, W5Re/W26Re, U, L  | Input range | [Typical example]<br>K type: -200 to 1300°C<br>J type: -200 to 1200°C   |  | 0                        | 0*    | 4 ch         |
|                               |   | Resolution  | 0.1°C or 1°C<br>(depending on the sensor used)  |  |                          |       |              |
|                               | Micro voltage input   | Input range | 0 to 10 mV DC, 0 to 100 mV DC   |  |                          |       |              |
|                               |   | Resolution  | 0.5 μV, 5.0 μV  |  |                          |       |              |
| FX3U-4LC                      | Resistance temperature detector<br>3-wire type Pt100<br>3-wire type JPt100<br>2-wire/3-wire type Pt1000 | Input range | [Typical example]<br>Pt100: -200 to 600°C<br>Pt1000: -200.0 to 650.0°C  | Between inside and channels:<br>Photocoupler isolation<br>Between inside and power supply:<br>Insulation by the DC-DC converter<br>Between channels: Isolated  |                          |       |              |
|                               |   | Resolution  | 0.1°C or 1°C<br>(depending on the sensor used)  |  |                          |       |              |
|                               | Thermocouple<br>K, J, R, S, E, T, B, N, PLII,<br>W5Re/W26Re, U, L                                       | Input range | [Typical example]<br>K type: -200.0 to 1300°C<br>J type: -200.0 to 1200°C   |  | O*2                      | O*2   | 4 ch         |
|                               |   | Resolution  | 0.1°C or 1°C<br>(depending on the sensor used)  |  |                          |       |              |
|                               | Miero voltoro int   | Input range | 0 to 10 mV DC, 0 to 100 mV DC   |  |                          |       |              |
|                               | Micro voltage input   | Resolution  | 0.5 μV, 5.0 μV  |  |                          |       |              |

\*1: Connection with FX5UC requires FX5-CNV-IFC or FX5-C1PS-5V.

\*2: Connection with FX5U or FX5UC.requires FX5-CNV-BUS or FX5-CNV-BUSC.

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#### FX5-4AD-PT-ADP type resistance temperature detector temperature sensor input expansion adapter

## ◇ Features



- 1) Resistance temperature detector (Pt100, Ni100) temperature sensor input expansion adapter
- 2) Four channels can be measured with high resolution of 0.1°C.
- 3) It is possible to use a combination of temperature sensors for each channel.
- 4) The measurement unit can be expressed in degrees Celsius (°C) or Fahrenheit (°F).
- 5) Data transfer is possible without programming (no dedicated instructions).

# ♦ Specifications

| Items  |                            |       | Specifications  |  |  |  |
|--|----------------------------|-------|---|--|--|--|
| Analog   | Analog input points        |       | 4 points (4 channels)   |  |  |  |
| Usable resistance                              |                            |       | Pt100   |  |  |  |
| tempe  | rature detecto             | **1   | Ni100 (DIN 43760 1987)  |  |  |  |
| Tempe  | erature                    | Pt100 | -200 to 850°C (-328 to 1562°F)  |  |  |  |
| measu  | iring range                | Ni100 | -60 to 250°C (-76 to 482°F)   |  |  |  |
|  |                            |       | 16-bit signed binary value  |  |  |  |
| Digital  | Digital output value       |       | -2000 to 8500 (-3280 to 1562)   |  |  |  |
|  |                            | Ni100 | -600 to 2500 (760 to 4820)  |  |  |  |
|  | Ambient                    | Pt100 | ±0.8°C  |  |  |  |
| Accuracy                                       | temperature<br>25±5°C      | Ni100 | ±0.4°C  |  |  |  |
| Accu   | Ambient                    | Pt100 | ±2.4°C  |  |  |  |
|  | temperature<br>-20 to 55°C | Ni100 | ±1.2°C  |  |  |  |
| Resolu   | Resolution                 |       | 0.1°C (0.1 to 0.2°F)  |  |  |  |
| Conve  | rsion speed*2              |       | Approx 85 ms/channel  |  |  |  |
| Isolation                                      |                            |       | Between input terminal and CPU module: Photocoupler isolation<br>Between input terminal channels: Non-isolation |  |  |  |
| Power supply                                   |                            |       | 24 V DC, 20 mA (internal power supply)<br>5 V DC, 10 mA (internal power supply)                                 |  |  |  |
| Comp   | atible CPU mo              | dule  | FX5U, FX5UC: Ver. 1.040 or later  |  |  |  |
| Number of occupied I/O points                  |                            | I/O   | 0 points (no occupied points)   |  |  |  |
| Number of connectable modules                  |                            | ble   | FX5U, FX5UC: Up to 4 modules to the left side of CPU module   |  |  |  |
| External dimensions $W \times H \times D$ (mm) |                            |       | 17.8 × 106 × 89.1   |  |  |  |
| MASS (Weight): kg                              |                            |       | Approx. 0.1   |  |  |  |

\* 1: Only 3-wire type resistance temperature detectors can be used.
 \* 2: For details of conversion speeds, refer to the manual.

# FX5-4AD-TC-ADP type thermocouple temperature sensor input expansion adapter

#### **○ Features**



- 1) Thermocouple temperature sensor input expansion adapter
- 2) Four channels can be measured with high resolution of 0.1°C.
- 3) It is possible to use a combination of temperature sensors for each channel.
- 4) The measurement unit can be expressed in degrees Celsius (°C) or Fahrenheit (°F).
- 5) Data transfer is possible without programming (no dedicated instructions).

# ♦ Specifications

|   | Item                          |         | Speci   | fications                 |  |  |  |
|---|-------------------------------|---------|---|---------------------------|--|--|--|
| Anal  | og input points               |         | 4 points (4 channels)   |                           |  |  |  |
|   | icable thermoco               | puple*1 | K, J, T, B, R, S  |                           |  |  |  |
| 7.4010  |                               | K       | -200 to 1200°C (-328 to 2192°F)   |                           |  |  |  |
|   |                               | J       | -40 to 750°C (-40 to 1382°F)  |                           |  |  |  |
| Tom   | ooroturo                      | Т       | -200 to 350°C (-328 to 662°F)   |                           |  |  |  |
|   | perature<br>suring range      | B       | 600 to 1700°C (1112 to 3092°F)  |                           |  |  |  |
| mou   | ouring rungo                  | R       | 0 to 1600°C (32 to 2912°F)  |                           |  |  |  |
|   |                               | S       | 0 to 1600°C (32 to 2912°F)  |                           |  |  |  |
|   |                               | 0       | . ,   |                           |  |  |  |
|   |                               |         | 16-bit signed binary value  |                           |  |  |  |
|   |                               | K       | -2000 to 12000 (-3280 to 21920)   |                           |  |  |  |
|   |                               | J       | -400 to 7500 (-400 to 13820)  |                           |  |  |  |
| Digit   | al output value               | T       | -2000 to 3500 (-3280 to 6620)   |                           |  |  |  |
|   |                               | В       | 6000 to 17000 (11120 to 30920)  |                           |  |  |  |
|   |                               | R       | 0 to 16000 (320 to 29120)   |                           |  |  |  |
|   |                               | S       | 0 to 16000 (320 to 29120)   |                           |  |  |  |
|   |                               | к       | ±3.7°C (-100 to 1200°C)*2   | ±4.9°C (-150 to -100°C)*2 |  |  |  |
|   |                               |         | ±7.2°C (-200 to -150°C)*2   |                           |  |  |  |
|   |                               | J       | ±2.8°C  |                           |  |  |  |
|   | Ambient                       | Т       | ±3.1°C (0 to 350°C)*2   | ±4.1°C (-100 to 0°C)*2    |  |  |  |
|   | temperature<br>25±5°C         |         | ±5.0°C (-150 to -100°C)*2   | ±6.7°C (-200 to -150°C)*2 |  |  |  |
|   | 2010 0                        | В       | ±3.5°C  |                           |  |  |  |
| Ŧ   |                               | R       | ±3.7°C  |                           |  |  |  |
| Accuracy*1                                      |                               | S       | ±3.7°C  |                           |  |  |  |
| our   |                               |         | ±6.5°C (-100 to 1200°C)*2   | ±7.5°C (-150 to -100°C)*2 |  |  |  |
| Ac  |                               | K       | ±8.5°C (-200 to -150°C)*2   |                           |  |  |  |
|   |                               | J       | ±4.5°C  |                           |  |  |  |
|   | Ambient                       | _       | ±4.1°C (0 to 350°C)*2   | ±5.1°C (-100 to 0°C)*2    |  |  |  |
|   | temperature<br>-20 to 55°C    | Т       | ±6.0°C (-150 to -100°C)*2   | ±7.7°C (-200 to -150°C)*2 |  |  |  |
|   | -20 10 55 0                   | В       | ±6.5°C  |                           |  |  |  |
|   |                               | R       | ±6.5°C  |                           |  |  |  |
|   |                               | S       | ±6.5°C  |                           |  |  |  |
|   | 1                             | K, J, T | 0.1°C (0.1 to 0.2°F)  |                           |  |  |  |
| Reso  | olution                       | B, R, S | 0.1 to 0.3°C (0.1 to 0.6°F)   |                           |  |  |  |
| Con   | version speed*3               | 10,11,0 |   |                           |  |  |  |
|   |                               |         | Approx. 85 ms/channel   |                           |  |  |  |
| Isola   | Isolation                     |         | Between input terminal and CPU module: Photocoupler isolation<br>Between input terminal channels: Non-isolation |                           |  |  |  |
| Power supply                                    |                               |         | 24 V DC, 20 mA (internal power supply)  |                           |  |  |  |
|   |                               |         | 5 V DC, 10 mA (internal power supply)<br>FX5U, FX5UC: Ver. 1.040 or later                                       |                           |  |  |  |
| Compatible CPU module<br>Number of occupied I/O |                               |         | ,   |                           |  |  |  |
| poin  |                               | 1/0     | 0 point (no occupied points)  |                           |  |  |  |
| Num<br>mod                                      | iber of connecta<br>ules      | able    | FX5U, FX5UC: Up to 4 modules to the le  | aft side of CPU module    |  |  |  |
|   | rnal dimensions<br>H × D (mm) |         | 17.8 × 106 × 89.1   |                           |  |  |  |
| <u> </u>  | S (Weight): kg                |         | Approx. 0.1   |                           |  |  |  |
|   |                               |         | au requires a warm up of 45 minutes (opera  |                           |  |  |  |

\*1: Obtaining sufficient accuracy requires a warm-up of 45 minutes (energization).
\*2: Accuracy varies depending on the measured temperature range in ().
\*3: For details of conversion speeds, refer to the manual.

#### FX5-8AD type multiple input module

#### **○ Features**



- 1) Since a single module can handle input of voltage, current, thermocouple, and resistance temperature detector, there is no need to prepare multiple modules for different objects.
- 2) The module can easily detect a disconnection of the thermocouple or resistance temperature detector, and therefore can reduce the downtime and maintenance cost.
- 3) Data of 10000 points can be logged for each channel and saved in buffer memory. Saving logs will be useful for troubleshooting.

# ♦ Specifications

|  | Item  |  | Specifications  |  |  |  |  |
|--|---|--|---|--|--|--|--|
| Analog input po  |   | 8 points (8 channels   |   |  |  |  |  |
| Analog input vo  |   | -10 to 10 V DC (input resistance 1 MΩ)   |   |  |  |  |  |
| Analog input cu  |   | -20 to +20 mA DC (input resistance 250 Ω)  |   |  |  |  |  |
| Absolute maxin   |   | Voltage: ±15 V, Current: ±30 mA  |   |  |  |  |  |
|  |   |  | K, J, T: 0.1°C (0.1 to 0.2°F)   |  |  |  |  |
| Input  | Thermocouple  |  | B, R, S: 0.1 to 0.3°C (0.1 to 0.6°F)  |  |  |  |  |
| characteristics,<br>resolution*1                           | Resistance<br>temperature<br>detector                       | 0.1°C (0.2°F)  |   |  |  |  |  |
| Digital output<br>value<br>(16-bit signed<br>binary value) | Thermocouple  | K: -2000 to +1200<br>J: -400 to +7500 (<br>T: -2000 to +3500<br>B: 6000 to 17000 (<br>R: 0 to 16000 (320<br>S: 0 to 16000 (320 | (-3280 to +6620)<br>11120 to 30920)<br>to 29120)  |  |  |  |  |
|  | Resistance<br>temperature<br>detector                       |  | 8500 (-3280 to +15620)<br>500 (-760 to +4820)   |  |  |  |  |
|  | Thermocouple*2  | Ambient<br>temperature<br>25±5°C   | K: ±3.5°C (-200 to -150°C)<br>K: ±2.5°C (-150 to -100°C)<br>K: ±1.5°C (-100 to 1200°C)<br>J: ±1.2°C<br>T: ±3.5°C (-200 to -150°C)<br>T: ±2.5°C (-150 to -100°C)<br>T: ±2.3°C<br>B: ±2.3°C<br>R: ±2.5°C<br>S: ±2.5°C   |  |  |  |  |
| Accuracy   |   | Ambient<br>temperature -20<br>to 55°C  | K: $\pm 8.5^{\circ}$ C (-200 to -150°C)<br>K: $\pm 7.5^{\circ}$ C (-150 to -100°C)<br>K: $\pm 6.5^{\circ}$ C (-100 to 1200°C)<br>J: $\pm 3.5^{\circ}$ C<br>T: $\pm 5.2^{\circ}$ C (-200 to -150°C)<br>T: $\pm 4.2^{\circ}$ C (-150 to -100°C)<br>T: $\pm 3.1^{\circ}$ C (-100 to 350°C)<br>B: $\pm 6.5^{\circ}$ C<br>S: $\pm 6.5^{\circ}$ C |  |  |  |  |
|  | Resistance  | Ambient<br>temperature<br>25±5°C   | Pt100: ±0.8°C<br>Ni100: ±0.4°C  |  |  |  |  |
|  | temperature<br>detector                                     | Ambient<br>temperature -20<br>to 55°C  | Pt100: ±2.4°C<br>Ni100: ±1.2°C  |  |  |  |  |
| Conversion<br>speed  | Thermocouple/<br>Resistance<br>temperature<br>detector      | 40 ms/ch   |   |  |  |  |  |
| Isolation  |   | Between input term   | inal and PLC: Photocoupler isolation<br>inal channels: Non-isolation  |  |  |  |  |
| Power supply   |   |  | ternal power supply)<br>5% 100 mA (external power supply)   |  |  |  |  |
| Compatible CPU module                                      |   |  | FX5U, FX5UC: Ver. 1.050 or later<br>Connection with FX5UC requires FX5-CNV-IFC or FX5-C1PS-5V.  |  |  |  |  |
| Applicable engineering tool                                |   | GX Works3 Ver. 1.0   | 035M or later   |  |  |  |  |
|  | Number of occupied I/O points Number of connectable modules |  | unted on either input or output)<br>idules<br>iodules, or up to 15 modules when using a powered I/O   |  |  |  |  |
| External dimens<br>W × H × D (mm                           |   | module<br>50 × 90 × 102.2  |   |  |  |  |  |
| MASS (Weight)  | <u>'</u>  | Approx. 0.3  |   |  |  |  |  |
| ioo (woight)   |   | II. pprov. 0.0   |   |  |  |  |  |

\*1: For details of input characteristics, refer to the manual.
 \*2: To stabilize the accuracy, warm-up (supply power) the system for 30 minutes or more after power-on.

#### FX5-4LC type temperature control module

#### ◇ Features



- Being compatible with the thermocouple, resistance temperature detector, and micro voltage input, the module can be used for a wide range of applications.
- The module can suppress the overshoot in which the output value exceeds the target value or hunting phenomenon which oscillates before and after the target value.
- Since the change in temperature can be checked with the waveform, parameters can be adjusted while checking the waveform displayed in real time.

# $\diamond$ Specifications

|                         | ltem  |  | Specifications   |   |  |  |  |  |
|-------------------------|---|--|--|---|--|--|--|--|
| Control system          |   | Two-position control, standard PID control, heating/cooling PID control, cascade control   |  |   |  |  |  |  |
| Control operation c     | ycle  | 250 ms/4 ch  |  |   |  |  |  |  |
| Temperature measu       | uring range   | Thermocouple   | K: -200 to +1300°C (-100 to +2400°F)<br>J: -200 to +1200°C (-100 to +2100°F)<br>T: -200 to +400°C (-300 to +700°F)<br>S: 0 to 1700°C (0 to 3200°F)<br>R: 0 to 1700°C (0 to 3200°F)<br>E: -200 to +1000°C (0 to 1800°F) | B: 0 to 1800°C (0 to 3000°F)<br>N: 0 to 1300°C (0 to 2300°F)<br>PLII: 0 to 1200°C (0 to 2300°F)<br>W5Re/W26Re: 0 to 2300°C (0 to 3000°F)<br>U: -200 to +600°C (-300 to +700°F)<br>L: 0 to 900°C (0 to 1600°F) |  |  |  |  |
|                         |   | Resistance temperature<br>detector         Pt100 (3-wire type): -200 to +600°C (-300 to +1100°F)           JPt100 (3-wire type): -200 to +500°C (-300 to +900°F)           Pt1000 (2-wire/3-wire type): -200.0 to +650.0°C (-328 to +1184°F)                   |  |   |  |  |  |  |
|                         |   | Micro voltage input  | 0 to 10 mV DC, 0 to 100 mV DC  |   |  |  |  |  |
| Heater disconnection    | on detection  | Alarm detection  |  |   |  |  |  |  |
|                         | Number of input points                                    | 4 points   |  |   |  |  |  |  |
|                         | Input type<br>(selectable for each channel)               | Thermocouple<br>Resistance temperature<br>detector   | K, J, R, S, E, T, B, N, PLII, W5Re/W26Re,<br>3-wire type Pt100<br>3-wire type JPt100<br>2-wire/3-wire type Pt1000  | U, L  |  |  |  |  |
|                         |   | Micro voltage input  |  |   |  |  |  |  |
|                         | Measurement accuracy*                                     | <u> </u>   | FX5 User's Manual (Temperature Control).   |   |  |  |  |  |
|                         | Cold junction temperature                                 | Ambient temperature<br>0 to 55°C   | Within ±1.0°C.<br>When the input value is -150 to -100°C: W<br>When the input value is -200 to -150°C: W   |   |  |  |  |  |
| Input<br>specifications | compensation error  | Ambient temperature<br>-20 to 0°CWithin ±1.8°C.<br>When the input value is -150 to -100°C: Within ±3.6°C.<br>When the input value is -200 to -150°C: Within ±5.4°C   |  |   |  |  |  |  |
|                         | Resolution  | 0.1°C (0.1°F), 1.0°C (1.0°F)   | , 0.5 µV, or 5.0 µV (depends on the input rar  | nge of the sensor used)   |  |  |  |  |
|                         | Sampling cycle  | 250 ms/4 ch  |  |   |  |  |  |  |
|                         | Influence of input conductor resistance                   | 3-wire type  | Approx. 0.03%/ $\Omega$ for full scale, and 10 $\Omega$ or less per line   |   |  |  |  |  |
|                         | (for resistance temperature detector input)               | 2-wire type  | Approx. 0.04%/ $\Omega$ for full scale, and 7.5 $\Omega$ or less per line  |   |  |  |  |  |
|                         | Influence of external resistance (for thermocouple input) | About 0.125 μV/Ω   |  |   |  |  |  |  |
|                         | Input impedance   | 1 MΩ or more   |  |   |  |  |  |  |
|                         | Sensor current  | Approx. 0.2 mA (for resistar   | nce temperature detector input)  |   |  |  |  |  |
|                         | Operation at input disconnection/<br>short circuit        | Upscale/downscale (for resistance temperature detector input)  |  |   |  |  |  |  |
| Current detector (CT)   | Number of input points                                    | 4 points   |  |   |  |  |  |  |
| input specifications    | Sampling cycle  | 0.5 seconds  |  |   |  |  |  |  |
| Output specification    | าร  |  | ansistor output, Rated load voltage: 5 to 24 \<br>mA, Control output cycle: 0.5 to 100.0 seco  |   |  |  |  |  |
| Power supply            |   | 5 V DC, 140 mA (internal po<br>24 V DC +20%, -15% 25 m   | ower supply)   |   |  |  |  |  |
| Isolation               |   | The analog input part and between the transistor output part and PLC are insulated by the photocoupler.     The analog input part and between the transistor output part and power supply are insulated by the DC/DC converter.     Insulated between channels |  |   |  |  |  |  |
| Compatible CPU m        | odule   | FX5U, FX5UC: Ver. 1.050 o<br>Connection with FX5UC rec   | r later<br>quires FX5-CNV-IFC or FX5-C1PS-5V.  |   |  |  |  |  |
| Applicable engineer     | ring tool   | GX Works3 Ver. 1.035M or   | later  |   |  |  |  |  |
| Number of occupie       | d I/O points  | 8 points (can be counted or  | n either input or output)  |   |  |  |  |  |
| Number of connect       | able modules  | FX5U: Up to 16 modules<br>FX5UC: Up to 16 modules,   | or up to 15 modules when using a powered   | d I/O module  |  |  |  |  |
| External dimensions     | s W $\times$ H $\times$ D (mm)                            | 60 × 90 × 102.2  |  |   |  |  |  |  |
| MASS (Weight): kg       |   | Approx. 0.3  |  |   |  |  |  |  |

Analog control

#### FX3U-4LC type temperature control module

#### ◇ Features



- The module provides 4-ch temperature sensor input and control output through which "two-position control, standard PID control (auto-tuning possible), heating/cooling PID control, and cascade control" can be carried out. It can also be used in combination with an analog input/output module to perform PID control by voltage and current.
- 2) The module is newly equipped with cascade control. With two control loops of master and slave, the module can quickly adjust the temperature against temperature change due to disturbance or the like.
- Heating/cooling PID control of up to 4 loops can be performed by output operation of 2 systems (heating output and cooling output). Temperature control can be achieved with high stability in both the heating and cooling sides.
- Micro voltage signals such as "0-10 mV DC" and "0-100 mV DC" can be input. Sensors such as micro voltage output sensor can directly be connected.
- 5) The module supports a wide range of thermocouple temperature sensor and high-precision Pt1000 temperature sensor.

#### ♦ Specifications

|  | Items  |  | Specifications   |  |  |  |
|--|--|--|--|--|--|--|
| Control system                                 |  | Two-position control, standard PID control, heating/cooling PID control, and cascade control   |  |  |  |  |
| Control operation cycle                        |  | 250 ms/4 ch  |  |  |  |  |
|  |  | Thermocouple   | K: -200.0 to 300°C (-100 to 400°F)<br>J: -200.0 to 200°C (-100 to 100°F)   |  |  |  |
| Se   | tting temperature range*1                                      | Resistance temperature detector  | Pt100 (3-wire type): -200.0 to 00.0°C (-300.0 to 100°F)<br>Pt1000 (2-wire/3-wire type): -200.0 to 50.0°C (-328 to 184°F) |  |  |  |
|  |  | Micro voltage input  | 0 to 10 mV DC, 0 to 100 mV DC  |  |  |  |
| He   | ater disconnection detection                                   | Detection of alarm by buffer men   | nory (variable in the range from 0.0 to 100.0 A)   |  |  |  |
|  | No. of input points  | 4 points   |  |  |  |  |
| Type of input<br>(selectable for each channel) |  | [Resistance temperature detecto<br>[Thermocouple] K, J, R, S, E, T, f<br>[Micro voltage input] 0 to 10 mV  | 3-wire type JPt100<br>2-wire/3-wire type Pt1000<br>B, N, PLII, W5Re/W26Re, U, L  |  |  |  |
| Input specifications                           | Example of measurement accuracy*1*2                            | [At ambient temperature 0 to 55°   | e is 500°C or more: Displayed value ±0.3% ±1 digit   |  |  |  |
| -  | Example of resolution*1  | 0.1°C (0.1°F), 1°C (1°F), 0.5 μV, α  | or 5.0 μV  |  |  |  |
|  | Sampling cycle   | 250 ms/4 ch  |  |  |  |  |
|  | Operation at the time of input disconnection/<br>short-circuit | Up scale/down scale (at the time of resistance thermometer sensor input)   |  |  |  |  |
| Cu   | rrent detector (CT) input specification                        | Number of points: 4<br>Current detector: CTL-12-S36-8, CTL-12-S56-10,<br>CTL-6-P-H (manufactured by U.R.D. Ltd.), sampling cycle: 0.5 sec.   |  |  |  |  |
| Ou   | tput specifications  | Number of points: 4<br>Type: NPN open collector transistor, Rated load voltage: 5 to 24 V DC,<br>Maximum load current: 100 mA, Control output cycle: 0.5 to 100.0 sec.   |  |  |  |  |
| Po   | wer supply   | 5 V DC 160 mA (Internal power supply)<br>24 V DC +20% -15% 50 mA (external power feed from terminal block)   |  |  |  |  |
| Iso  | lation   | Use of photocoupler for isolation between analog inputs/transistor outputs and PLC<br>Use of DC/DC converter for isolation between analog inputs/transistor outputs and power supply<br>Isolation between channels |  |  |  |  |
| Co   | mpatible CPU module  | FX5U, FX5UC, compatible from initial product<br>Connection with FX5U or FX5UC requires FX5-CNV-BUS or FX5-CNV-BUSC.  |  |  |  |  |
| Nu   | mber of occupied input/output points                           | 8 points (Either input or output is  | available for counting.)   |  |  |  |
| Co   | mmunication with PLC   | Carried out by FROM/TO instruc   | tion via buffer memory (buffer memory can directly be specified)   |  |  |  |
| Nu   | mber of connectable modules                                    | FX5U : Up to 8 modules when FX3U extension power supply modules are used<br>Up to 6 modules when FX3U extension power supply modules are not used<br>FX5UC: Up to 6 modules  |  |  |  |  |
| Ext  | ternal dimensions $W \times H \times D$ (mm)                   | 90 × 90 × 86   |  |  |  |  |
| MA   | ASS (Weight): kg   | Approx. 0.4  |  |  |  |  |

\*1: Differs depending on the sensor input range.

\*2: To stabilize the measurement accuracy, warm-up (supply power) the system for 30 minutes or more after power-on.

# **High speed counter**

Using high-speed counters allow PLC to capture high-speed signals from encoders and sensors. Since the CPU module has built-in high performance high-speed counters, high-speed control is possible with simple programs.

# List of high-speed counters

# ♦ Built-in high-speed counter functions of CPU module

| Model   | Model                           | Maximum frequency | Operation mode  | High-speed processing instruction   |
|---|---------------------------------|-------------------|---|---|
| FX5U/FX5UC  | 1 phase, 1 input (S/W)          | 200 kHz           |   |   |
| Annual | 1 phase, 1 input (H/W)          | 200 kHz           |   | - 32-bit data comparison set  |
|   | 1 phase, 2 input                | 200 kHz           | - Normal mode<br>- Pulse density                        | <ul> <li>- 32-bit data comparison reset</li> <li>- 32-bit data band comparison</li> </ul> |
| 1000  | 2 phase, 2 input [1 edge count] | 200 kHz           | measurement mode  | - 16-bit data high-speed input/output   |
|   | 2 phase, 2 input [2 edge count] | 100 kHz           | <ul> <li>Rotation speed<br/>measurement mode</li> </ul> | function start/stop<br>- 32-bit data high-speed input/output                              |
| Ô.  | 2 phase, 2 input [4 edge count] | 50 kHz            |   | function start/stop   |
|   | Internal clock                  | 1 MHz (fixed)     |   |   |

 $\boldsymbol{\star}:$  For details, refer to the programming manual and hardware manual of each product.

# ♦ High-speed counter of FX5U/FX5UC CPU module

High speed counters use parameters to make input allocation and function settings and use HIOEN instruction to perform operations.

| Types of high-spe        | ed counters    | Pulse input signal type   |
|--------------------------|----------------|---|
| 1 phase, 1 input co      | ountor (SAMA   | Phase A Input ON OFF  |
| i pliase, i input o      | Junier (J/VV)  | Counting Direction OFF ON<br>Switching Bit                              |
| 1 phase, 1 input co      | ountor (HAAA   | Phase A Input ON OFF  |
| i phase, i input o       | Dunter (m/ vv) | Phase B Input<br>(input for switching the OFF ON<br>counting direction) |
| 1 phase 2 input o        | ounter         | Phase A Input<br>(Up-Counting Input<br>from OFF to ON: +1) OFF          |
| 1 phase, 2 input counter |                | Phase B Input<br>(Down-Counting Input)<br>(from OFF to ON: -1) OFF      |
|                          |                | At Up-Counting At Down-Counting   |
|                          | 1 edge         | Phase A Input   |
|                          | count          | +1+1/-1/-1  |
|                          |                | Phase B Input   |
|                          |                | At Up-Counting At Down-Counting   |
| 2 phase, 2 input         | 2 edge         | Phase A Input   |
| counter                  | count          | +1 +1 -1/ (-1   |
|                          |                | Phase B Input   |
|                          |                | At Up-Counting At Down-Counting   |
|                          | 4 edge         | Phase A Input   |
|                          | count          |   |
|                          |                | Phase B Input   |
|                          |                | Counting Direction Switching BitOFF ON                                  |
| Internal clock           |                | ON<br>Internal Clock (1 MHz)  |
|                          |                |   |
|                          |                | II  |

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#### ◇ Built-in high-speed counter input allocation

Parameter is used to set the input device allocation of high-speed counters.

Parameter is used to set the function for each channel, and input device allocation is determined by the settings. When internal clock is used, the allocation is the same as that of 1 phase, 1 input (S/W), without using phase A.

| СН               | Type of<br>high-speed counter | X0 | X1 | X2 | X3 | X4 | X5 | X6 | X7  | X10  | X11 | X12 | X13 | X14 | X15 | X16 | X17 |
|------------------|-------------------------------|----|----|----|----|----|----|----|-----|------|-----|-----|-----|-----|-----|-----|-----|
|                  | 1 phase, 1 input (S/W)        | А  |    |    |    |    |    |    |     | Р    | E   |     |     |     |     |     |     |
|                  | 1 phase, 1 input (H/W)        | А  | В  |    |    |    |    |    |     | Р    | E   |     |     |     |     |     |     |
| CH1              | 1 phase, 2 input              | А  | В  |    |    |    |    |    |     | Р    | E   |     |     |     |     |     |     |
|                  | 2 phase, 2 input              | А  | В  |    |    |    |    |    |     | Р    | E   |     |     |     |     |     |     |
|                  | 1 phase, 1 input (S/W)        |    | A  |    |    |    |    |    |     |      |     | Р   | E   |     |     |     |     |
| CH2              | 1 phase, 1 input (H/W)        |    |    | A  | В  |    |    |    |     |      |     | Р   | E   |     |     |     |     |
|                  | 1 phase, 2 input              |    |    | A  | В  |    |    |    |     |      |     | Р   | E   |     |     |     |     |
|                  | 2 phase, 2 input              |    |    | A  | В  |    |    |    |     |      |     | Р   | E   |     |     |     |     |
|                  | 1 phase, 1 input (S/W)        |    |    | A  |    |    |    |    |     |      |     |     |     | Р   | E   |     |     |
| СНЗ              | 1 phase, 1 input (H/W)        |    |    |    |    | Α  | В  |    |     |      |     |     |     | Р   | E   |     |     |
| 003              | 1 phase, 2 input              |    |    |    |    | A  | В  |    |     |      |     |     |     | Р   | E   |     |     |
|                  | 2 phase, 2 input              |    |    |    |    | A  | В  |    |     |      |     |     |     | Р   | E   |     |     |
|                  | 1 phase, 1 input (S/W)        |    |    |    | A  |    |    |    |     |      |     |     |     |     |     | Р   | E   |
| CH4              | 1 phase, 1 input (H/W)        |    |    |    |    |    |    | Α  | В   |      |     |     |     |     |     | Р   | E   |
| 0П4              | 1 phase, 2 input              |    |    |    |    |    |    | A  | В   |      |     |     |     |     |     | Р   | E   |
|                  | 2 phase, 2 input              |    |    |    |    |    |    | Α  | В   |      |     |     |     |     |     | Ρ   | E   |
|                  | 1 phase, 1 input (S/W)        |    |    |    |    | A  |    |    |     | Р    | E   |     |     |     |     |     |     |
| CH5              | 1 phase, 1 input (H/W)        |    |    |    |    |    |    |    |     | A    | В   | Р   | E   |     |     |     |     |
|                  | 1 phase, 2 input              |    |    |    |    |    |    |    |     | A    | В   | Р   | E   |     |     |     |     |
|                  | 2 phase, 2 input              |    |    |    |    |    |    |    |     | A    | В   | Р   | E   |     |     |     |     |
|                  | 1 phase, 1 input (S/W)        |    |    |    |    |    | A  |    |     |      |     | Р   | E   |     |     |     |     |
| CH6              | 1 phase, 1 input (H/W)        |    |    |    |    |    |    |    |     |      |     | A   | В   | Р   | E   |     |     |
|                  | 1 phase, 2 input              |    |    |    |    |    |    |    |     |      |     | A   | В   | Р   | E   |     |     |
|                  | 2 phase, 2 input              |    |    |    |    |    |    |    |     |      |     | А   | В   | Р   | E   |     |     |
|                  | 1 phase, 1 input (S/W)        |    |    |    |    |    |    | A  |     |      |     |     |     | Р   | E   |     |     |
| CH7              | 1 phase, 1 input (H/W)        |    |    |    |    |    |    |    |     |      |     |     |     | A   | В   | Ρ   | E   |
|                  | 1 phase, 2 input              |    |    |    |    |    |    |    |     |      |     |     |     | A   | В   | Ρ   | E   |
|                  | 2 phase, 2 input              |    |    |    |    |    |    |    |     |      |     |     |     | A   | В   | Ρ   | E   |
|                  | 1 phase, 1 input (S/W)        |    |    |    |    |    |    |    | A   |      |     |     |     |     |     | Р   | E   |
| CH8              | 1 phase, 1 input (H/W)        |    |    |    |    |    |    |    |     |      |     |     |     |     |     | А   | В   |
|                  | 1 phase, 2 input              |    |    |    |    |    |    |    |     |      |     |     |     |     |     | А   | В   |
|                  | 2 phase, 2 input              |    |    |    |    |    |    |    |     |      |     |     |     |     |     | А   | В   |
| CH1<br>to<br>CH8 | Internal clock                |    |    |    |    |    |    |    | Not | used |     |     |     |     |     |     |     |

A: Phase A input B: Phase B input (With 1 phase 1 input (H/W), however, direction switching input is made.) P: External preset input (Use or nonuse can be selected for each channel using parameters.) E: External enable input (Use or nonuse can be selected for each channel using parameters.)

#### ◇ High-speed pulse input/output module

| Model          | Туре                               | Highest frequency | Operation mode | High-speed processing instruction  | Compatible | CPU module |
|----------------|------------------------------------|-------------------|----------------|--|------------|------------|
| IVIOUEI        | туре                               |                   | Operation mode | High-speed processing instruction  | FX5U       | FX5UC      |
| FX5-16ET/ES-H  | 1 phase, 1 input (S/W)             | 200 kHz           |                |  |            |            |
| FX5-16ET/ESS-H | 1 phase, 1 input (H/W)             | 200 kHz           |                |  |            |            |
| 151 June 191   | 1 phase, 2 input                   | 200 kHz           |                |  | 0          | 0*         |
|                | 2 phase, 2 input<br>[1 edge count] | 200 kHz           | - Normal mode  | <ul> <li>16-bit data high-speed input/output function<br/>start/stop</li> <li>32-bit data high-speed input/output function<br/>start/stop</li> </ul> |            |            |
|                | 2 phase, 2 input<br>[2 edge count] | 100 kHz           |                |  |            |            |
|                | 2 phase, 2 input<br>[4 edge count] | 50 kHz            |                |  |            |            |
|                | Internal clock                     | 1 MHz (fixed)     |                |  |            |            |

\*: Connection with FX5UC requires FX5-CNV-IFC or FX5-C1PS-5V.

#### ◇ Input assignment and the maximum frequency for each input assignment of the high-speed pulse input/output module

"
—" of each input represents the prefix input number of the high-speed pulse input/output module. "X $\square$ +6" and "X $\square$ +7" are input frequency up to 10 kHz without regard to the maximum frequency value. The "preset" input and "enable" input are input frequency up to 10 kHz without regard to the maximum frequency value.

| СН          | High-speed counter type         | X□       | X□+1 | X□+2 | X□+3 | X□+4 | X□+5 | X□+6 | X□+7 | Maximum<br>frequency |
|-------------|---------------------------------|----------|------|------|------|------|------|------|------|----------------------|
|             | 1 phase, 1 input (S/W)          | A        | Р    |      |      |      |      | E    |      | 200 kHz              |
| СН9,        | 1 phase, 1 input (H/W)          | A        | В    | Р    |      |      |      | E    |      | 200 kHz              |
| CH11,       | 1 phase, 2 input                | A        | В    | Р    |      |      |      | E    |      | 200 kHz              |
| CH13,       | 2 phase, 2 input [1 edge count] | A        | В    | Р    |      |      |      | E    |      | 200 kHz              |
| CH15        | 2 phase, 2 input [2 edge count] | A        | В    | Р    |      |      |      | E    |      | 100 kHz              |
|             | 2 phase, 2 input [4 edge count] | A        | В    | Р    |      |      |      | E    |      | 50 kHz               |
|             | 1 phase, 1 input (S/W)          |          |      |      | A    | Р    |      |      | E    | 200 kHz              |
| CH10,       | 1 phase, 1 input (H/W)          |          |      |      | A    | В    | Р    |      | E    | 200 kHz              |
| CH12,       | 1 phase, 2 input                |          |      |      | A    | В    | Р    |      | E    | 200 kHz              |
| CH14,       | 2 phase, 2 input [1 edge count] |          |      |      | A    | В    | Р    |      | E    | 200 kHz              |
| CH16        | 2 phase, 2 input [2 edge count] |          |      |      | A    | В    | Р    |      | E    | 100 kHz              |
|             | 2 phase, 2 input [4 edge count] |          |      |      | A    | В    | Р    |      | E    | 50 kHz               |
| CH9 to CH16 | Internal clock                  | Not used |      |      |      |      |      |      |      |                      |

A: Phase A input

B: Phase A input (For 1-phase 1-input (H/W): direction change input) P: External "preset" input (Use or nonuse can be selected for each channel using parameters.)

E: External "enable" input (Use or nonuse can be selected for each channel using parameters.)

# ◇ High-speed counter module

| Model<br>(Number of | Туре               | Highest response frequency  | Function   | Hardware comparison | 2-phase counter<br>edge count | Compatible CPU module |                       |  |
|---------------------|--------------------|---|--|---------------------|-------------------------------|-----------------------|-----------------------|--|
| channels)           |                    |   |  | output function     | function                      | FX5U                  | FX5UC                 |  |
| FX3U-2HC (2 ch)     | 1 phase<br>1 input | Max. 200 kHz  |  |                     |                               |                       |                       |  |
|                     | 1 phase<br>2 input | Max. 200 kHz  | With match output (delay of up<br>to 30 µs) function<br>Output type: Output common<br>to sink/source<br>2 points/channel | 0                   | _                             | O*<br>Up to 2 modules | O*<br>Up to 2 modules |  |
|                     | 2 phase<br>2 input | 1 edge count: Max. 200 kHz<br>2 edge count: Max. 100 kHz<br>4 edge count: Max. 50 kHz |  |                     | 0                             |                       |                       |  |

\*: Connection with FX5U or FX5UC requires FX5-CNV-BUS or FX5-CNV-BUSC.

# FX3U-2HC type high-speed counter module

#### ◇ Features



- Input of 2-ch high-speed signal can be made in a module to count a maximum of 200 kHz. Each channel is equipped with 2 high-speed output terminal points based on the setting of comparison value received from CPU module.
- 2) In 2-phase input, 1/2/4 edge count mode can be set.
- 3) Counting can be permitted/inhibited in CPU module or external input.
- Connection with an encoder of line driver output type can be made.
- 5) I/O signal connection adopts a connector system and is compact.

### $\diamond$ Specifications

| Items  | Specifications   |
|--|--|
| No. of input points                            | 2 points   |
| Signal level                                   | According to connection terminals, 5 V DC, 12 V DC and 24 V DC are selectable.<br>The line driver output type is connected to the 5 V terminal.  |
| Frequency                                      | 1 phase, 1 input: 200 kHz or less<br>1 phase, 2 input: 200 kHz or less<br>2 phase, 2 input: 200 kHz or less/1 edge count, 100 kHz or less/2 edge count,<br>50 kHz or less/4 edge count |
| Counting range                                 | Binary signed 32 bits (-2,147,483,648 to +2,147,483,647) or binary unsigned 16 bits (0 to 65,535)  |
| Count mode                                     | Automatic up/down (with 1 phase 2 input or 2 phase input, or selected up/down (with 1 phase 1 input)   |
| Match output                                   | When the current value of the counter matches a comparison set value, comparison output is set within 30 $\mu s$ (ON), and cleared (OFF) within 100 $\mu s$ by reset instruction.      |
| Output type                                    | 2 points/ch, 5 to 24 V DC 0.5 A (output common to sink/source)   |
| Additional function                            | Buffer memory is available to set mode and comparison data from the CPU module.<br>Current value, comparison results, and error status can be monitored via the CPU<br>module.         |
| Current consumption                            | 5 V DC 245 mA (Internal power supply)  |
| Compatible CPU module                          | FX5U, FX5UC, compatible with initial product or later<br>Connection with FX5U or FX5UC requires FX5-CNV-BUS or FX5-CNV-BUSC.   |
| Number of occupied<br>input/output points      | 8 points (Either input or output is available for counting.)   |
| Communication with PLC                         | Carried out by FROM/TO instruction via buffer memory (buffer memory can directly<br>be specified)  |
| Number of connectable modules                  | FX5U, FX5UC: Up to 2 modules   |
| External dimensions $W \times H \times D$ (mm) | 55 × 90 × 87   |
| MASS (Weight): kg                              | Approx. 0.2  |

#### $\Diamond$ Option

Connector for discrete wires (40-pin)

| Model name     | Туре   |
|----------------|--|
| FX-I/O-CON2-S  | Connector for single wires<br>AWG22 (0.3 mm <sup>2</sup> ) |
| FX-I/O-CON2-SA | Connector for single wires<br>AWG20 (0.5 mm²)              |

External device connection connectors and connection cables etc. are not included with the product. Please arrange them by the customer.

# FX5-16ET/E -H type high-speed pulse input/output module

#### ◇ Features



- 1) Input of high-speed pulses can be counted (2 ch, 200 kHz).
- 2) The high-speed counter function and the positioning function can be used together (2 ch + 2 axes). The terminals not assigned can be used as general-purpose input/ output.

#### ♦ Specifications

| Items  |                  | Specifications  |
|--|------------------|---|
| High-speed pulse                               | input            | 2 ch  |
| Input response                                 | X□ to X□+5*      | 200 kHz   |
| frequency                                      | X□+6, X□+7*      | 10 kHz  |
| Power supply                                   |                  | 5 V DC, 100 mA (internal power supply)<br>24 V DC, 125 mA (supplied from service power supply or external power supply) |
| Compatible CP                                  | J module         | FX5U, FX5UC from Ver. 1.030<br>Connection with FX5UC requires FX5-CNV-IFC or FX5-C1PS-5V.                               |
| Number of conr                                 | nectable modules | FX5U, FX5UC: Up to 4 modules  |
| External dimensions $W \times H \times D$ (mm) |                  | 40 × 90 × 83  |
| MASS (Weight): kg                              |                  | Approx. 0.25  |

\*: "□" represents the prefix input number of each high-speed pulse input/output module.

# **Positioning control**

In addition to CPU module built-in positioning instructions, a pulse output module has been prepared to achieve full-scale positioning control. Furthermore, simple motion modules, which can perform complicated control as well as even multi-axis/interpolation control, are lined up to support positioning control.

# List of positioning control

# $\diamond$ Built-in pulse output function of CPU module

|            | Model/feature  | Items                    | Function  |
|------------|--|--------------------------|---|
| module     | FX5U/FX5UC   | Number of control axes   | 4 axes* (Simple linear interpolation by 2-axis simultaneous start)  |
| f CPU n    | A ADD THE ADD  | Maximum frequency        | 2147483647 (200 kpps in pulses)   |
| function o |  | Positioning program      | Sequence program, Table operation   |
| output fu  | The module is equipped with positioning function for | Compatible CPU module    | Transistor output type  |
| pulse o    | 4-axis pulse output and 8-ch input.                  | Pulse output instruction | PLSY and DPLSY instructions   |
| Built-in   |  | Positioning instruction  | DSZR, DDSZR, DVIT, DDVIT, TBL, DRVTBL, DRVMUL, DABS, PLSV, DPLSV, DRVI, DDRVI, DRVA, and DDRVA instructions |

 $\boldsymbol{\star}$  : The number of control axes is 2 when the pulse output mode is CW/CCW mode.

# $\diamond$ High-speed pulse input/output module

|              | Model/feature   | Items                    | Function   | Compatible CPU module |       |
|--------------|---|--------------------------|--|-----------------------|-------|
|              | Model/leature   | items                    | FUNCTION   |                       | FX5UC |
| module       | FX5-16ET/ES-H<br>FX5-16ET/ESS-H   | Number of control axes   | 2 axes (Simple linear interpolation by 2-axis simultaneous start)  |                       |       |
| nt mod       | 111 m   | Maximum frequency        | 2147483647 (200 kpps in pulses)  |                       |       |
| input/output |   | Positioning program      | Sequence program, Table operation  |                       |       |
|              |   | Output type              | FX5-16ET/ES-H: Transistor output (Sink type)   | 0                     | 0*    |
| pulse        |   | Output type              | FX5-16ET/ESS-H: Transistor output (Source type)  |                       |       |
| speed        | Up to 200 kpps pulse output is possible.<br>Because various positioning operation modes are | Pulse output instruction | -  |                       |       |
| High-s       | supported, the module is suitable for 2-axis simple positioning.                            | Positioning instruction  | DSZR, DDSZR, DVIT, DDVIT, DRVTBL, DRVMUL,<br>DABS, PLSV, DPLSV, DRVI, DDRVI, DRVA, and<br>DDRVA instructions |                       |       |

\*: Connection with FX5UC requires FX5-CNV-IFC or FX5-C1PS-5V.

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# $\diamond$ Pulse output module

| Model/feature       |  | Items                                  | Function  |   | Compatible | CPU module |
|---------------------|--|--|---|---|------------|------------|
|                     |  | ILEITIS                                | FX5-20PG-P  | FX5-20PG-D                                  | FX5U       | FX5UC      |
|                     | FX5-20PG-P<br>FX5-20PG-D   | Number of control axes                 | 2 axes  |   |            |            |
|                     |  | Interpolation                          | 2-axis linear interpolation, 2-axis circular interpolation  |   |            |            |
|                     |  | Output type                            | Transistor  | Differential driver                         |            |            |
|                     |  | Pulse output type                      | PULSE/SIGN mode, CW/<br>Phase A/B (4 multiplication)  | /CCW mode<br>, phase A/B (1 multiplication) |            |            |
|                     | Two-axis positioning module equipped with linear   | Command speed                          | 200 kpps  | 5 Mpps                                      | 0          | O*1        |
|                     | interpolation and circular interpolation. By analyzing the positioning data in advance, it can start the positioning at high speeds. | Control system                         | PTP (Point To Point) conti<br>linear and arc configurabl<br>position switching control<br>control |   | 0          | 0**        |
| e                   |  | Positioning program                    | Sequence program  |   |            |            |
| noou                |  | Positioning data                       | 600 data/axis   |   |            |            |
| Pulse output module |  | Number of occupied<br>I/O points       | 8 points (can be counted  | on either input or output)                  |            |            |
| lse o               | FX3U-1PG   | Number of control axes                 | 1 axis  |   |            |            |
| P                   |  | Interpolation function                 | _   |   |            |            |
|                     |  | Command speed                          | 200 kpps  |   |            |            |
|                     |  | Output type                            | Transistor  |   |            |            |
|                     | Up to 200 kpps pulse output is possible.<br>Because various positioning operation modes are  | Pulse output type                      | Forward rotation pulse/re<br>pulse train + direction  | verse rotation pulse, or                    | O*2        | O*2        |
|                     | supported the module is suitable for 1-axis simple positioning.  | Manual pulse generator connection      | _   |   |            |            |
|                     |  | Positioning program                    | Sequence program (FRO   | M/TO instruction)                           |            |            |
|                     |  | ABS current value read                 | Allowed by a sequence p   | rogram                                      |            |            |
|                     | Connection with EVELIC requires EVE CNIVIEC or EVE C1DS F  | Number of occupied input/output points | 8 points<br>(Either input or output is a  | available for counting.)                    |            |            |

\*1 : Connection with FX5UC requires FX5-CNV-IFC or FX5-C1PS-5V. \*2 : Connection with FX5U or FX5UC requires FX5-CNV-BUS or FX5-CNV-BUSC.

# **♦** Simple motion module

|                 | Mar   | 14 a se a                                      | Funct  | ion                                       | Compatible | CPU module |
|-----------------|---|--|--|---|------------|------------|
|                 | Model/feature   |  | FX5-40SSC-S  | FX5-80SSC-S                               | FX5U       | FX5UC      |
|                 | FX5-40SSC-S<br>FX5-80SSC-S  | Number of control axes                         | 4 axes   | 8 axes                                    |            |            |
|                 |   | Interpolation function                         | 2-axis, 3-axis, 4-axis linear<br>2-axis circular interpolation   |   |            |            |
| lle             |   | Control system                                 | PTP (Point To Point) control, Trajectory control (both<br>linear and arc), Speed control, Speed-position<br>switching control, Position-speed switching control,<br>Speed-torque control |   |            |            |
| high<br>.ug com | ince the module is compatible with SSCNET III/H,<br>igh-speed/high-precision positioning can be achieved in<br>ombination with MR-J4 servo motor.<br>'arameter settings and table operation settings can easily | Mark detection function                        | mode, Ring Buffer mode   | rk detection signal: up to 4 points, mark |            | O*1        |
| nple m          | be made with GX Works3.   | Digital oscilloscope<br>function* <sup>2</sup> | Bit data: 16 ch, Word data:  | 16 ch                                     |            |            |
| Sin             |   | Servo amplifier connection method              | SSCNET III/H   |   |            |            |
|                 |   | Manual pulse generator connection              | Possible to connect 1 mod  | lule                                      |            |            |
|                 |   | Positioning program                            | Sequence program   |   |            |            |
|                 |   | Number of occupied<br>input/output points      | 8 points<br>(Either input or output is av  | ailable for counting.)                    |            |            |

\*1: Connection with FX5UC requires FX5-CNV-IFC or FX5-C1PS-5V. \*2: 8 ch word data and 8 ch bit data can be displayed in real time.

# ♦ List of positioning operation modes

To confirm detailed operation of each module, refer to manuals of the product.

| Positioning instruction<br>Operation pattern  | Details  | FX5U,<br>FX5UC | FX5-16ET/ES-H,<br>FX5-16ET/ESS-H | FX5-20PG-P,<br>FX5-20PG-D | FX3U-1PG  | FX5-40SSC-S,<br>FX5-80SSC-S |
|---|--|----------------|----------------------------------|---------------------------|-----------|-----------------------------|
| JOG operation     Speed     JOG Speed     JOG Speed     Start     JOG     Command   | While the forward rotation/reverse rotation instruction input is ON, the motor performs forward rotation/reverse rotation.   | O<br>*1        | O<br>*1                          | 0                         | 0         | 0                           |
| ◆ Machine home position return Speed Home position return speed Origin Zero DOG Start   | The module starts operation at a home position return speed according to the machine home position return start instruction and then outputs clear signal after the end of machine home position return.   | O<br>*2        | 0<br>*2                          | O<br>*2*3                 | O<br>*2*3 | O<br>*2*4                   |
| <ul> <li>1-speed positioning</li> <li>Speed</li> <li>Operation Speed</li> <li>Start</li> <li>Target Position</li> </ul>   | The module starts operation at an operation speed according to start instruction and then stops at a target position.  | 0              | 0                                | 0                         | 0         | 0                           |
| 2-speed operation (2-speed positioning)<br>Speed Operation Speed (1)<br>Operation Speed (2)<br>Start Amount of Amount of<br>movement (1)<br>Amount of Comment (2)   | The module moves at operation speed<br>(1) for amount of movement (1) and then<br>moves at operation speed (2) for amount of<br>movement (2) according to start instruction.                               | O<br>*5        | O<br>*5                          | 0                         | 0         | 0                           |
| Multi-speed operation     Speed Operation Speed (2)     Operation Speed (3)     Start Anount of Amount of movement (2)     movement (2)     movement (2)     movement (2)     movement (2)     movement (3) | Multi-speed operation can be achieved by<br>performing continuous trajectory control of<br>multiple tables.<br>The diagram at left shows continuous<br>trajectory control of 3 tables.                     | 0<br>*5        | O<br>*5                          | 0                         | ×         | 0                           |
| Interrupt stop     Speed     Operation Speed     Start     Interrupt Input     Amount of movement   | The module starts operation according to<br>start instruction and then stops at the target<br>position.<br>When interrupt input is ON, the module<br>decelerates and stops.                                | 0              | 0                                | ×                         | 0         | ×                           |
| Interrupt and 1-speed positioning<br>(interrupt and 1-speed pitch feed) Speed Operation Speed Start Interrupt Input<br>Amount of movement   | When interrupt input is ON, the module<br>moves at the same speed for the specified<br>amount of movement, and then decelerates<br>and stops.  | 0              | 0                                | 0                         | 0         | 0                           |
| <ul> <li>Interrupt and 2-speed positioning<br/>(interrupt and 2-speed pitch feed)</li> <li>Speed</li> <li>Speed</li> <li>Speed</li> <li>Start</li> <li>Interrupt<br/>Input (1)</li> </ul>                   | When interrupt input (1) is ON, the module decelerates to the 2nd speed. When interrupt input (2) is ON again, the module moves only for the specified amount of movement, and then decelerates and stops. | 0<br>*6        | ○<br>*6                          | 0<br>*7                   | 0         | O<br>*7                     |

\* 1: Can be substituted by variable speed operation instruction.
\* 2: Dog search function available
\* 3: Count type, and data set type function available
\* 4: Count type, scale origin signal detection type, and data set type function available.
\* 5: Can be substituted by 1-speed positioning table operation.
\* 6: Can be substituted by variable speed operation or interrupt 1-speed positioning operation.
\* 7: Can be substituted by speed-position switching control and speed change function.

| Positioning instruction<br>Operation pattern  | Details   | FX5U,<br>FX5UC | FX5-16ET/ES-H,<br>FX5-16ET/ESS-H | FX5-20PG-P,<br>FX5-20PG-D | FX3U-1PG | FX5-40SSC-S,<br>FX5-80SSC-S |
|---|---|----------------|----------------------------------|---------------------------|----------|-----------------------------|
| <ul> <li>Interrupt 2-speed positioning<br/>(external instruction positioning)</li> <li>Speed Operation Speed (1)</li> <li>Operation Speed (2)</li> <li>Start Deceleration Step Command<br/>Command (DOG) (STOP Input)</li> </ul>          | The module starts operation at operation speed (1) according to start instruction and then starts decelerating according to deceleration instruction. The module performs operation at operation speed (2) until the input of stop instruction. | ○<br>*6        | ○<br>*6                          | ×                         | 0        | ×                           |
| ◆ Variable speed operation<br>Speed Operation Speed<br>↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓  | The module operates at the operation speed specified from PLC.  | 0              | 0                                | 0                         | 0        | 0                           |
| Linear interpolation       y Coordinate       y Coordinate       Target Position (x, y)       Start Point       x Coordinate  | The module moves to the target position at<br>the specified speed.<br>For the speed, composite speed and<br>reference axis speed are selectable.  | 0<br>*8        | 0<br>*8                          | 0                         | ×        | 0                           |
| ◆ Circular interpolation<br>CW Target Position (x, y)<br>Radius r<br>Point CCW Target<br>Position (x, y)<br>Start<br>Point CW Radius r<br>Position (x, y)<br>Radius r<br>Position (x, y)<br>Start<br>Point CCW Radius r<br>Solid Line:ccw | The module moves to the target position (x, y) at the peripheral speed according to circular interpolation instruction.<br>Operation can be performed according to sub point designation or center point designation.                           | ×              | ×                                | 0                         | ×        | 0                           |
| ◆ Table operation           No.         Position         Speed            1         200         500            2         500         1000            3         1000         2000  | A table is available to create a program for positioning control.   | 0              | 0                                | 0                         | ×        | 0                           |
| ◆ Pulse generator input operation   | External pulse can be input from the manual<br>pulse generator input terminal.<br>Synchronous ratio operation using an<br>encoder etc., can be performed.   | ×              | ×                                | 0                         | ×        | 0                           |

46: Can be substituted by variable speed operation or interrupt 1-speed positioning operation.
 \*8: Simple linear interpolation only.

# Built-in positioning function of FX5U/FX5UC CPU module

#### ◇ Features



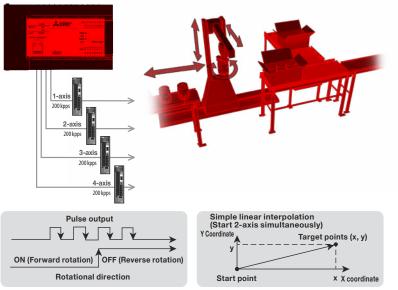
- Can position up to 4 axes using transistor outputs (Y0, Y1, Y2 and Y3) of the CPU module.
- 2) Can output pulse trains of 200 kpps maximum.
- Can realize a reasonable system configuration because the intelligent function module for positioning is not required.
- Change of the speed and positioning address can be made during positioning operation.
- 5) Supports the simple linear interpolation operation.

#### ♦ Specifications

| Items                    | Specifications   |
|--------------------------|--|
| Number of control axes   | 4 axes* (Simple linear interpolation possible by 2-axis simultaneous start)                                    |
| Maximum frequency        | 2147483647 (200 kpps in pulses)  |
| Positioning program      | Sequence program, Table operation  |
| Compatible CPU module    | Transistor output type   |
| Pulse output instruction | PLSY and DPLSY instructions  |
| Positioning instruction  | DSZR, DDSZR, DVIT, DDVIT, TBL, DRVTBL, DRVMUL, DABS, PLSV, DPLSV, DRVI,<br>DDRVI, DRVA, and DDRVA instructions |

\*: The number of control axes is 2 when the pulse output mode is CW/CCW mode.

[Example of Packaging System Using built-in positioning]



# FX5-16ET/ED-H type high-speed pulse input/output module

#### ◇ Features



- Can extend the high-speed counter function (2 channels) and positioning function (2 axes) at the same time, and realize a reasonable system configuration.
- 2) Offers easy extension in the same way as the positioning function built in the CPU module.
- 3) Can output pulse trains of 200 kpps maximum.
- Allows terminals not using the highspeed counter function or positioning function to be used for generalpurpose inputs/outputs.

# ♦ Specifications

| Itomo  | Constituent   |
|--|---|
| Items  | Specifications  |
| Number of control axes                         | 2 axes (Simple linear interpolation by 2-axis simultaneous start)   |
| Maximum frequency                              | 2147483647 (200 kpps in pulses)   |
| Positioning program                            | Sequence program, Table operation   |
| Output type                                    | FX5-16ET/ES-H: Transistor output (Sink type)<br>FX5-16ET/ESS-H: Transistor output (Source type)                         |
| Pulse output instruction                       | -   |
| Positioning instruction                        | DSZR, DDSZR, DVIT, DDVIT, DRVTBL, DRVMUL, DABS, PLSV, DPLSV, DRVI,<br>DDRVI, DRVA, and DDRVA instructions               |
| Power supply                                   | 5 V DC, 100 mA (internal power supply)<br>24 V DC, 125 mA (supplied from service power supply or external power supply) |
| Compatible CPU module                          | FX5U, FX5UC from Ver. 1.030<br>Connection with FX5UC requires FX5-CNV-IFC or FX5-C1PS-5V.                               |
| Number of connectable modules                  | FX5U, FX5UC: Up to 4 modules  |
| External dimensions $W \times H \times D$ (mm) | 40 × 90 × 83  |
| MASS (Weight): kg                              | Approx. 0.25  |

#### FX5-20PG-P type pulse train positioning module (transistor output) FX5-20PG-D type pulse train positioning module (differential driver output)

#### 



- By analyzing the positioning data in advance, the module can start the positioning at a higher speed than the normal positioning start.
- It can easily draw the smooth path by combining linear interpolation, 2-axis circular interpolation, and continuous path control in a table-type program.
- 3) Acceleration/deceleration processing can be selected from two methods of trapezoidal and S-shaped acceleration/deceleration, and four kinds each of acceleration time and deceleration time can be set. In the case of S-shaped acceleration/ deceleration, the S-character ratio can also be set.

#### $\bigcirc$ Specifications

|  | Specifi   | cations   |  |
|--|---|---|--|
| Items  | FX5-20PG-P  | FX5-20PG-D  |  |
| Number of control axes                         | 2 axes  |   |  |
| Control unit                                   | mm, inch, degree, pulse   |   |  |
| Output type                                    | Transistor  | Differential driver                               |  |
| Command speed                                  | 200 kpps  | 5 Mpps  |  |
| Pulse output                                   | Output signal: PULSE/SIGN mode, CW/<br>CCW mode, phase A/B (4 multiplication),<br>phase A/B (1 multiplication)<br>Output terminal: Transistor<br>5 to 24 V DC 50 mA or less | Differential driver equivalent to AM26C31         |  |
| External I/O<br>specifications                 | I /aro point signal P(-05 terminal: 5 V I)(-5 mA  |   |  |
| Power supply                                   | 24 V DC +20%, -15% 120 mA (external power supply)   | 24 V DC +20%, -15% 165 mA (external power supply) |  |
| Compatible CPU module                          | FX5U, FX5UC: Ver. 1.050 or later<br>Connection with FX5UC requires FX5-CNV-I  | FC or FX5-C1PS-5V.                                |  |
| Number of occupied<br>I/O points               | 8 points (Either input or output is available fo  | pr counting.)                                     |  |
| Number of connectable modules                  | FX5U: Up to 16 modules<br>FX5UC: Up to 16 modules, or up to 15 modules when using a powered I/O module  |   |  |
| External dimensions $W \times H \times D$ (mm) | 50 × 90 × 83  |   |  |
| MASS (Weight): kg                              | Approx. 0.2   |   |  |

#### $\Diamond$ Option

Connector for external devices (40-pin)

| Model name | Туре   |
|------------|--|
| A6CON1     | Soldered type (straight protrusion)                    |
| A6CON2     | Crimped type (straight protrusion)                     |
|            | Soldered type (both straight/inclined protrusion type) |

External device connection connectors and connection cables etc. are not included with the product. Please arrange them by the customer. 5

# FX3U-1PG type pulse output module

#### ◇ Features



- The module is equipped with
   7 operation modes necessary for simple positioning control.
- 2) Pulse train of up to 200 kpps can be output.
- Speed and target address can be changed during positioning operation to perform operation for each process.
- Approximate S-curve acceleration/ deceleration is supported. Smooth high-speed operation can be performed.

# ♦ Specifications

| Items  | Specifications  |
|--|---|
| Number of control axes                         | 1 axis  |
| Command speed                                  | 200 kpps (instruction unit can be selected from among 1 pps, cm/min, inch/min, and 10 deg/min)  |
| Set pulse                                      | <ul> <li>-2,147,483,648 to 2,147,483,647 (Instruction unit can be selected from pulse, μm,<br/>mdeg, 10<sup>-4</sup> inch.</li> <li>In addition, magnification can be set for position data.)</li> </ul>  |
| Pulse output                                   | Output signal format: Forward rotation (FP)/reverse rotation (RP) pulse or pulse (PLS)/<br>direction (DIR) can be selected.<br>Pulse output terminal: Transistor output<br>5 to 24 V DC, 20 mA or less (photo-coupler isolation, with indication of operation by LED) |
| External input/output specification            | Input: For STOP/DOG terminal, 24 V DC, 7 mA<br>For zero-point signal PG0 terminal, 5 to 24 V DC, 20 mA or less<br>Output: For each of FP (forward rotation), RP (reverse rotation), and CLR (clear) terminals,<br>5 to 24 V DC, 20 mA or less                         |
| Driving power                                  | For input signal: 24 V DC, 40 mA<br>For pulse output: 5 to 24 V DC, power consumption 35 mA or less   |
| Control power                                  | 5 V DC, 150 mA (supplied from PLC via extension cable)  |
| Compatible CPU module                          | FX5U, FX5UC, compatible from initial product<br>Connection with FX5U or FX5UC requires FX5-CNV-BUS or FX5-CNV-BUSC.   |
| Number of occupied input/output points         | 8 points (Either input or output is available for counting.)  |
| Communication with PLC                         | Carried out by FROM/TO instruction via buffer memory (buffer memory can directly be<br>specified)   |
| Number of connectable modules                  | FX5U       : Up to 8 modules when FX3U extension power supply modules are used<br>Up to 6 modules when FX3U extension power supply modules are not used<br>FX5UC : Up to 6 modules  |
| External dimensions $W \times H \times D$ (mm) | 43 × 90 × 87  |
| MASS (Weight): kg                              | Approx. 0.2   |

# Advanced synchronous control

FX5-40SSC-S and FX5-80SSC-S type simple motion modules are intelligent function modules compatible with SSCNET III/H. It can use a servo motor to perform positioning control via SSCNET III/H compatible servo amplifier. For positioning control, refer to the relevant manual.

# FX5-40SSC-S type simple motion module FX5-80SSC-S type simple motion module

#### 



FX5-40SSC-S and FX5-80SSC-S are equipped with the 4/8-axis positioning functions compatible with SSCNET III/H. By combining linear interpolation, 2-axis circular interpolation and continuous trajectory control in the program set with a table, a smooth trajectory can be easily drawn. In "synchronous control", "parameter for synchronous control" is set and synchronous control is started for each output axis to perform control in synchronization with the input axes (servo input axis, instruction generation axis\*1, and synchronous encoder axis).

\*1: The instruction generation axis is used only for instruction generation. It can be controlled independently as an axis connected to a servo amplifier. (It is not counted as a control axis.)

#### ◇ Specifications

|  |   |  | cations                                |  |  |  |  |  |
|--|---|--|--|--|--|--|--|--|
|  |   | FX5-40SSC-S  | FX5-80SSC-S                            |  |  |  |  |  |
| Number of c                            |   | 4 axes   | 8 axes                                 |  |  |  |  |  |
| Operation cy                           |   | 0.888 ms/1.777 ms  |  |  |  |  |  |  |
| Interpolation                          |   | Linear interpolation (maximum 4 axes)<br>PTP (Point To Point) control, Trajectory<br>control, Speed-position switching con<br>Speed-torque control | control (both linear and arc), Speed   |  |  |  |  |  |
| Acceleration                           | /deceleration process                   | Trapezoidal acceleration/deceleration,   | S-curve acceleration/ deceleration     |  |  |  |  |  |
| Synchronous                            | Input axis                              | Servo input axis, synchronous encode   | er axis, command generation axis       |  |  |  |  |  |
| control                                | Output axis                             | Cam shaft  |  |  |  |  |  |  |
|  | Number of<br>registration* <sup>2</sup> | Up to 64 cams  | Up to 128 cams                         |  |  |  |  |  |
| Cam control                            | Cam data type                           | Stroke ratio data type, Coordinate dat   | a type                                 |  |  |  |  |  |
|  | Cam auto-generation                     | Cam auto-generation for rotary cutter  |  |  |  |  |  |  |
| Control unit                           |   | mm, inch, degree, pulse  |  |  |  |  |  |  |
| Number of p                            | oositioning data                        | 600 data (positioning data No. 1 to 60<br>axis (Can be set with MELSOFT GX W   | 10)/<br>/orks3 or a sequence program.) |  |  |  |  |  |
| Backup                                 |   | Parameters, positioning data, and block (battery-less backup)  | k start data can be saved on flash RON |  |  |  |  |  |
|  | Linear control                          | 1-axis linear control, 2-axis linear inter<br>3-axis linear interpolation control, 4-ax<br>(Composite speed, Reference axis spe                    | is linear interpolation control*3      |  |  |  |  |  |
|  | Fixed-pitch feed control                | 1-axis fixed-pitch feed, 2-axis fixed-pitch feed, 3-axis fixed-pitch feed,<br>4-axis fixed-pitch feed*3  |  |  |  |  |  |  |
|  | 2-axis circular interpolation           | Sub point designation, center point designation  |  |  |  |  |  |  |
|  | Speed control                           | 1-axis speed control, 2-axis speed control*3, 3-axis speed control*3,<br>4-axis speed contro*3   |  |  |  |  |  |  |
| Positioning control                    | Speed-position<br>switching control     | INC mode, ABS mode   |  |  |  |  |  |  |
|  | Position-speed<br>switching control     | INC mode   |  |  |  |  |  |  |
|  | Current value change                    | Positioning data, Start No. for a current value changing   |  |  |  |  |  |  |
|  | NOP instruction                         | Provided   |  |  |  |  |  |  |
|  | JUMP instruction                        | Unconditional JUMP, Conditional JUMP<br>Provided   |  |  |  |  |  |  |
|  | High-level positioning control          | Block start, Condition start, Wait start,  | Simultaneous start, Repeated start     |  |  |  |  |  |
| Servo amplifie                         | er connection method                    | SSCNET III/H   |  |  |  |  |  |  |
| Maximum ov                             | erall cable distance [m]                | 400  |  |  |  |  |  |  |
| Maximum di<br>stations [m]             | stance between                          | 100  |  |  |  |  |  |  |
| 24 V DC ext<br>consumption             | ernal current<br>า                      | 250 mA   |  |  |  |  |  |  |
| Compatible CPU module                  |   | Compatible with FX5U and FX5UC, from their first released products   |  |  |  |  |  |  |
| Number of occupied input/output points |   |  |  |  |  |  |  |  |
| Communication with PLC                 |   | Carried out by FROM/TO instruction via buffer memory<br>(buffer memory can directly be specified)  |  |  |  |  |  |  |
| Number of c                            | connectable modules                     | FX5U: Up to 16 modules<br>FX5UC: Up to 16 modules, or up to 15 modules when using a powered I/O<br>module  |  |  |  |  |  |  |
|  |   | 50 × 90 × 83   |  |  |  |  |  |  |
| External dim<br>W × H × D (r           |   | 50 × 90 × 83   |  |  |  |  |  |  |

\*2: The number of registered cams varies depending on the memory capacity, cam resolution, and the number of coordinates.
 \*3: Only the reference axis speed is effective for the interpolation speed specification method.

# Advanced synchronous control

memo

90

# **Network/Communication**

MELSEC iQ-F Series can support not only high-speed networks like CC-Link but also other networks corresponding to control contents such as Ethernet , MODBUS, Sensor Solution, and PROFIBUS-DP.

In addition, communication function to easily establish simple data link between MELSEC iQ-F Series and to RS-232C and RS-485 devices is also supported.

# ♦ CC-Link

| Types   | Contents  | Total extension<br>length or<br>transmission<br>distance  | Station types                                 |     | oatible<br>module<br>FX5UC |
|---|---|---|---|-----|----------------------------|
| CC-Link V2 (CC-Link V2 system supported by MELSEC iQ-F Series master)           MELSEC         CC-Link master           Very Comparison         Partner manufacturer           Sensors, solenoid         valves etc.  | <ul> <li>Outline</li> <li>This is a CC-Link V2 system where</li> <li>MELSEC iQ-F Series is used as master station.</li> <li>CC-Link V2 system can be established</li> </ul>   |   | Master station<br>(FX5-CCL-MS)                | 0   | O*2                        |
| Termination resistance  | using just MELSEC iQ-F Series. Ver. 1.10 is<br>also supported.<br>• Scale<br>Remote I/O station: max. 14*1 modules<br>Intelligent device station or remote device<br>station: max. 14*1 modules   | Max.<br>1200 m  | Master station<br>(FX3U-16CCL-M)              | O*3 | O*3                        |
| MELSEC<br>iQ-F Series Intelligent<br>station CC-Link<br>remote I/O Mitsubishi<br>electric<br>inverter,<br>AC servo, etc.  | <ul> <li>Station, max. 14 an inducities</li> <li>Scope</li> <li>Distributed control and central management<br/>of lines, configuration of small-scale and<br/>high-speed network, etc.</li> </ul>   |   | Intelligent<br>device station<br>(FX3U-64CCL) | O*3 | O*3                        |
| CC-Link V2 (CC-Link V2 system with MELSEC iQ-R Series master)   | Outline     MELSEC IQ-F series can be connected as     an intelligent device station to the CC-Link     V2 system in which is the MELSEC IQ-R     series etc. is the master station.     Scale     Max. 64 modules  | Max.  | Intelligent<br>device station<br>(FX5-CCL-MS) | 0   | O*2                        |
| MELSEC<br>IQ-F Series station CC-Link<br>remote I/O Mitsubishi<br>electric<br>inverter,<br>AC servo, etc.   | <ul> <li>Scope</li> <li>Distributed control and central management<br/>of lines, information transfer from the host<br/>network, etc.</li> </ul>  | 1200 m  | Intelligent<br>device station<br>(FX3U-64CCL) | O*3 | O*3                        |
| CC-Link IE Field<br>For star connections<br>MELSEC<br>iQ-R Series<br>MeLSEC<br>Intelligent<br>device<br>Series<br>station<br>MELSEC<br>Intelligent<br>device<br>Series<br>station<br>MELSEC<br>Intelligent<br>device<br>Series<br>station<br>MELSEC<br>Series<br>MELSEC<br>Series<br>MELSEC<br>Series<br>MELSEC<br>MeLSEC<br>Series<br>MELSEC<br>Series<br>MELSEC<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>MELSEC<br>MELSEC<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series<br>Series | <ul> <li>Outline</li> <li>MELSEC iQ-F Series can be connected as<br/>intelligent device stations for the CC-Link<br/>IE field network system using MELSEC<br/>iQ-R series as master station.</li> <li>Scale</li> <li>Max. 121 modules (1 master station, 120<br/>slave stations)</li> <li>Scope</li> <li>Distributed control and central<br/>management of lines, information transfer<br/>from the host network, etc.</li> </ul> | Line topology:<br>12000 m (With<br>121 modules<br>connected)<br>Star topology:<br>Depending on<br>the system<br>configuration<br>Ring topology:<br>12100 m (With<br>121 modules<br>connected) | Intelligent<br>device station<br>(FX5-CCLIEF) | 0   | ○*2                        |
| CC-Link IE Field Network Basic MELSEC iQ-F series FX5-ENET PC etc.  | • Outline<br>CC-Link IE Field Network Basic is an FA<br>network utilizing general-purpose Ethernet.<br>Data communication is performed<br>periodically (cyclic transmission) using a<br>link device between the master station and<br>slave station.  | Depending on the system   | Master station<br>(FX5U, FX5UC)               | 0   | 0                          |
| HUB<br>Remote I/O for CC-Link<br>IE Field Network Basic AC servo etc.   | Scale     FX5U, FX5UC: Up to 16 modules     FX5-ENET: Up to 32 modules     Scope     Distributed control and centralized     management of lines, and exchange of     information with upper network     The maximum purplex is 8 whoe FX8U 46CC  | configuration   | Master station<br>(FX5-ENET)                  | 0   | O*2                        |

\*1: This number is applicable when FX5-CCL-MS is used as the master station. The maximum number is 8 when FX3U-16CCL-M is used as the master station.
 \*2: Connection with FX5UC requires FX5-CNV-IFC or FX5-C1PS-5V.
 \*3: Connection with FX5U/FX5UC requires FX5-CNV-BUS or FX5-CNV-BUSC.

# $\Diamond$ Ethernet

|  |   | Total extension<br>length or | Compatible CPU module |       |
|--|---|------------------------------|-----------------------|-------|
| Types  | Contents  |                              | FX5U                  | FX5UC |
| FX5U/FX5UC CPU Module<br>PC, etc.<br>[SLMP]<br>HUB<br>FX5U/FX5UC<br>FX5U/FX5UC<br>FX5U/FX5UC                                 | <ul> <li>Outline</li> <li>Ethernet port is built in.</li> <li>Setting is enabled from GX Works3.</li> <li>Protocol type</li> <li>Compatible with CC-Link IE Field Network</li> <li>Basic, MELSOFT connection, SLMP</li> <li>(3E frame), socket communications, communication protocol support, FTP</li> <li>server, MODBUS/TCP communication, SNTP client, Web server (HTTP), simple CPU communication function</li> <li>Scale</li> <li>1:n</li> <li>Scope</li> <li>Distributed control of lines, central management, data collection, program maintenance, etc.</li> </ul> | _                            | 0                     | 0     |
| FX5-ENET   | Outline Intelligent function module with built-in Ethernet port. Settings can be configured from GX Works3.     Protocol type Compatible with CC-Link IE Field Network Basic, socket communication     Scale 1:n     Scope Distributed control of lines, central management, data collection, etc.  | _                            | 0                     | 0*    |
| FX5-ENET/IP<br>FX5U/FX5UC HUB<br>HUB<br>EtherNet/IP devices<br>*: Connection with FX5UC requires FX5-CNV-IFC or FX5-C1PS-5V. | Outline     Intelligent function module with built-in     Ethernet port.     Settings can be configured from GX Works3     and EtherNet/IP Configuration Tool for     FX5-ENET/IP.     Protocol type     EtherNet/IP communication, socket     communication     Scale     1:n     Scope     Distributed control of lines, central     management, data collection, etc.  | _                            | 0                     | 0*    |

# ○ Simple CPU communication

|   |  | Total extension<br>length or | Compatible | CPU module |
|---|--|------------------------------|------------|------------|
| Турез   | Contents   |                              | FX5U       | FX5UC      |
| Simple CPU communication (with built-in Ethernet port)       FX5U/FX5UC       HUB       FX5U       Rn(En) CPU         QnUDV CPU | <ul> <li>Outline</li> <li>Transmit and receive data from a specified device at a specified timing using the built-in Ethernet function.</li> <li>Settings can be configured from GX Works3.</li> <li>Scale</li> <li>Max. 16 modules</li> <li>Scope</li> <li>Distributed control of lines, central management, data collection, etc.</li> </ul> | _                            | 0          | 0          |

### 

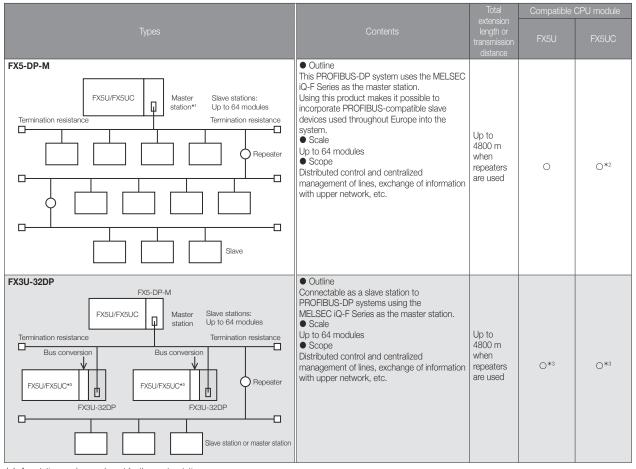
|  |  | Total extension | Compatible | CPU module |
|--|--|-----------------|------------|------------|
| Турез  | Contents   |                 | FX5U       | FX5UC      |
| FX5U/FX5UC CPU Module (built-in RS-485 port), FX5-485-BD   | <ul> <li>Outline</li> <li>Connectable from RS-485 to MODBUS by<br/>using FX5 as master or slave.</li> <li>Scale</li> <li>Max. 32 stations</li> <li>Scope</li> <li>Configuration of small-size and high-speed<br/>network, etc.</li> </ul>  | Max. 50 m       | 0          | 0*         |
| FX5-232ADP, FX5-232-BD   | <ul> <li>Outline</li> <li>Connectable from RS-232C to MODBUS by<br/>using FX5 as master or slave.</li> <li>Scale</li> <li>1:1</li> <li>Scope</li> <li>Data transfer from PCs, code readers,<br/>printers, various measurement devices, etc.</li> </ul>   | Max. 15 m       | 0          | 0*         |
| FX5-485ADP   | <ul> <li>Outline</li> <li>Connectable from RS-485 to MODBUS by<br/>using FX5 as master or slave.</li> <li>Scale</li> <li>Max. 32 stations</li> <li>Scope</li> <li>Distributed control of lines, central<br/>management, etc.</li> </ul>  | Max. 1200 m     | 0          | 0          |
| FX5U/FX5UC CPU module (with built-in Ethernet port)          FX5U/FX5UC       MELSEC         Master station       PC         Master station       PC         Master station       FX5U/FX5UC         FX5U/FX5UC       FX5U/FX5UC         Slave station       Slave station | <ul> <li>Outline</li> <li>Connections with the FX5 set as the slave station are possible via Ethernet connection to various MODBUS/TCP master devices.</li> <li>Scale</li> <li>Up to 8 connections</li> <li>Scope</li> <li>Distributed control of lines, central management, data collection, program maintenance, etc.</li> </ul> | _               | 0          | 0          |

 $\boldsymbol{*}\colon$  No expansion board can be used in FX5UC.

# ♦ Sensor Solution

|               |   | Total extension | Compatible | CPU module |
|---------------|---|-----------------|------------|------------|
| Турез         | Contents  |                 | FX5U       | FX5UC      |
| FX5-ASL-M     | Outline     This is the master module of the     AnyWireASLINK system.     A sensor saving wiring system of     AnyWireASLINK system can be constructed.     Scale     Max. 128 modules     Scope     Distributed control of lines, central     management of sensors, etc. | Max. 200 m      | 0          | O*1        |
| FX3U-128ASL-M | Outline     This is the master module of the     AnyWireASLINK system.     A sensor saving wiring system of     AnyWireASLINK system can be constructed.     Scale     Max. 128 modules     Scope     Distributed control of lines, central     management of sensors, etc. | Max. 200 m      | O*2        | O*2        |

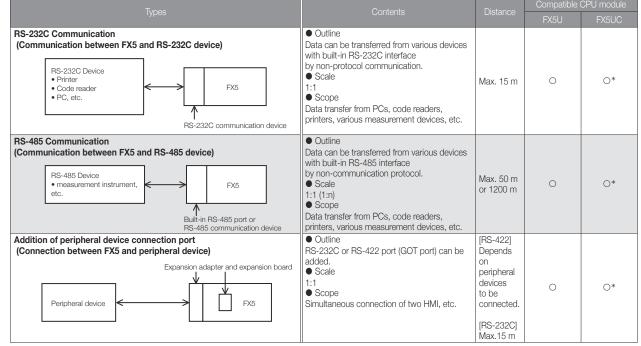
#### ◇ PROFIBUS-DP



\*1: Any station number can be set for the master station.

\*2: Connection with FX5UC requires FX5-CNV-IFC or FX5-C1PS-5V.
 \*3: Connection with FX5U/FX5UC requires FX5-CNV-BUS or FX5-CNV-BUSC.

# ♦ General-purpose communication/peripheral device communication



\*: No expansion board can be used in FX5UC

# $\Diamond$ Data link

|  |  | Total extension                       | Compatible | CPU module |
|--|--|---------------------------------------|------------|------------|
| Types  | Contents   | length or<br>transmission<br>distance | FX5U       | FX5UC      |
| N:N network (n:n connection)   | <ul> <li>Outline</li> <li>Enabling a simple data link between FX5 and FX3.</li> <li>Scale</li> <li>Max. 8 modules</li> <li>Scope</li> <li>Distributed control and central management of lines, etc.</li> </ul>   | Max. 50 m<br>or 1200 m                | 0          | 0*         |
| Parallel link<br>Built-in RS-485 port or RS-485 communication device   | <ul> <li>Outline</li> <li>With two FX5 PLCs connected, devices can be<br/>linked to each other. The data link is automatically<br/>updated between the two FX5 PLCs.</li> <li>Scale</li> <li>1:1</li> <li>Scope</li> <li>Distributed control and centralized control of<br/>small-scale lines</li> </ul>   | Max. 50 m<br>or 1200 m                | 0          | 0*         |
| MC protocol (1: n connection to external device)<br>RS-232C/<br>RS-485 converter<br>RS-485<br>RS-485<br>RS-485<br>RS-485<br>RS-485ADP<br>Built-in RS-485 port<br>RS-485-BD | <ul> <li>Outline</li> <li>FX5 can be connected as a slave station<br/>by setting an external device (PC, etc.) as a<br/>master station.</li> <li>Frame 1C: Compatible to Type 1/Type 4</li> <li>Frame 3C: Compatible to Type 1/Type 4</li> <li>Frame 4C: Compatible to Type 1/Type 4/Type 5</li> <li>Scale</li> <li>1:n (n = max. 16 modules)</li> <li>Scope</li> <li>Distributed control and central management<br/>of lines, etc.</li> </ul> | Max. 50 m<br>or 1200 m                | 0          | 0*         |
| MC protocol (1:1 connection to external device)  | <ul> <li>Outline</li> <li>FX5 can be connected as a slave station<br/>by setting an external device (PC, etc.) as a<br/>master station.</li> <li>Frame 1C: Compatible to Type 1/Type 4</li> <li>Frame 3C: Compatible to Type 1/Type 4</li> <li>Frame 4C: Compatible to Type 1/Type 4/Type 5</li> <li>Scale</li> <li>1:1</li> <li>Scope</li> <li>Data collection, central management, etc.</li> </ul>   | Max. 15 m                             | 0          | 0*         |

 $\boldsymbol{*}\colon$  No expansion board can be used in FX5UC.

# **CC-Link IE Field**

CC-Link IE Field is a high speed (1Gbps), high capacity open field network using Ethernet (1000BASE-T). FX5-CCLIEF is an intelligent function module to connect the FX5 CPU module as an intelligent device station to a CC-Link IE Field network.

# FX5-CCLIEF type CC-Link IE Field Network Intelligent device station module

#### ◇ Features



MELSEC iQ-F Series modules can be connected as intelligent device stations in the CC-Link IE Field network.

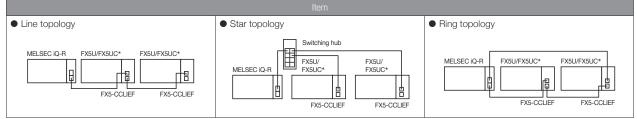
#### ♦ Specifications

| Items                  |                            | Specifications   |
|------------------------|----------------------------|--|
| Station type           |                            | Intelligent device station   |
| Station number         |                            | 1 to 120 (set by parameter or program)   |
| Communication speed    | 1                          | 1 Gbps   |
| Network topology       |                            | Line topology, star topology (coexistence of line topology and<br>star topology is also possible), and ring topology |
| Maximum station-to-s   | tation distance            | 100 m(conforms to ANSI/TIA/EIA-568-B (Category 5e))  |
| Cascade connection     |                            | Max. 20 stages   |
| Communication metho    | od                         | Token passing  |
|                        | RX                         | 384 points, 48 bytes   |
| Maximum number of      | RY                         | 384 points, 48 bytes   |
| link points*1          | RWr                        | 1024 points, 2048 bytes*2  |
|                        | RWw                        | 1024 points, 2048 bytes*2  |
| Compatible CPU mod     | ule                        | FX5U, FX5UC*3 from Ver. 1.030  |
| Applicable engineering | tool                       | GX Works3 Ver. 1.025B or later   |
| Number of occupied I/  | O points                   | 8 points (Either input or output is available for counting.)   |
| Communication with F   | PLC                        | Done by FROM/TO instruction via buffer memory<br>(buffer memory can be directly specified)                           |
| Number of connectabl   | e modules                  | FX5U, FX5UC: Max. 1 module   |
| Power supply           |                            | 5 V DC 10 mA (internal power supply)<br>24 V DC 230 mA (external power supply)                                       |
| External dimensions W  | $/ \times H \times D$ (mm) | 50 × 90 × 103  |
| MASS (Weight): kg      | . ,                        | Approx. 0.3  |
| k 1. The meyimum pum   | oor of link points that    | a master station can assign to one EX5- CCLIEE module  |

\*1: The maximum number of link points that a master station can assign to one FX5- CCLIEF module. \*2: 256 points (512 bytes) when the mode of the master station is online (High-Speed Mode).

#### \*3: Connection with the FX5UC requires FX5-CNV-IFC or FX5-C1PS-5V.

#### Network topology



\*: Connection with FX5UC requires FX5-CNV-IFC or FX5-C1PS-5V.

# CC-Link V2

CC-Link V2 is an open network enabling connection of various FA equipment. A master module to set MELSEC iQ-F Series as CC-Link master, as well as an interface to connect as a CC-Link slave are available.

# FX5-CCL-MS type CC-Link system master/intelligent device module

#### ◇ Features



- 1) Since this module has both functions, the master station and intelligent device station, it can be used as either of them by switching with parameters.
- 2) When using the module as an intelligent device station, the transmission speed can be set to auto-tracking. Since the module tracks the transmission speed of the master station automatically, there is no setting mistake.
- 3) Supporting the other station access function, the module can use GX Works3 connected to the local station to monitor program writing and reading and devices of PLCs of other stations in the same network. This function thus eliminates the need for connecting GX Works3 to individual MELSEC iQ-F series and reduces man-hours.

# ◇ Specifications

|  |   |   | Specifications  |   |                                  |  |                                  |                                      |                                  |                                      |                                  |
|--|---|---|---|---|----------------------------------|--|----------------------------------|--------------------------------------|----------------------------------|--------------------------------------|----------------------------------|
| Compatible   | e functions                                       | Master station of   | Master station or intelligent device station  |   |                                  |  |                                  |                                      |                                  |                                      |                                  |
| CC-Link su   | upported version                                  | Ver. 2.00 and Ve  | /er. 2.00 and Ver. 1.10   |   |                                  |  |                                  |                                      |                                  |                                      |                                  |
| Transmissi   | on Speed  |   | Master station: 156 kbps/625 kbps/2.5 Mbps/5 Mbps/10 Mbps     Intelligent device station: 156 kbps/625 kbps/2.5 Mbps/5 Mbps/10 Mbps/auto-tracking |   |                                  |  |                                  |                                      |                                  |                                      |                                  |
| Station nur  | mber  | <ul> <li>Master station</li> </ul>  | n: O •  | Intelligent devic                       | e station: 1 to 6                | 4  |                                  |                                      |                                  |                                      |                                  |
|  | ble station type<br>e of master station)          | Remote I/O stat   | ion, remote dev   | rice station, intelli                   | igent device sta                 | tion (local station                      | and standby m                    | aster station can                    | not be connecte                  | d)                                   |                                  |
| Maximum o  | overall cable length                              | 1200 m (varies of   | depending on tr   | ansmission spee                         | d)                               |  |                                  |                                      |                                  |                                      |                                  |
|  | number of connected the time of master            | • The total num   |   |   |                                  | oints of remote l/<br>e stations: 14 max |                                  |                                      | oints of intelliger              | nt device station -                  | ⊦ remote                         |
|  | occupied stations (at intelligent device station) | 1 to 4 stations (   | changed accord  | ding to the setting                     | g of engineering                 | tool)                                    |                                  |                                      |                                  |                                      |                                  |
| Maximum<br>number<br>of link   | CC-Link Ver. 1                                    | Remote regist     Remote regist   | er (RWw): 56 po<br>er (RWr): 56 poi   | pints<br>ints                           |                                  | ts <sup>*3</sup> + remote de             |                                  |                                      |                                  | ,                                    |                                  |
| points per<br>system*5   | CC-Link Ver. 2                                    | <ul> <li>Remote I/O (F</li> <li>Remote regist</li> <li>Remote regist</li> </ul> | er (RWw): 112 p   | points                                  | tation: 448 poin                 | ts <sup>*3</sup> + remote de             | vice stations an                 | d intelligent devid                  | e stations: 448 p                | points)                              |                                  |
|  |   | CC Link   | Vor 1   | CC-Link Ver. 2                          |                                  |  |                                  |                                      |                                  |                                      |                                  |
|  | Extended cyclic setting                           | CC-Link Ver. 1  |   | Single Double                           |                                  | ible                                     | Quadruple                        |                                      | Octuple                          |                                      |                                  |
|  | Number of occupied stations                       | Remote I/O  | Remote<br>register  | Remote I/O                              | Remote<br>register               | Remote I/O                               | Remote<br>register               | Remote I/O                           | Remote<br>register               | Remote I/O                           | Remote<br>register               |
| Number   | 1 station occupied                                | RX, RY: 32 points<br>(16 points)*4  | RWw: 4 points<br>RWr: 4 points  | RX, RY: 32 points<br>(16 points)*4      | RWw: 4 points<br>RWr: 4 points   | RX, RY: 32 points<br>(16 points)*4       | RWw: 8 points<br>RWr: 8 points   | RX, RY: 64 points<br>(48 points)*4   | RWw: 16 points<br>RWr: 16 points | RX, RY: 128 points<br>(112 points)*4 | RWw: 32 points<br>RWr: 32 points |
| of link<br>points <sup>*5</sup>  | 2 stations occupied                               | RX, RY: 64 points<br>(48 points)*4  | RWw: 8 points<br>RWr: 8 points  | RX, RY: 64 points<br>(48 points)*4      | RWw: 8 points<br>RWr: 8 points   | RX, RY: 96 points<br>(80 points)*4       | RWw: 16 points<br>RWr: 16 points | RX, RY: 192 points<br>(176 points)*4 | RWw: 32 points<br>RWr: 32 points | RX, RY: 384 points<br>(368 points)*4 | RWw: 64 points<br>RWr: 64 points |
|  | 3 stations occupied                               | RX, RY: 96 points<br>(80 points)*4  | RWw: 12 points<br>RWr: 12 points  | RX, RY: 96 points<br>(80 points)*4      | RWw: 12 points<br>RWr: 12 points | RX, RY: 160 points<br>(144 points)*4     | RWw: 24 points<br>RWr: 24 points | RX, RY: 320 points<br>(304 points)*4 | RWw: 48 points<br>RWr: 48 points |                                      |                                  |
|  | 4 stations occupied                               | RX, RY: 128 points<br>(112 points)*4  | RWw: 16 points<br>RWr: 16 points  | RX, RY: 128 points<br>(112 points)*4    | RWw: 16 points<br>RWr: 16 points | RX, RY: 224 points<br>(208 points)*4     | RWw: 32 points<br>RWr: 32 points | RX, RY:<br>448 points (-)*4          | RWw, RWr:<br>64 points(-)*4      |                                      |                                  |
| Transmissi   | on cable  |   |   | C-Link dedicated                        | d cable                          |  |                                  |                                      |                                  |                                      |                                  |
| Compatible   | e CPU module                                      | FX5U, FX5UC:<br>Connection with   |   | er<br>es FX5-CNV-IFC (                  | or FX5-C1PS-5\                   | Ι.                                       |                                  |                                      |                                  |                                      |                                  |
| Applicable   | engineering tool                                  | GX Works3 Ver.  | 1.035M or later   |   |                                  |  |                                  |                                      |                                  |                                      |                                  |
| Communic   | cation method                                     | Broadcast pollir  | ng method   |   |                                  |  |                                  |                                      |                                  |                                      |                                  |
| Transmissi   | on format   | HDLC complian   | t   |   |                                  |  |                                  |                                      |                                  |                                      |                                  |
| Error control system CRC (X <sup>16</sup> + X <sup>12</sup> + X <sup>6</sup> + 1)          |   |   |   |   |                                  |  |                                  |                                      |                                  |                                      |                                  |
| Number of occupied I/O points 8 points (Either input or output is available for counting.) |   |   |   |   |                                  |  |                                  |                                      |                                  |                                      |                                  |
| Number of c  | connectable modules                               | One module ca   | n be connected  | to CPU module                           | for each station                 | type • Maste                             | r station: 1 mod                 | lule*1 • Intellig                    | gent device stati                | on: 1 module*2                       |                                  |
| Power sup  | ply   | 24 V DC +20%,   | -15% 100 mA (   | external power s                        | upply)                           |  |                                  |                                      |                                  |                                      |                                  |
| Accessorie   | es  |   |   | cable (1 m, 3-wire<br>edicated cable te |                                  | tor (2) 110 Ω 1/2                        | W (color code:                   | brown, brown, b                      | rown) Dust pro                   | of protection she                    | et (1)                           |
| External dim   | nensions $W \times H \times D$ (mm)               | $50 \times 90 \times 83$  |   |   |                                  |  |                                  |                                      |                                  |                                      |                                  |
| MASS (We   | eight): kg  | Approx. 0.3   |   |   |                                  |  |                                  |                                      |                                  |                                      |                                  |
| k 1. When  | using the FX5-CCL-N                               | S as the master   | station it can  | not be used to                          | aether with the                  | EX3U-16CCL-                              | M                                |                                      |                                  |                                      |                                  |

\* 1: When using the FX5-CCL-MS as the master station, it cannot be used together with the FX3U-16CCL-M.
\* 2: When using the FX5-CCL-MS as the intelligent device station, it cannot be used together with the FX3U-64CCL.
\* 3: The number of remote I/O points that can be used per system varies depending on the number of input/output points of the extension device. For the limit of the number of I/O points, refer to the following manual.

→ MELSEC iQ-F FX5U User's Manual (Hardware)

→ MELSEC iQ-F FX5UC User's Manual (Hardware)

 \* 4: The numbers in parentheses are the points that can be used when the module is an intelligent device station.
 \* 5: Number of links with FX5U/FX5UC CPU module Ver. 1.100 or later. GX Works3 Ver. 1.047Z or later required. For details on the number of links with FX5U/FX5UC CPU module earlier than Ver. 1.100, refer to the following manual. → MELSEC iQ-F FX5 User's Manual (CC-Link)

# FX3U-16CCL-M type CC-Link master module

#### ◇ Features



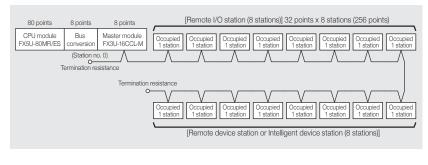
- 1) A master module setting MELSEC iQ-F Series as master station of CC-Link.
- 2) Up to 8 remote I/O stations and up to 8 remote device stations or intelligent device stations can be connected to a master station.

# $\diamond$ Specifications

|                             | Items  |   |  |                               |                                  | Specifi                          | ications                         |                                  |                                  |                                  |                 |
|-----------------------------|--|---|--|-------------------------------|----------------------------------|----------------------------------|----------------------------------|----------------------------------|----------------------------------|----------------------------------|-----------------|
| Support                     | ed functions                                 | Master statio   | laster station function (No local station and standby master station functions)  |                               |                                  |                                  |                                  |                                  |                                  |                                  |                 |
| CC-Link                     | compatible version                           | Ver. 2.00 con   | /er. 2.00 compliance (Ver. 1.10 compatible at the time of setting extension cyclic to 1 time)  |                               |                                  |                                  |                                  |                                  |                                  |                                  |                 |
| Transmis                    | ssion speed                                  | 156 kbps/62   | 5 kbps/2.5 Mb  | ps/5 Mbps/1                   | 0 Mbps (settin                   | g by a rotary s                  | switch)                          |                                  |                                  |                                  |                 |
| Station N                   | No.  | 0 (setting by   | a rotary switch  | )                             |                                  |                                  |                                  |                                  |                                  |                                  |                 |
| Connect                     | table station type                           | Remote I/O s  | station, remote  | device station                | n, intelligent de                | vice station (lo                 | ocal station an                  | d standby ma                     | ster station ca                  | nnot be conne                    | ected)          |
| Max. cal                    | ble extension length                         | 1,200 m (vari   | es depending   | on the transm                 | ission speed.)                   |                                  |                                  |                                  |                                  |                                  |                 |
| Max. no.                    | . of connection stations                     | Max. 16 static  |  |                               |                                  |                                  | ies 32 I/O point<br>maximum (The |                                  | of RX/RY points                  | is 256 or less.                  | )               |
| Max. no<br>system           | of I/O points per                            | [FX5U/FX5U0   |  | C actual I/O poi              |                                  | cupied intelligen                | 12 or less.<br>It function modul | le points) + (Occ                | cupied FX3U-160                  | CCL-M points: 8                  | points) ≤ 256   |
|                             |  | CC-Link   | Ver. 1.10  |                               |                                  |                                  | CC-Link                          | Ver. 2.00                        |                                  |                                  |                 |
|                             | Extension cyclic setting                     | -   | -  | Sir                           | igle                             | Doi                              | uble                             | Quad                             | druple                           | Oct                              | uple            |
|                             | No. of occupied stations                     | Remote I/O  | Remote register  | Remote I/O                    | Remote register                  | Remote I/O                       | Remote register                  | Remote I/O                       | Remote register                  | Remote I/O                       | Remote register |
| No                          | One station occupied                         | RX: 32 points<br>RY: 32 points  | RWw: 4 points<br>RWr: 4 points   |                               | RWw: 4 points<br>RWr: 4 points   |                                  | RWw: 8 points<br>RWr: 8 points   |                                  |                                  | RX: 128 points<br>RY: 128 points |                 |
| No.<br>of link<br>points    | Two stations occupied                        | RX: 64 points<br>RY: 64 points  | RWw: 8 points<br>RWr: 8 points   |                               | RWw: 8 points<br>RWr: 8 points   |                                  |                                  | RX: 192 points<br>RY: 192 points | RWw: 32 points<br>RWr: 32 points |                                  |                 |
| ponto                       | Three stations occupied                      |   | RWw: 12 points<br>RWr: 12 points   |                               | RWw: 12 points<br>RWr: 12 points | RX: 160 points<br>RY: 160 points |                                  |                                  |                                  |                                  |                 |
|                             | Four stations occupied                       |   |  |                               |                                  |                                  | RWw: 32 points<br>RWr: 32 points |                                  |                                  |                                  |                 |
| Transmis                    | ssion cable                                  | CC-Link spe   | cific cable, CC  | -Link specific                | high-performa                    | nce cable, Ver                   | r. 1.10 compat                   | ible CC-Link s                   | pecific cable                    |                                  | ~               |
| RAS fun                     | ction  | Automatic return function, slave separating function, abnormal detection by link special relay/register,<br>slave station refresh/Forced clear settings at the time of PLC CPU stop, and cyclic data consistency function |  |                               |                                  |                                  |                                  |                                  |                                  |                                  |                 |
| Compati                     | ible CPU module                              | Supported free Connection v   | om the first pro<br>vith FX5U or F2  | duct of FX5U<br>X5UC requires | or FX5UC<br>S FX5-CNV-BU         | S or FX5-CN                      | /-BUSC.                          |                                  |                                  |                                  |                 |
| No. of c                    | occupied I/O points                          | 8 points (cou   | ntable either b  | y input or out                | out)                             |                                  |                                  |                                  |                                  |                                  |                 |
| Commu                       | nication with PLC                            | Done by FRC   | M/TO instruct  | ion via buffer i              | memory (buffe                    | r memory can                     | be directly sp                   | ecified)                         |                                  |                                  |                 |
| No.of co                    | onnectable modules                           | FX5U, FX5U  | C: Max. 1 mod  | ule*                          |                                  |                                  |                                  |                                  |                                  |                                  |                 |
| External<br>power<br>supply | Power supply voltage/<br>Current consumption | 24 V DC +20   | 24 V DC +20%/ -15% ripple (p-p) within 5% (Electricity supplied from terminal block for power supply)/240 mA   |                               |                                  |                                  |                                  |                                  |                                  |                                  |                 |
| Accesso                     | pries  | <ul> <li>For standar</li> <li>For high-period</li> </ul>  | Terminal resistors <ul> <li>For standard cable:110 Ω 1/2 W (Color code, brown/brown/brown) 2 pcs.</li> <li>For high-performance cable:130 Ω 1/2 W (Color code, brown/orange/brown) 2 pcs.</li> </ul> Special block No. label |                               |                                  |                                  |                                  |                                  |                                  |                                  |                 |
| External c<br>W × H ×       | dimensions<br>D (mm)                         | 55 × 90 × 87  |  |                               |                                  |                                  |                                  |                                  |                                  |                                  |                 |
| MASS (V                     | Veight): kg                                  | Approx. 0.3   |  |                               |                                  |                                  |                                  |                                  |                                  |                                  |                 |

\*: When using the FX3U-16CCL-M, it cannot be used together with the FX5-CCL-MS used as the master station.

# ♦ Example of system configuration with FX5U



The maximum number of remote I/O stations to be connected is 8 when connecting 80-point type CPU module and FX3U-16CCL-M. The maximum number of remote I/O stations to be connected is less than 8 when the total number of points exceeds the maximum I/O points (512 points) due to the connection of I/O modules and intelligent function modules.

# FX3U-64CCL type CC-Link interface module

#### ◇ Features



MELSEC iQ-F Series can be connected as intelligent device stations of CC-Link.

# **♦** Specifications

|   | Items                                  | Specifications   |                                  |                                  |                                  |                                  |                                  |                                  |                                  |  |  |
|---|--|--|----------------------------------|----------------------------------|----------------------------------|----------------------------------|----------------------------------|----------------------------------|----------------------------------|--|--|
| Isolation   | type                                   | Photocoupler isolation   |                                  |                                  |                                  |                                  |                                  |                                  |                                  |  |  |
| CC-Link   | compatible version                     | ersion Ver. 2.00 (Ver. 1.10 compliance at the time of setting extension cyclic to 1 time; Buffer memory FX2N-32CCL compatibility also selv |                                  |                                  |                                  |                                  |                                  |                                  |                                  |  |  |
| Station 1   | types                                  | Intelligent device   | station                          |                                  |                                  |                                  |                                  |                                  |                                  |  |  |
| Station I   | No.                                    | 1 to 64 (setting b   | by a rotary switch)              |                                  |                                  |                                  | ·                                |                                  |                                  |  |  |
|   | ccupied stations/<br>on cyclic setting | Occupied 1 to 4  | stations, set to 1               | to 8 times (setting              | g by a rotary switc              | ch). Refer to the ta             | ble below for the                | details of allowab               | le range.                        |  |  |
| Transmi   | ssion speed                            | 156 kbps/625 k   | bps/2.5 Mbps/5 N                 | /lbps/10 Mbps (se                | etting by a rotary s             | switch)                          |                                  |                                  |                                  |  |  |
| Transmi   | ssion cable                            | Ver. 1.10 compa  | tible CC-Link spe                | cific cable, CC-Lii              | nk specific high-pe              | erformance cable                 |                                  |                                  |                                  |  |  |
|   |  | CC-Link  | Ver. 1.10                        |                                  |                                  | CC-Link                          | Ver. 2.00                        |                                  |                                  |  |  |
|   | Extension cyclic setting               | Sir  | igle                             | Do                               | uble                             | Quad                             | druple                           | Oct                              | tuple                            |  |  |
|   | No. of occupied stations*1             | Remote I/O   | Remote register                  | Remote I/O                       | Remote register                  | Remote I/O                       | Remote register                  | Remote I/O                       | Remote register                  |  |  |
| No.   | One station occupied                   | RX:32 points<br>RY:32 points   | RWw: 4 points<br>RWr: 4 points   | RX:32 points<br>RY:32 points     | RWw: 8 points<br>RWr: 8 points   | RX:64 points<br>RY:64 points     | RWw: 16 points<br>RWr: 16 points | RX: 128 points<br>RY: 128 points | RWw: 32 points<br>RWr: 32 points |  |  |
| of link<br>points   | Two stations occupied                  | RX:64 points<br>RY:64 points   | RWw: 8 points<br>RWr: 8 points   | RX:96 points<br>RY:96 points     | RWw: 16 points<br>RWr: 16 points | RX: 192 points<br>RY: 192 points | RWw: 32 points<br>RWr: 32 points |                                  |                                  |  |  |
|   | Three stations occupied                | RX:96 points<br>RY:96 points   | RWw: 12 points<br>RWr: 12 points | RX: 160 points<br>RY: 160 points | RWw: 24 points<br>RWr: 24 points |                                  |                                  |                                  |                                  |  |  |
|   | Four stations occupied                 | RX: 128 points<br>RY: 128 points   | RWw: 16 points<br>RWr: 16 points | RX:224 points<br>RY:224 points   | RWw: 32 points<br>RWr: 32 points |                                  |                                  |                                  |                                  |  |  |
| Compat  | ible CPU module                        | Supported from the first product of FX5U or FX5UC<br>Connection with FX5U or FX5UC requires FX5-CNV-BUS or FX5-CNV-BUSC.                   |                                  |                                  |                                  |                                  |                                  |                                  |                                  |  |  |
| No. of o  | ccupied I/O points                     | 8 points (counta   | ble either by input              | or output)                       |                                  |                                  |                                  |                                  |                                  |  |  |
| Commu   | nication with PLC                      | Done by FROM/TO instruction via buffer memory (buffer memory can be directly specified)  |                                  |                                  |                                  |                                  |                                  |                                  |                                  |  |  |
| No. of c  | onnectable modules                     | FX5U, FX5UC: Max. 1 module*2   |                                  |                                  |                                  |                                  |                                  |                                  |                                  |  |  |
| External<br>power<br>supply<br>Current consumption<br>24 V DC +20%/ -15% ripple (p-p) within 5% (Electricity suppli |  |  |                                  |                                  |                                  | terminal block fo                | r power supply)/2                | 20 mA                            |                                  |  |  |
| External dimensions<br>W × H × D (mm) 55 × 90 × 87  |  |  |                                  |                                  |                                  |                                  |                                  |                                  |                                  |  |  |
| MASS (\   | Weight): kg                            | Approx. 0.3  |                                  |                                  |                                  |                                  |                                  |                                  |                                  |  |  |

\*1: RX/RY for a high-order word of the last station of "Remote I/O" points is occupied as a system area. \*2: When using the FX3U-64CCL, it cannot be used together with the FX5-CCL-MS used as the intelligent device station.

# Ethernet

Connecting FX5 to LAN (Local Area Network) via Ethernet enables various data communications and program maintenance.

#### Built-in Ethernet communication

#### ◇ Features

- 1) The built-in Ethernet port can be used to connect to a PC or other device. In addition, the Ethernet communication port can handle seamless SLMP communication with the upper-level device.
- 2) Monitors and diagnoses the CPU module using a Web browser via connected network. Connect not only from a general-purpose browser on an Ethernet-connected PC but also from any generalpurpose browser on a tablet or smartphone connected to an Ethernet network.

#### ♦ Communication Specifications

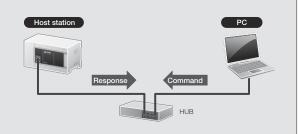
| Item                                     |                             | Specifications   |  |  |  |
|--|-----------------------------|--|--|--|--|
| ILEIT                                    |                             | FX5U / FX5UC   |  |  |  |
| Data transmission sp                     | eed                         | 100/10 Mbps  |  |  |  |
| Communication mod                        | е                           | Full duplex/Half duplex*1  |  |  |  |
| Interface                                |                             | RJ45 connector   |  |  |  |
| Transmission method                      | 1                           | Base band  |  |  |  |
| Maximum segment le<br>between hub and no |                             | 100 m  |  |  |  |
| Cascade                                  | 100BASE-TX                  | Max. 2 stages*2  |  |  |  |
| connection                               | 10BASE-T                    | Max. 4 stages*2  |  |  |  |
| Supported protocol                       |                             | CC-Link IE Field Network Basic, MELSOFT connection, SLMP (3E<br>frame), socket communications, communication protocol support,<br>FTP server, MODBUS/TCP communication, SNTP client, Web server<br>(HTTP), simple CPU communication function |  |  |  |
| No. of connections                       |                             | Total of 8 connections*3*4<br>(Up to 8 external devices are accessible to one CPU module at a time.)   |  |  |  |
| Hub*1                                    |                             | A hub having 100BASE-TX or 10BASE-T port can be used.  |  |  |  |
| IP address*5                             |                             | Initial value: 192.168.3.250   |  |  |  |
| Isolation                                |                             | Pulse transformer isolation  |  |  |  |
| Cable used*6 When connecting 100BASE-TX  |                             | Ethernet standard-compatible cable Category 5 or higher (STP cable)  |  |  |  |
| Capie used."                             | When connecting<br>10BASE-T | Ethernet standard-compatible cable Category 3 or higher (STP cable)  |  |  |  |

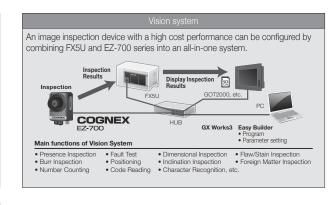
#### Outline of Functions

The CPU module is connected to an engineering tool (GX Works3) without using a hub but only by one Ethernet cable. This connection communicates by only



SLMP (SeamLess Message Protocol) can read/write the device data of PLC from the PC via the Ethernet communication (up to 8 connections).





Remote maintenance enables comfortable remote maintenance and monitoring. Realizes flexible maintenance using Internet regardless of where base is located! VPN connection construction (Optical/xDSL/CATV) VPN route VPN route GX Works3 Office VPN rooter: Relaying communication device by encrypting data VPN (Virtual Private Network)\* This is a technology that connects networks by encrypting the communication contents. In combination with the Internet, VPN allows remotely separated networks to be accessed as if connected with each other via LAN.

\*: A VPN connection ce support partner will help you support VPN system construction

Allows data communications between specified devices at the specified timing just by setting simple parameters from GX Works3. Device data Device data Ш Device data 10000 HUB

- \*1: IEEE802.3x flow control is not supported.
- \*2: No. of connectable stages when using a repeater hub. For the no. of connectable stages when a switching hub is in use, check the switching hub specification.
- \*3: The first device for MELSOFT connection is not included in the number of connections. (The second and the following devices are included.)
- \*4: The CC-Link IE field network Basic, FTP server, SNTP client, Web server and simple CPU communication function are not included in the number of connections.
- \*5. If the first octet is 0 or 127, a parameter error (2222H) will occur. (Example: 0.0.0, 127.0.0, etc.)
- \*6: A straight cable can be used. If a personal computer or GOT and CPU module are directly connected, a cross cable can be used.

# ◇ Features



- 1) Master module for using the MELSEC iQ-F Series as a CC-Link IE Field Network Basic master station. Co-existence with general-purpose Ethernet is also possible.
- 2) Up to 32 connectable slave stations for CC-Link IE Field Network Basic, with control for up to 2048 link points for RX/RY, and 1024 points for RWr/ RWw within the same network.
- 3) Grouping of slave stations for CC-Link IE Field Network Basic with configuration of a group number, with cyclic transmission possible for each group. Grouping stations according to the slave station standard response time makes it possible to suppress the influence of differences in the standard response times of each slave station.

#### Specifications

| Items   |  |                                     |                 | Specifications   |
|---|--|-------------------------------------|-----------------|--|
| Station type  |  |                                     |                 | Master station   |
|   | Maximum number of connectable stations*1       |                                     |                 | 32   |
|   | Number of stations occupied by a slave station |                                     |                 | 1 to 4   |
|   | Number of slave station groups                 |                                     |                 | 2  |
|   |  | 0_1                                 | RX              | 2048 points  |
|   | Maximum num                                    | ber of link points                  | RY              | 2048 points  |
|   | per network                                    |                                     |                 | 1024 points  |
|   |  |                                     |                 | 1024 points  |
|   |  |                                     | RX              | 2048 points  |
|   |  | Master station                      | RY              | 2048 points  |
|   | Maximum  |                                     | RWr             | 1024 points  |
|   | number of                                      |                                     | RWw             | 1024 points  |
|   | link points per                                |                                     | RX              | 64/128/192/256 points  |
| CC-Link IE Field  | station  | 0                                   | RY              | 64/128/192/256 points  |
| Network Basic   |  | Slave station*2                     | RWr             | 32/64/96/128 points  |
|   |  |                                     | RWw             | 32/64/96/128 points  |
|   | UDP port num                                   | ber used in the cycl                | ic transmission | 61450  |
|   |  | ber used in automa                  |                 | Master station: An unused port number is assigned automatically.                                     |
|   | connected dev                                  | vices                               |                 | Slave station: 61451   |
|   |  | Data transfer spee                  | d               | 100 Mbps   |
|   |  | Interface                           |                 | RJ45 connector   |
|   | Transmission                                   | Maximum station-to-station distance |                 | 100 m  |
|   | specifications                                 | Overall cable distance              |                 | Depends on the system configuration  |
|   |  | Number of cascade 100PACE TX        |                 | When using a switching hub, check the number of cascaded stages with the manufacturer of the         |
|   |  | connections 100BASE-TX              |                 | hub to be used.  |
|   | Network topole                                 | ogy                                 |                 | Star topology  |
|   | Hub*3  |                                     |                 | Hubs with 100BASE-TX ports*4 can be used.  |
|   | Connection cable*5 100BASE-TX                  |                                     |                 | Ethernet standard-compatible cable Category 5 or higher (STP cable)                                  |
|   | Data transfer speed                            |                                     |                 | 100/10 Mbps  |
|   |  | Communication mode                  |                 | Full-duplex or half-duplex*3   |
|   |  | Transmission method                 |                 | Base band  |
|   | Transmission                                   | Interface                           |                 | RJ45 connector   |
|   | specifications                                 | Maximum segment length              |                 |  |
| General-  | opcomoationo                                   | (Maximum distance between hub       |                 | 100 m*6  |
| purpose Ethernet  |  | and node)                           |                 |  |
| communication   |  | Number of cascade                   |                 | Max. 2 stages*7  |
|   |  | connections                         | 10BASE-T        | Max. 4 stages*7  |
|   | Supported pro                                  |                                     |                 | Socket communication   |
|   | Number of cor                                  | Inections                           |                 | Total of 32 connections (Up to 32 external devices can access one FX5-ENET module at the same time.) |
|   | Hub*3  |                                     |                 | Hubs with 100BASE-TX or 10BASE-T ports*8 can be used.  |
|   | Connection cable*5                             |                                     |                 | Ethernet standard-compatible cable Category 5 or higher (STP cable)                                  |
|   | 10BASE-T                                       |                                     | 10BASE-1        | Ethernet standard-compatible cable Category 3 or higher (STP/UTP cable)                              |
| Number of ports   |  |                                     |                 | 2*9  |
| Compatible CPU module                                       |  |                                     |                 | FX5U, FX5UC: Ver. 1.110 or later   |
|   |  |                                     |                 | Connection with FX5UC requires FX5-CNV-IFC or FX5-C1PS-5V.   |
| Number of occupied I/O points Number of connectable modules |  |                                     |                 | 8 points (Either input or output is available for counting.)   |
|   | able modules                                   |                                     |                 | FX5U, FX5UC: Up to 1 module  |
| Power supply  |  |                                     |                 | 24 V DC, 110 mA (internal power supply)  |
| External dimensions W × H × D (mm)                          |  |                                     |                 | 40 × 90 × 83   |
| MASS (Weight): kg   |  |                                     |                 | Approx. 0.2  |

\*1: Maximum number of connected slave stations that FX5-ENET (master station) can manage. However, the maximum number of connectable modules varies depending on the number of station occupied by a slave station and the Erkel (master station) carmanage. However, number of station occupied by a slave station. \*2: Value for 1-station occupation, 2-station occupation, 3-station occupation, or 4-station occupation. \*3: IEEE802.3x flow control is not supported. \*4: The ports must comply with the IEEE802.3 100BASE-TX standards.

\*5: A straight/cross cable can be used.
\*6: For maximum segment length (length between hubs), consult the manufacturer of the hub used.

\*7: This number applies when a repeater hub is used. When using a switching hub, check the number of cascaded stages with the manufacturer of the hub to be used.
 \*8: The ports must comply with the IEEE802.3 100BASE-TX or IEEE802.3 10BASE-T standards.

\*9: Because the IP address is shared by two ports, only one address can be set.

# EtherNet/IP

CIP communication protocol achieves a seamless communication with EtherNet/IP Network.

#### FX5-ENET/IP type Ethernet module

#### ◇ Features



- 1) Module for connecting the MELSEC iQ-F Series to EtherNet/IP Network and general-purpose Ethernet. Co-existence with EtherNet/IP and general-purpose Ethernet is also possible.
- 2) Not only setting of EtherNet/IP communication, but also detection of EtherNet/IP devices on the network and on-line setting of EtherNet/IP communication is possible.
- 3) Settings can be configured with the following software:
  - GX Works3 (Ver. 1.050C or later)
  - EhterNet/IP Configuration Tool for FX5-ENET/IP (Ver. 1.00A or later)

# ♦ Specifications

|  | Class 1<br>communications      | Communication format<br>Number of connections<br>Communication data size | Standard EtherNet/IP<br>32  |  |  |
|--|--------------------------------|--|---|--|--|
|  |                                |  | 32  |  |  |
|  |                                | Communication data size  |   |  |  |
|  |                                | Our in unication uata size   | 1444 bytes (per connection)   |  |  |
| _  | COMMUNICATIONS                 | Connection type  | Point-to-point, multicast   |  |  |
|  |                                | RPI (communication cycle)  | 2 to 60000 ms   |  |  |
| _  |                                | PPS (communication   | 2000 ppp (oppp of 100 by too)   |  |  |
|  |                                | processing performance)  | 3000 pps (case of 128 bytes)  |  |  |
|  |                                | Communication format   | Standard EtherNet/IP  |  |  |
|  | Class 3<br>communications      | Number of connections<br>(number of simultaneous<br>executions)          | 32*1  |  |  |
|  | Contractional                  | Communication data size  | 1414 bytes (per onnection)*2  |  |  |
| SU   |                                | Connection type  | Point-to-point  |  |  |
| atio   |                                | Communication format   | Standard EtherNet/IP  |  |  |
| Inic   |                                | Number of connections  |   |  |  |
|  | UCMM<br>communications         | (number of simultaneous executions)                                      | 32*1  |  |  |
|  |                                | Communication data size  | 1414 bytes*2  |  |  |
|  |                                | Connection type  | Point-to-point  |  |  |
|  |                                | Data transmission speed  | 100 Mbps  |  |  |
|  |                                | Communication mode   | Full-duplex   |  |  |
| -  | Transmission                   | Transmission method  | Base band   |  |  |
|  | specifications                 | IP version   | IPv4 is supported.  |  |  |
|  |                                | Maximum segment length   | 100 m*3   |  |  |
|  |                                | Number of cascade connections  | 100BASE-TX: 2 levels maximum*4  |  |  |
|  | Network topology               |  | Star topology, line pology  |  |  |
|  | Hub*5                          |  | *6  |  |  |
| (  | Connection cable               | k7   | 100BASE-TX  |  |  |
|  |                                | Data transfer speed  | 100/10 Mbps   |  |  |
| on   | Transmission<br>specifications | Communication mode   | Full-duplex or half-duplex*5  |  |  |
| se   |                                | Transmission method  | Base band   |  |  |
| nur (  |                                | Maximum segment length   | 100 m*3   |  |  |
| General-purpose<br>Ethernet communication      |                                | Number of cascade connections  | 100BASE-TX:2 levels maximum <sup>*4</sup><br>10BASE-T:4 levels maximum <sup>*4</sup>              |  |  |
| net  | Protocol type                  |  | Socket communication  |  |  |
| ther   | Number of connect              | otions   | Total of 32 connections*8   |  |  |
| L L  | Hub*5                          |  | *9  |  |  |
| Connection cable*7                             |                                |  | 100BASE-TX, 10BASE-T  |  |  |
| Number of ports                                |                                |  | 2*10  |  |  |
| Compatible CPU module                          |                                |  | FX5U, FX5UC: Ver. 1.110 or later<br>Connection with FX5UC requires FX5-CNV-IFC or<br>FX5-C1PS-5V. |  |  |
| Number of occupied I/O points                  |                                |  | 8 points (Either input or output is available for counting.)                                      |  |  |
| Number of connectable units                    |                                |  | FX5U, FX5UC: Up to 1 module   |  |  |
| Power supply                                   |                                |  | 24 V DC, 110 mA (internal power supply)   |  |  |
| External dimensions $W \times H \times D$ (mm) |                                |  | 40×90×83  |  |  |
| MASS (Weight): kg                              |                                |  | Approx. 0.2   |  |  |

\*1 : The total number of connections for Class 3 communications and UCMM communications is 32. \*2 : This size is the maximum size which can be specified to 'Data length' of Class1 communication input data area of the request command during the client operation. During the sever operation, since the FX5-ENET/IP automatically

responds according to the request command received from the client, the maximum size is not prescribed.
\*3 : For maximum segment length (length between hubs), consult the manufacturer of the hub used.
\*4 : This number applies when a repeater hub is used. When using a switching hub, check the number of cascaded stages with the manufacturer of the hub to be used.

stages with the final diadurer of the habit to be used.
\$5: IEEE802.3x flow control is not supported.
\$6: Hubs with 100BASE-TX ports can be used. The ports must comply with the IEEE802.3 100BASE-TX standards.
\$7: A straight/cross cable can be used.
\$8: Up to 32 external devices can access one FX5-ENET/IP module at the same time.
\$9: Hubs with 100BASE-TX or 10BASE-T ports can be used. The ports must comply with the IEEE802.3 100BASE-TX standards. or IEEE802.3 10BASE-T standards

\* 10: Since the IP address is shared by two ports, only one address can be set.

# MODBUS

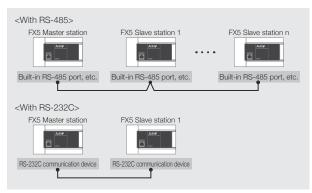
FX5 can be connected to various MODBUS communication devices as master station or slave station of the MODBUS communication.

# **MODBUS RTU communication**

#### ♦ Outline of Functions

- 1) Connection to 32 slave stations for RS-485 communication and one slave station for RS-232C communication is possible with a single master station.
- 2) Master function and slave functions are supported, and the master and slave can be used simultaneously by a single FX5. (However, only one channel can be used for the master station.)
- 3) Up to 4 channels can be used for MODBUS serial communication function by one CPU module.

#### ♦ System configuration example



# ♦ Specifications

| ltem  |  | Specifications  |                          |  |  |
|---|--|---|--------------------------|--|--|
|   |  | Built-in RS-485 port<br>FX5-485-BD<br>FX5-485ADP  | FX5-232-BD<br>FX5-232ADP |  |  |
| Number of connected modules   |  | Up to 4 channels*1 (only 1 channel for the master)  |                          |  |  |
|   | Communication interface                                      | RS-485 RS-232C  |                          |  |  |
| ations  | Baud rate  | 300/600/1200/2400/4800/96<br>38400/57600/115200 bps   | 00/19200/                |  |  |
| liji  | Data length  | 8 bits  |                          |  |  |
| bed   | Parity bit   | None, odd or even   |                          |  |  |
| 5   | Stop bit   | 1 bit/2 bits  |                          |  |  |
| Communication Specifications  | Transmission distance*2                                      | 1200 m or less when<br>configured with FX5-485ADP<br>only<br>50 m or less when configured<br>other than the above | 15 m or less             |  |  |
| Communication protocol  |  | RTU   |                          |  |  |
|   | Number of connectable slaves*3                               | 32 stations   | 1 station                |  |  |
| Number of functions<br>Number of simultaneous<br>transmission messages<br>Maximum number of<br>writes |  | 8 (without diagnostic function)   |                          |  |  |
|   |  | 1 message   |                          |  |  |
| Maste   | Maximum number of writes                                     | 123 words or 1968 coils   |                          |  |  |
|   | Maximum number of<br>reads                                   | 125 words or 2000 coils   |                          |  |  |
| uo  | Number of functions  | 8 (without diagnostic function)   |                          |  |  |
| Slave function  | Number of messages<br>that can be received<br>simultaneously | 1 message   |                          |  |  |
| 0   | Station number   | 1 to 247  |                          |  |  |
| ¥ 1. Auc  | ilable by either master or el                                | 81/0  |                          |  |  |

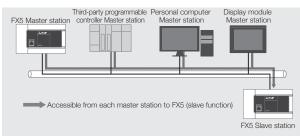
\* 2: The transmission distance varies depending on the type of communications equipment. \* 3: The number of slaves varies depending on the type of communications equipment

# **MODBUS/TCP** communication

#### ◇ Features

- 1) Communication is possible, via Ethernet connection, with various MODBUS/TCP master devices connected to the FX5 set as the slave station.
- 2) Master function and slave functions are supported, and the master and slave can be used simultaneously by a single FX5.
- 3) Up to 8 connections can be used for MODBUS/TCP communication function by one CPU module.

# ○ System configuration example



# ♦ Specifications

For communication specification other than the followings, refer to the MELSEC iQ-F FX5 User's Manual (Ethernet Communication).

|                       | Items                  | Specifications  |  |
|-----------------------|------------------------|---|--|
| Supported p           | rotocol                | MODBUS/TCP (Binary only supported)  |  |
| Number of connections |                        | Total of 8 connections <sup>*1</sup><br>(Up to 8 external devices can access one CPU<br>module at the same time.) |  |
| Slave                 | Number of<br>functions | 10  |  |
| TUNCTION              | Port station No.       | 502*2   |  |

\*1: The number of available connections decreases when the other Ethernet communication function is used. However, the first MELSOFT connection, CC-Link IE Field Network Basic, FTP server, SNTP client, and Web server are not included in the number of connections (The second and subsequent MELSOFT connections are included). For details on the Ethernet communication function, refer to the following manual.  $\rightarrow$  MELSEC iQ-F FX5 User's Manual (Ethernet Communication)

\*2: The port station No. can be changed by the communication setting.

# **Sensor Solution**

Sensor wire-saving system of AnyWireASLINK is easily configurable.

#### FX5-ASL-M type AnyWireASLINK system master module

#### ◇ Features



- 1) The AnyWireASLINK system can centrally monitor the status of sensors from the PLC and perform disconnection/short-circuit detection, sensor sensitivity setting, status monitoring, etc. It has no restrictions about the minimum distance between terminals, and also provides free wiring methods such as T-branch, multidrop, star etc., allowing for flexible branching and connection.
- 2) Since the status of the sensor can be monitored from the PLC, it is possible to predict the occurrence of troubles such as a decrease in the amount of light received by the sensor and prevent the production line from stopping in advance.
- 3) ID (address) can be changed from the buffer memory for one slave module without using the address writer. A slave ID can be changed even from a remote location.\*
- \*: For the slave modules compatible with the remote address change function, contact Anywire Corporation.

#### ♦ Safety precautions

FX5-ASL-M is jointly developed and manufactured with Anywire Corporation. Note that the warranty for this product differs from the ones for other PLC products. For details of warranty and specifications, refer to the manual.

#### ♦ Specifications

|  | Specifications  |
|--|---|
| Transmission clock                                       | 27.0 kHz  |
| Maximum transmission distance (total extension distance) | 200 m*1   |
| Transmission system                                      | DC power supply superimposed total frame/cyclic system  |
| Connection type  | Bus type (multi-drop method, T-branch method, tree branch method)   |
| Transmission protocol                                    | Dedicated protocol (AnyWireASLINK)  |
| Error control  | Checksum, double check method   |
| Number of connected I/O points                           | Up to 448 points*2*3 (256 input points maximum/256 output points maximum)   |
| Number of connected modules                              | Up to 128 modules (the number varies depending on the current<br>consumption of each slave module)  |
| Maximum number of I/O points per system                  | Number of slave module input points + number of slave module output<br>points ≤ 384 points  |
| External interface                                       | 7-piece spring clamp terminal block push-in type  |
| RAS function   | Transmission line disconnection position detection function     Transmission line short-circuit detection function     Transmission power drop detection function   |
| Transmission line (DP, DN)                               | UL compatible general-purpose 2-wire cable (VCTF, VCT 1.25 mm <sup>2</sup> , 0.75 mm <sup>2</sup> , temperature rating 70°C or higher)<br>UL compatible general-purpose cable (1.25 mm <sup>2</sup> , 0.75 mm <sup>2</sup> , temperature rating 70°C or higher)<br>Dedicated flat cable (1.25 mm <sup>2</sup> , 0.75 mm <sup>2</sup> , temperature rating 90°C) |
| Power cable (24 V, 0 V)                                  | UL compatible general-purpose 2-wire cable (VCTF, VCT 0.75 to 2.0 mm <sup>2</sup> ,<br>temperature rating 70°C or higher)<br>UL compatible general-purpose power cable (0.75 to 2.0 mm <sup>2</sup> , temperature<br>rating 70°C or higher)<br>Dedicated flat cable (1.25 mm <sup>2</sup> , 0.75 mm <sup>2</sup> , temperature rating 90°C)                     |
| Memory   | Built-in EEPROM (Number of times of overwrite : 100000 times)   |
| Compatible CPU module                                    | FX5U, FX5UC: Ver. 1.050 or later<br>Connection with FX5UC requires FX5-CNV-IFC or FX5-C1PS-5V.  |
| Power supply   | 5 V DC, 200 mA (internal power supply)<br>24 V DC -10%, +15% 100 mA (external power supply)   |
| Number of occupied I/O points                            | 8 points (Either input or output is available for counting.)  |
| Number of connectable modules                            | FX5U, FX5UC: Max. 1 module*4  |
| External dimensions $W \times H \times D$ (mm)           | 40 × 90 × 97.3  |
| MASS (Weight): kg  | Approx. 0.2   |

\*1: For the slave module in which the transmission line (DP, DN) and module body are integrated, the length of the transmission line (DP, DN) is also included in the total extension. When laying a 4-wire (DP, DN, 24 V, 0 V) line for fifty meters or more, insert a power line noise filter between the

power supply and the line.

For details, refer to the manual of ASLINK filter (ANF-01) made by Anywire Corporation. \* 2: The number of remote I/O points that can be used per system varies depending on the number of input/output points of the extension device.

For the limit of the number of I/O points, refer to the following manual.

- → MELSEC iQ-F FX5U User's Manual (Hardware)
   → MELSEC iQ-F FX5UC user's Manual (Hardware)
   ★ 3: Supported by FX5U CPU modules Ver. 1.100 or later and by GX Works3 Ver. 1.047Z or later.
- \*4: Use together with the FX3U-128ASL-M is not possible

# FX3U-128ASL-M type AnyWireASLINK System Master Module

#### ◇ Characteristics



- A master module enables MELSEC iQ-F series to be connected to the AnyWireASLINK sensor wire-saving system of Anywire Corporation.
- FX3U-128ASL-M type
   AnyWireASLINK system master module has a proprietary AnyWire transmission system including a power supply (equivalent to 24 V DC, MAX. 2 A) as a transmission signal, and thus realizes save wiring up to 200 m with a 4-core or 2-core cable.
- When using ASLINKAMP or ASLINKSENSOR, settings can be changed by a ladder program, engineering tool or GOT. Set-up changes can be done remotely.

# ○ Safety Precautions

FX3U-128ASL-M is jointly developed/ manufactured with Anywire Corporation. Guarantee details are different from other PLC products. Refer to manuals for guarantees/ specifications.

### ♦ Specifications

| Items   | Specifications   |
|---|--|
| Transmission clock                                  | 27.0 kHz   |
| Max. transmission distance (total extension length) | 200 m  |
| Transmission method                                 | DC power supply superimposing total frame/cyclic method  |
| Connection configuration                            | Bus type (Multi-drop method, T-branch method, tree branch method)  |
| Transmission protocol                               | Dedicated protocol (AnyWireASLINK)   |
| Error control                                       | Double verification method, checksum   |
| No. of connection I/O points                        | Max. 128 points  |
| No. of connection modules                           | Max. 128 modules (variable depending on current consumption)   |
| Max. no of I/O points per system                    | No. of input points of slave module + No. of output points of slave module $\leq$ 128 points   |
| RAS function  | Transmission line disconnection position detection function     Transmission line short-circuit detection function     Transmission power drop detection function  |
| AnyWireASLINK transmission line                     | UL supported general-use 2-line cable (VCTF, VCT 1.25 mm <sup>2</sup> , 0.75 mm <sup>2</sup> , rated temperature: 70°C or higher)<br>UL supported general-use electric wire (1.25 mm <sup>2</sup> , 0.75 mm <sup>2</sup> , rated temperature: 70°C or higher), dedicated flat cable (1.25 mm <sup>2</sup> , 0.75 mm <sup>2</sup> , rated temperature: 90°C ) |
| 24 V DC power supply line                           | UL supported general-use 2-line cable (VCTF, VCT 0.75 to 2.0 mm <sup>2</sup> , rated temperature: 70°C or higher)<br>UL supported general-use electric wire (0.75 to 2.0 mm <sup>2</sup> , rated temperature: 70°C or higher), dedicated flat cable (1.25 mm <sup>2</sup> , 0.75 mm <sup>2</sup> , rated temperature: 90°C)                                  |
| Compatible CPU module                               | Supported from the first product of FX5U or FX5UC<br>Connection with FX5U or FX5UC requires FX5-CNV-BUS or FX5-CNV-BUSC.   |
| Power supply  | 5 V DC, 130 mA (internal power supply)<br>24 V DC -10% + 15% 100 mA (AnyWireASLINK communication external<br>power supply)   |
| No. of occupied I/O points                          | 8 points (countable either by input or output)   |
| Communication with PLC                              | Done by FROM/TO instruction via buffer memory<br>(buffer memory can be directly specified)   |
| No.of connectable modules                           | FX5U, FX5UC: Max. 1 module*  |
| External dimensions W x H x D (mm)                  | 43 × 90 × 95.5   |
| MASS (Weight): kg                                   | Approx. 0.2  |

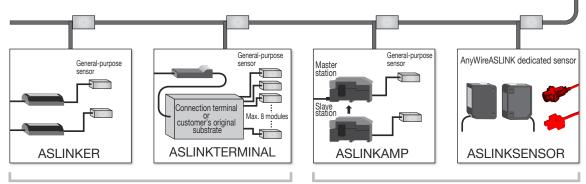
\*: Use together with the FX5-ASL-M is not possible.

Your requests for reduced wiring, detecting of disconnection/short circuit, setting of sensor sensitivity, and status monitoring can be satisfied by MELSEC iQ-F.

# Example of system configuration (AnyWireASLINK)

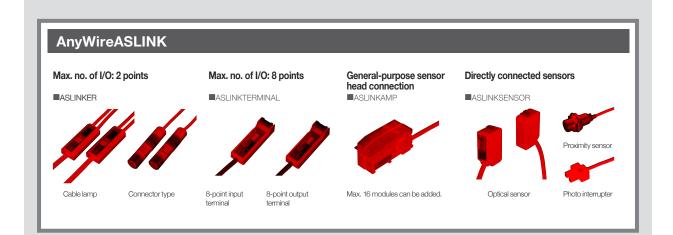


Total extension length of 200 m<sup>\*1 \*2</sup>, Max. 448 points<sup>\*3 \*4</sup> and Max. 128 modules<sup>\*2</sup> connectable



Sensor disconnection is detectable

Disconnection and short-circuit of sensors are detectable Setting of sensor sensitivity or status monitoring are possible



- \* 1: Total extension distance including the portion of branch line.
   \* 2: Subject to change based upon current consumption of each slave module.
- \* 2: Subject to change based upon current consumption of each slave module.
   \* 3: The number of remote I/O points that can be used per system varies depending on the number of input/output points of the extension device. For the limit of the number of I/O points, refer to the following manual.
  - → MELSEC iQ-F FX5U User's Manual (Hardware)
  - → MELSEC iQ-F FX5UC User's Manual (Hardware)
- \*4: Supported by FX5U CPU modules Ver. 1.100 or later and by GX Works3 Ver. 1.047Z or later.

# **PROFIBUS-DP**

PROFIBUS is an industrial fieldbus developed and maintained by PROFIBUS & PROFINET International (PI). This protocol enables high-speed data transmission between field devices such as a remote I/O module or drive and a controller.

# FX5-DP-M type PROFIBUS-DP master station module

#### 



- This master module is necessary for using the MELSEC iQ-F Series as a PROFIBUS-DP master station. Using this product makes it possible to incorporate compatible slave devices into the system.
- Using the buffer memory makes it possible to obtain communications error information or extended communications error information generated by a slave station during I/O data transmission.
- Settings can be configured with the following software:
  - GX Works3 (Ver. 1.050C or later)
  - PROFIBUS Configuration Tool (Ver.
  - 1.02C or later)

# FX3U-32DP type PROFIBUS-DP slave station module

#### ◇ Features



 Connectable as a MELSEC iQ-F Series slave station in PROFIBUS-DP systems.

#### ♦ Specifications

| Items  | Specifications   |  |            |          |          |                               |
|--|--|--|------------|----------|----------|-------------------------------|
| PROFIBUS-DP station type                       | PROFIBUS-DP slave station  |  |            |          |          |                               |
| Transmission speed                             | 9.6 kbps, 19.2 kbps, 45.45 kbps, 93.75 kbps, 187.5 kbps, 500 kbps, 1.5 Mbps, 3 Mbps, 6 Mbps, 12 Mbps             |  |            |          |          |                               |
|  | Transmission speed   | 9.6 kbps,<br>19.2 kbps,<br>45.45 kbps,<br>93.75 kbps | 187.5 kbps | 500 kbps | 1.5 Mbps | 3 Mbps,<br>6 Mbps,<br>12 Mbps |
| Transmission distance/segment                  | No repeaters   | 1,200 m  | 1,000 m    | 400 m    | 200 m    | 100 m                         |
|  | 1 repeater   | 2,400 m  | 2,000 m    | 800 m    | 400 m    | 200 m                         |
|  | 2 repeaters  | 3,600 m  | 3,000 m    | 1,200 m  | 600 m    | 300 m                         |
|  | 3 repeaters  | 4,800 m  | 4,000 m    | 1,600 m  | 800 m    | 400 m                         |
| Transmittable data                             | Up to 144 bytes  |  |            |          |          |                               |
| Iransmittable data                             | Default: 32 bytes (cyclic input / cyclic output)   |  |            |          |          |                               |
| PROFIBUS module ID                             | F332h  |  |            |          |          |                               |
| Global control                                 | Supports SYNC, UNSYNC, FREEZE, and UNFREEZE modes  |  |            |          |          |                               |
| Compatible CPU module                          | FX5U, FX5UC: Compatible from initial product<br>Connection with FX5U/FX5UC requires FX5-CNV-BUS or FX5-CNV-BUSC. |  |            |          |          |                               |
| Number of occupied I/O points                  | 8 points (Either input or output is available for counting.)   |  |            |          |          |                               |
| Number of connectable modules                  | FX5U: Up to 8 modules*, FX5UC: Up to 6 modules   |  |            |          |          |                               |
| Power supply                                   | 24 V DC, 145 mA (internal power supply)  |  |            |          |          |                               |
| External dimensions $W \times H \times D$ (mm) | 43 × 90 × 89   |  |            |          |          |                               |
| MASS (Weight): kg                              | Approx. 0.2  |  |            |          |          |                               |

\*: When using FX3U-1PSU-5V. Up to 6 modules when not using FX3U-1PSU-5V.

#### ♦ Specifications

| Items   |             | Specifications   |  |  |  |
|---|-------------|--|--|--|--|
| PROFIBUS-DP station type  |             | Class 1 master station   |  |  |  |
| Electrical standard and characteristics                         |             | Compliant with EIA-RS485   |  |  |  |
| Medium  |             | Shielded twisted pair cable  |  |  |  |
| Network configuration   |             | Bus topology (or tree topology when repeaters are used)  |  |  |  |
|   |             | Between DP-Masters: Token passing  |  |  |  |
| Data link method  |             | Between DP-Master and DP-Slave; Polling  |  |  |  |
| Encoding method   |             | NRZ  |  |  |  |
| Transmission speed*   |             | 9.6 kbps, 19.2 kbps, 93.75 kbps, 187.5 kbps, 500 kbps, 1.5 Mbps, 3 Mbps, 6 Mbps, 12 Mbps       |  |  |  |
| Transmission distance   |             | Differs depending on transmission speed  |  |  |  |
| Maximum number of repeaters<br>(Between DP-Master and DP-Slave) |             | 3 repeaters  |  |  |  |
| Number of connectable modules (per segment)                     |             | 32 per segment (including repeaters)   |  |  |  |
| Maximum number of D   | P-Slaves    | 64 modules   |  |  |  |
| Number of connectable nodes (number of repeaters)               |             | 32, 62 (1), 92 (2), 122 (3), 126 (4)   |  |  |  |
| Transmittable data  | Input data  | Max. of 2048 bytes (Max. of 244 bytes per DP-Slave)  |  |  |  |
| Iransmittable data  | Output data | Max. of 2048 bytes (Max. of 244 bytes per DP-Slave)  |  |  |  |
| Compatible CPU module   |             | FX5U, FX5UC: Ver. 1.110 or later<br>Connection with FX5UC requires FX5-CNV-IFC or FX5-C1PS-5V. |  |  |  |
| Number of occupied I/O points                                   |             | 8 points (Either input or output is available for counting.)                                   |  |  |  |
| Number of connectable modules                                   |             | FX5U, FX5UC: Up to 1 module  |  |  |  |
| Power supply  |             | 24 V DC, 150 mA (internal power supply)  |  |  |  |
| External dimensions $W \times H \times D$ (mm)                  |             | 40 × 90 × 85.3   |  |  |  |
| MASS (Weight): kg   |             | Approx. 0.2  |  |  |  |

\*: Transmission speed accuracy is within ±0.2% (compliant with IEC61158-2).

6

# General-purpose Communication Devices

Various communication functions can be added easily using an expansion board or expansion adapter. Communications with data link or external serial interface device can be realized easily by adding an expansion board.

#### **Expansion board (for communication)**

#### 

- 1) Communication expansion board can be added to FX5U CPU module.
- 2) Communication function can be added inexpensively.

Refer to the following items for usage method of expansion board.

- "N:N network"
- "Parallel link"

F. R b

- "MC protocol"
- "Non-protocol communication"
- "Connection to peripheral device"
- "Inverter communication function"

#### ♦ Specifications



| Model/Characteristics             | Items  | Specifications   |  |
|-----------------------------------|--|--|--|
| FX5-232-BD                        | Transmission standard                          | Conforming to RS-232C standard   |  |
| RS-232C communication expansion   | Max. transmission distance                     | 15 m   |  |
| board                             | External device connection method              | 9-pin D-sub (male)   |  |
|                                   | Isolation                                      | No isolation (between communication line and CPU)  |  |
| and the second second             | Communication method                           | Half-duplex bidirectional/Full-duplex bidirectional*   |  |
|                                   | Protocol type                                  | MELSOFT connection, MC protocol (1C/3C/4C frame), non-protocol<br>communication, MODBUS RTU communication, predefined protocol support |  |
| <b>1</b>                          | Communication speed                            | 300/600/1200/2400/4800/9600/19200/38400/57600/115200 (bps)*  |  |
|                                   | Terminal resistors                             | -  |  |
| *                                 | Power supply                                   | 5 V DC, 20 mA (internal power supply)  |  |
|                                   | Compatible CPU module                          | FX5U CPU module  |  |
|                                   | No. of occupied I/O points                     | 0 points (No occupied points)  |  |
|                                   | External dimensions $W \times H \times D$ (mm) | 38 × 51.4 × 18.2   |  |
|                                   | MASS (Weight): kg                              | Approx. 0.02   |  |
| The communication method and comm | subjection aread year depending upon the comm  | unidentian tumo  |  |

\*: The communication method and communication speed vary depending upon the communication type.

| Model/Characteristics  | Items  | Specifications  |  |  |
|--|--|---|--|--|
| FX5-485-BD   | Transmission standard                          | Conforming to RS-485 and RS-422 standards   |  |  |
| RS-485 communication expansion   | Max. transmission distance                     | 50 m  |  |  |
| board  | External device connection method              | European-type terminal block  |  |  |
|  | Isolation                                      | No isolation (between communication line and CPU)   |  |  |
| Lan I  | Communication method                           | Half-duplex bidirectional/Full-duplex bidirectional*  |  |  |
|  | Protocol type                                  | MELSOFT connection, MC protocol (1C/3C/4C frame), non-protocol<br>communication, MODBUS RTU communication, inverter communication, N:N<br>network, parallel link, predefined protocol support |  |  |
|  | Communication speed                            | 300/600/1200/2400/4800/9600/19200/38400/57600/115200 (bps)*   |  |  |
| A REAL PROPERTY OF THE PARTY OF | Terminal resistors                             | Built in (OPEN/110 Ω/330 Ω)   |  |  |
|  | Power supply                                   | 5 V DC, 20 mA (internal power supply)   |  |  |
|  | Compatible CPU module                          | FX5U CPU module   |  |  |
|  | No. of occupied I/O points                     | 0 points (No occupied points)   |  |  |
|  | External dimensions $W \times H \times D$ (mm) | 38 × 51.4 × 30.5  |  |  |
|  | MASS (Weight): kg                              | Approx. 0.02  |  |  |

\*: The communication method and communication speed vary depending upon the communication type

#### **General-purpose Communication Devices**

| Model/Characteristics          | Items  | Specifications                                    |
|--------------------------------|--|---|
| FX5-422-BD-GOT                 | Transmission standard                          | Conforming to RS-422 standard                     |
| RS-422 communication expansion | Max. transmission distance                     | As per GOT specifications                         |
| board (GOT connection)         | External device connection method              | 8-pin MINI-DIN (female)                           |
|                                | Isolation                                      | No isolation (between communication line and CPU) |
|                                | Communication method                           | Half-duplex bidirectional                         |
| A MEAR                         | Communication speed                            | 9600/19200/38400/57600/115200 (bps)               |
| 2.                             | Terminal resistors                             | -   |
|                                | Power supply                                   | 5 V DC, 20 mA (internal power supply)*            |
|                                | Compatible CPU module                          | FX5U CPU module                                   |
|                                | No. of occupied I/O points                     | 0 points (No occupied points)                     |
|                                | External dimensions $W \times H \times D$ (mm) | 38 × 51.4 × 15.4                                  |
|                                | MASS (Weight): kg                              | Approx. 0.02                                      |

\*: When the GOT 5V type is connected with this product, the power consumption increases. For the current consumption, refer to the manual of the model to be connected.

#### FX5-232ADP type RS-232C communication expansion adapter

#### 



Isolation type RS-232C communication adapter Refer to the "MC protocol", "Non-protocol communication", "Connection to peripheral device" for more details of functions.

#### ♦ Specifications

| Items  | Specifications  |
|--|---|
| Transmission standard                          | Conforming to RS-232C standard  |
| Max. transmission distance                     | 15 m  |
| Isolation                                      | Photocoupler isolation (between communication line and CPU)   |
| External device connection method: connector   | 9-pin D-sub (male)  |
| Communication method                           | Half-duplex bidirectional/Full-duplex bidirectional   |
| Protocol type                                  | MELSOFT connection, MC protocol (1C/3C/4C frame), non-protocol<br>communication, MODBUS RTU communication, predefined protocol<br>support |
| Communication speed                            | 300/600/1200/2400/4800/9600/19200/38400/57600/115200 (bps)*   |
| No. of occupied I/O points                     | 0 points (No occupied points)   |
| Current consumption (internal supply)          | 5 V DC 30 mA/24 V DC 30 mA  |
| Compatible CPU module                          | Compatible with FX5U and FX5UC, from their first released products  |
| Number of connectable modules                  | FX5U, FX5UC: Up to two communication adapters are provided on the left side of the CPU module.  |
| External dimensions $W \times H \times D$ (mm) | 17.6 × 106 × 82.8   |
| MASS (Weight): kg                              | Approx. 0.08  |

\*: The communication method and communication speed vary depending upon the communication type.

#### FX5-485ADP type RS-485 communication expansion adapter

#### ◇ Features



Isolation type RS-485 communication adapter Refer to the "N:N network", "Parallel link", "MC Protocol", "Non-protocol communication", "Connection to peripheral device", "Inverter communication function" for more details of functions.

#### ♦ Specifications

| Items  | Specifications  |
|--|---|
| Transmission standard                          | Conforming to RS-485 and RS-422 standards   |
| Max. transmission distance                     | 1200 m  |
| Isolation                                      | Photocoupler isolation (between communication line and CPU)   |
| External device connection method              | European-type terminal block  |
| Communication method                           | Half-duplex bidirectional/Full-duplex bidirectional   |
| Protocol type                                  | MELSOFT connection, MC protocol (1C/3C/4C frame), non-protocol<br>communication, MODBUS RTU communication, inverter communication,<br>N:N network, parallel link, predefined protocol support |
| Communication speed                            | 300/600/1200/2400/4800/9600/19200/38400/57600/115200 (bps)*   |
| Terminal resistors                             | Built in (OPEN/110 Ω/330 Ω)   |
| No. of occupied I/O points                     | 0 points (No occupied points)   |
| Current consumption<br>(internal supply)       | 5 V DC 20 mA/24 V DC 30 mA  |
| Compatible CPU module                          | Compatible with FX5U and FX5UC, from their first released products  |
| Number of connectable modules                  | FX5U, FX5UC: Up to two communication adapters are provided on the left<br>side of the CPU module.   |
| External dimensions $W \times H \times D$ (mm) | 17.6 × 106 × 89.1   |
| MASS (Weight): kg                              | Approx. 0.08  |

 $\star$ : The communication method and communication speed vary depending upon the communication type.

### **N:N Network**

Using the built-in RS-485 port, RS-485 communication expansion board, or expansion adapter enables data link of 2 to 8 PLCs easily.

#### **RS-485** communication device

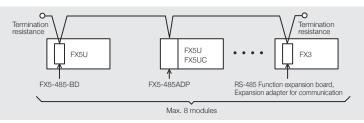
| Model      | Types                | Compatible CPU module |       |  |
|------------|----------------------|-----------------------|-------|--|
| IVIOUEI    | Types                | FX5U                  | FX5UC |  |
| FX5-485-BD | Expansion board      | 0                     | х     |  |
| FX5-485ADP | Expansion adapter    | 0                     | 0     |  |
| -          | Built-in RS-485 port | 0                     | 0     |  |

#### **N:N network function**

#### ◇ Features

- Data link can be realized by a simple program for connecting up to 8 modules of FX5 or FX3.
- 2) The bit device (0 to 64 points) and word device (4 to 8 points) are automatically linked between each station. The ON/OFF state of other stations and data register values can be obtained by the device allocated on the local station.

#### ○ System configuration example



#### ♦ Specifications of N:N network function

| Items                     |                | Specifications  |
|---------------------------|----------------|---|
| Transmission standard     |                | Conforming to RS-485 standard   |
| Total extension length    |                | Configuration only using FX5-485ADP: 1200 m or less<br>Configuration using FX5-485ADP, FX3U-485ADP(-MB): 500 m or less<br>Configuration other than above: 50 m or less (at coexisting of built-in RS-485 port, FX5-485-BD and 485-BD for FX3: 50 m or less) |
| Communication metho speed | d/Transmission | Half-duplex bidirectional, 38400 bps  |
| No.of connectable mod     | dules          | Max. 8 modules  |
|                           | Pattern 0      | Bit device: 0 points<br>Word device: 4 points   |
| No. of link points        | Pattern 1      | Bit device: 32 points<br>Word device: 4 points  |
|                           | Pattern 2      | Bit device: 64 points<br>Word device: 8 points  |
|                           | Pattern 0      | Based on the no. of connection modules, 2 modules (20), 3 modules (29), 4 modules (37), 5 modules (46), 6 modules (54), 7 modules (63), 8 modules (72)  |
| Link refresh time (ms)    | Pattern 1      | Based on the no. of connection modules, 2 modules (24), 3 modules (35), 4 modules (45), 5 modules (56), 6 modules (67), 7 modules (78), 8 modules (88)  |
|                           | Pattern 2      | Based on the no. of connection modules, 2 modules (37), 3 modules (52), 4 modules (70), 5 modules (87), 6 modules (105), 7 modules (122), 8 modules (139)   |
|                           | FX5U           | FX5-485ADP, FX5-485-BD  |
|                           | FX5UC          | FX5-485ADP  |
| Connection device         | FX3S           | FX3G-485-BD(-RJ) or FX3S-CNV-ADP+FX3U-485ADP(-MB)   |
| with PLC                  | FX3G           | FX3G-485-BD(-RJ) or FX3G-CNV-ADP+FX3U-485ADP(-MB)   |
|                           | FX3GC          | FX3U-485ADP(-MB)  |
|                           | FX3U, FX3UC*   | FX3U-485-BD or Function expansion board+FX3U-485ADP(-MB)  |
| Compatible CPU modu       | le             | FX5U, FX5UC, FX3S, FX3G, FX3GC, FX3U, FX3UC   |

\*: Function expansion board cannot be connected to FX3UC-DIMT/D, FX3UC-DIMT/DSS, and FX3UC-16MR/DD-T. A special adapter can be connected directly.

### Parallel link

2 modules of FX5U/FX5UC can be connected using the built-in RS-485 port, RS-485 communication expansion board, and expansion adapter, and devices can be linked to each other.

#### **RS-485** communication equipment

| Model name   | Classification       | Compatible CPU module |       |  |
|--------------|----------------------|-----------------------|-------|--|
| WOUEITIAITIE | Ciassilication       |                       | FX5UC |  |
| FX5-485-BD   | Expansion board      | 0                     | х     |  |
| FX5-485ADP   | Expansion adapter    | 0                     | 0     |  |
| -            | Built-in RS-485 port | 0                     | 0     |  |

#### **Parallel link function**

#### ◇ Features

- 1) With 2 modules of FX5U/FX5UC connected, devices can be linked to each other only by parameter setting.
- 2) 2 types of link modes, normal parallel link mode and high-speed parallel link mode, can be selected according to the number of points you want to link to and the link time, and the data link is automatically updated between the 2 modules of FX5U/FX5UC.

#### ○ System configuration example

Parallel link



#### ○ Parallel link specifications

| Item                           | Specifications  |
|--------------------------------|---|
| Number of connected modules    | Up to 2 modules (1:1)   |
| Transmission standards         | RS-485 standard compliant   |
| Maximum overall cable distance | 1200 m or less when configured with FX5-485ADP only 50 m or less when configured other than the above   |
| Link time                      | Normal parallel link mode: 15 ms + master station<br>operation cycle (ms) + slave station operation cycle (ms)<br>High-speed parallel link mode: 5 ms + master station<br>operation cycle (ms) + slave station operation cycle (ms) |

### MC Protocol

Data link of multiple PLCs can be realized by setting a CPU module or external device as a master station using MC protocol (serial communication).

Since data link is done by command from the external device, it is suitable for configuration of data management and control system by the external device as the main controller.

#### RS-232C, RS-485 communication device

| Model      | Turana               | Compatible CPU module |       |  |  |
|------------|----------------------|-----------------------|-------|--|--|
| MOUEI      | Types                | FX5U                  | FX5UC |  |  |
| FX5-232-BD | Expansion board      | 0                     | х     |  |  |
| FX5-232ADP | Expansion adapter    | 0                     | 0     |  |  |
| FX5-485-BD | Expansion board      | 0                     | ×     |  |  |
| FX5-485ADP | Expansion adapter    | 0                     | 0     |  |  |
| -          | Built-in RS-485 port | 0                     | 0     |  |  |

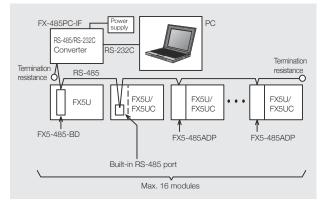
#### **MC** protocol function

#### ◇ Features

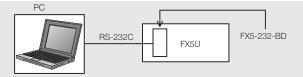
- 1) Using the RS-485 communication device enables connection of up to 16 modules of FX5U/FX5UC, and data can be transferred according to commands from the PC.
- 2) Using the RS-232C communication device enables 1 : 1 data transfer with the PC.
- 3) Communication by MC protocol A-compatible 1C frame and QnA-compatible-3C/4C frame is possible. (Type 1/Type 4/ Type 5)

#### ○ System configuration example

1) 1 : n connection using RS-485 communication



2) 1:1 connection using RS-232C communication



#### ◇ MC protocol function specifications

| Items                    |           | Specifications  |
|--------------------------|-----------|---|
| Transmission standard    |           | Conforming to RS-485/RS-232C standard   |
| Total<br>extension       | RS-485    | When using FX5-485ADP: 1200 m or less<br>When using the built-in RS-485 port or FX5-485-BD:<br>50 m or less |
| length                   | RS-232C   | 15 m or less  |
| Communicati              | on method | Half-duplex bidirectional   |
| Transmission             | speed     | 300/600/1200/2400/4800/9600/19200/38400/57600/<br>115200 bps  |
| No.of connect<br>modules | table     | Max. 16 modules   |
| Protocol type            | S         | MC protocol (dedicated protocol)<br>1C/3C Frame (Type1/Type4) /<br>4C Frame (Type1/Type4/Type5)             |
| RS-485<br>connection     | FX5U      | Built-in RS-485 port, FX5-485-BD or FX5-485ADP  |
| device                   | FX5UC     | Built-in RS-485 port or FX5-485ADP  |
| RS-232C                  | FX5U      | FX5-232-BD or FX5-232ADP  |
| connection device        | FX5UC     | FX5-232ADP  |
| Compatible CPU module    |           | FX5U, FX5UC   |

# RS-232C/RS-485 Non-protocol communication

MELSEC iQ-F Series modules can communicate with printers, code readers, measurement instruments, etc. having an interface in accordance with RS-232C/RS-485 (RS-422).

Communication is performed using sequence programs (RS2 instruction).

#### **RS-232C** communication

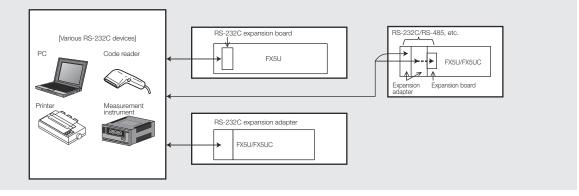
#### ◇ RS-232C communication device

|                         | Communication method                                    |   | Maximum<br>transmission<br>distance | Control instruction | Compatible CPU module    |                          |
|-------------------------|---|---|-------------------------------------|---------------------|--------------------------|--------------------------|
| Model (No. of channels) |   | Isolation   |                                     |                     | FX5U                     | FX5UC                    |
| FX5-232-BD (1 ch)       | Half-duplex bidirectional/<br>Full-duplex bidirectional | No isolation (between communication line and CPU)           | 15 m                                | RS2<br>instruction  | O<br>(Max.<br>1 module)  | x                        |
| FX5-232ADP (1 ch)       | Half-duplex bidirectional/<br>Full-duplex bidirectional | Photocoupler isolation (between communication line and CPU) | 15 m                                | RS2<br>instruction  | O<br>(Max.<br>2 modules) | O<br>(Max.<br>2 modules) |

#### ○ Communication specification

Refer to the specifications of each communication device for the details of RS-232C device specifications.

#### ♦ System configuration



#### RS-485 (RS-422) communication

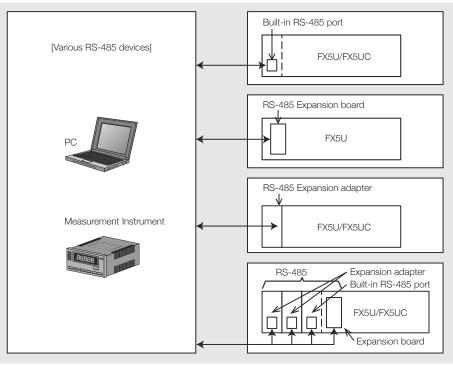
| ○ I13-+05 (I13-+22) (       | communication de  | evice   | 1                       |                    |                          |                          |
|-----------------------------|---|---|-------------------------|--------------------|--------------------------|--------------------------|
| Model (No. of channels)     | Communication method                                    | Isolation   | Maximum<br>transmission | Control            |                          | CPU module               |
| ``                          |   |   | distance                | instruction        | FX5U                     | FX5UC                    |
| FX5-485-BD (1 ch)           | Half-duplex bidirectional/<br>Full-duplex bidirectional | No isolation (between<br>communication line and CPU)        | 50 m                    | RS2<br>instruction | O<br>(Max. 1<br>module)  | ×                        |
| FX5-485ADP (1 ch)           | Half-duplex bidirectional/<br>Full-duplex bidirectional | Photocoupler isolation (between communication line and CPU) | 1200 m                  | RS2<br>instruction | O<br>(Max. 2<br>modules) | O<br>(Max. 2<br>modules) |
| Built-in RS-485 port (1 ch) | Half-duplex bidirectional/<br>Full-duplex bidirectional | No isolation (between<br>communication line and CPU)        | 50 m                    | RS2<br>instruction | 0                        | 0                        |

#### RS-485 (RS-422) communication device

#### $\diamond$ Communication specification

Refer to the specifications of each communication device for the details of RS-485 device specifications.

#### $\diamond$ System configuration example



# **Connection to Peripheral Devices**

Installing RS-422/RS-232C communication devices enables addition of connection ports with peripheral devices. PLC programming devices such as PC and HMI (GOT) can be connected to the added ports.

#### **RS-232C** communication

#### ○ RS-232C communication device

|                         |   |   | Maximum                  | Compatible CPU module |                       |  |  |
|-------------------------|---|---|--------------------------|-----------------------|-----------------------|--|--|
| Model (No. of channels) | Communication method                                    | Isolation   | transmission<br>distance | FX5U                  | FX5UC                 |  |  |
| FX5-232-BD (1 ch)       | Half-duplex bidirectional/<br>Full-duplex bidirectional | No isolation (between<br>communication line and CPU)        | 15 m                     | O<br>(Max. 1 module)  | ×                     |  |  |
| FX5-232ADP (1 ch)       | Half-duplex bidirectional/<br>Full-duplex bidirectional | Photocoupler isolation (between communication line and CPU) | 15 m                     | O<br>(Max. 2 modules) | O<br>(Max. 2 modules) |  |  |

#### ♦ Communication specification

Refer to the specifications of each communication device for the detailed specifications of RS-232C peripheral devices (programming protocol).

#### ♦ Connection cable for RS-232C communication device and peripheral devices

The main connection cables are as follows:

| Connection destination | Cable  |
|------------------------|--|
| DOS/V PC (9-pin D-SUB) | FX-232CAB-1  |
| HMI (GOT)              | Use the specific cable or wire for RS-232C connection of each HMI. |

#### ○ Concurrent use of peripheral device

Connect an engineering tool such as PC software to either one of peripheral devices to avoid programs from being changed by multiple peripheral devices.

#### **RS-422 (GOT) communication**

#### ◇ RS-422 communication device

|                         |                           |   | Maximum                      | Compatible CPU module |       |  |
|-------------------------|---------------------------|---|------------------------------|-----------------------|-------|--|
| Model (No. of channels) | Communication method      | Isolation   | transmission<br>distance     | FX5U                  | FX5UC |  |
| FX5-422-BD-GOT (1 ch)   |                           |   |                              |                       |       |  |
|                         | Half-duplex bidirectional | No isolation<br>(between communication line and<br>CPU) | As per GOT<br>specifications | O<br>(Max. 1 module)  | ×     |  |

#### ○ Communication specification

Refer to the manual of GOT.

#### ♦ Communication cable

Use a dedicated cable for GOT.

### Inverter Communication Function

Dedicated instructions for Mitsubishi Electric inverter protocol and communication control are built in FX5. Connecting an inverter enables simple control of inverter.

#### **RS-485** communication

|                             |  |   | Maximum                  | Control                 | Compatible               | CPU module               |
|-----------------------------|--|---|--------------------------|-------------------------|--------------------------|--------------------------|
| Model (No. of channels)     | Communication method                                     | Isolation   | transmission<br>distance | instruction             | FX5U                     | FX5UC                    |
| FX5-485-BD (1 ch)           | Half-duplex bidirectional/<br>Full-duplex bidirectional* | No isolation (between<br>communication line and CPU)        | 50 m                     | Inverter<br>instruction | O<br>(Max. 1<br>module)  | ×                        |
| FX5-485ADP (1 ch)           | Half-duplex bidirectional/<br>Full-duplex bidirectional* | Photocoupler isolation (between communication line and CPU) | 1200 m                   | Inverter<br>instruction | O<br>(Max. 2<br>modules) | O<br>(Max. 2<br>modules) |
| Built-in RS-485 port (1 ch) | Half-duplex bidirectional/<br>Full-duplex bidirectional* | No isolation (between<br>communication line and CPU)        | 50 m                     | Inverter<br>instruction | 0                        | 0                        |

#### $\bigcirc$ RS-485 communication device

\*: Half-duplex bidirection in case of connecting to inverter.

#### ♦ System configuration example



#### Connectable Mitsubishi Electric general-purpose inverter



#### Inverter

[Connectable Models] FR-A800/F800/F700PJ/E700/E700EX (sensorless servo) /D700

#### **Inverter Communication Function**

memo

## **Engineering Tool**

Various types of engineering software are prepared to enable easy programming for the Mitsubishi Electric PLC and realize comfortable operation.

#### MELSOFT iQ Works FA Integrated Engineering Software

#### • iQ Works (English version) ...... Model: SW2DND-IQWK-E

#### ◇ Features

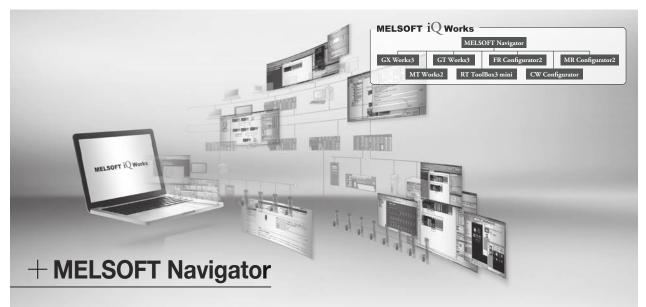
- By realization of a seamless integrated engineering environment, the total cost will be reduced.
- All the system labels can be checked on MELSOFT Navigator.
- Parameter settings for each project (GX Works3, GX Works2, MT Works2, and GT Works3) can be configured from MELSOFT Navigator.

This eliminates the need to launch various tools when configuring the parameter settings.

- System configuration can be managed graphically. Allows the user to manage the system configuration graphically, and the effort to search for an appropriate tool can be eliminated by linking the project.
- Double click the project from the system configuration figure and work space tree of MELSOFT Navigator to start the software for the device automatically.
- The data on whole system can be backed up in a batch by simple operation.

#### By realization of a seamless integrated engineering environment, the total cost will be reduced!

Sold as a set integrating various engineering software centered around MELSOFT Navigator, MELSOFT iQ Works eliminates the need to purchase software separately. The ability to share design information including system design and programming throughout the control system makes it possible to improve efficiency of system design and programming while reducing total costs.





For details on MELSOFT iQ Works, refer to the following catalog:

#### MELSOFT GX Works3 PLC Engineering Software

#### GX Works3 ...... Model: SW1DND-GXW3-E

#### ◇ Features

- · Achieving an easy and intuitive programming by only making "selections" in a graphical environment with module configuration diagram and module label/ module FB.
- Supporting various applications (parameter settings) of simple motion module, creation of positioning data, parameter setting and servo adjustments of servo amplifier).
- · Complying with the international standard IEC 61131-3 for engineering software and supporting the modularized and structured programming. Programming languages such as ladder, ST, FBD/ LD are available.
- · Enabling transmitting/receiving of the data between an external device and the CPU module by matching the protocol of the external device. (Communication protocol support function)



For details on MELSOFT GX Works3, refer to the following catalog available on request



"MELSOFT GX Works3 catalog" L(NA)08334ENG

#### MELSOFT MX series Integrated Data Link Software

- MX Sheet (Microsoft<sup>®</sup> Excel<sup>®</sup> Communication Support Tool) ...... Model: SW2DNC-SHEET-E

#### ◇ Features

- A group of middleware remarkably improving development efficiency in the system configuration.
- Familiar Microsoft<sup>®</sup> Excel<sup>®</sup> settings on the screen enables easy data access of the on-site PLC without any program.
- Enabling the system to be configurable without considering a communication protocol.
- Enabling monitoring of on-site system only by setting parameters on the screen.

## **Operating environment**

Engineering tool operating environment. For details, refer to catalogs and manuals.

#### MELSOFT iQ Works and GX Works3 operating environment

|              | Items                      |   | Contents   |  |  |
|--------------|----------------------------|---|--|--|--|
| PC<br>Module | OS*¹<br>English<br>Version | Microsoft <sup>®</sup> Windows <sup>®</sup> 10 Home<br>Microsoft <sup>®</sup> Windows <sup>®</sup> 10 Pro<br>Microsoft <sup>®</sup> Windows <sup>®</sup> 10 Enterprise<br>Microsoft <sup>®</sup> Windows <sup>®</sup> 10 Iducation<br>Microsoft <sup>®</sup> Windows <sup>®</sup> 10 Iducation<br>Microsoft <sup>®</sup> Windows <sup>®</sup> 8.1<br>Microsoft <sup>®</sup> Windows <sup>®</sup> 8.1 Enterprise<br>Microsoft <sup>®</sup> Windows <sup>®</sup> 8.1 Enterprise<br>Microsoft <sup>®</sup> Windows <sup>®</sup> 8.1                      | Microsoft® Windows® 8 Pro<br>Microsoft® Windows® 8 Enterprise<br>Microsoft® Windows® 7 Starter<br>Microsoft® Windows® 7 Home Basic*3<br>Microsoft® Windows® 7 Home Premium<br>Microsoft® Windows® 7 Professional<br>Microsoft® Windows® 7 Enterprise<br>Microsoft® Windows® 7 Enterprise<br>Microsoft® Windows Vista® Home Basic | Microsoft® Windows Vista® Home Premium<br>Microsoft® Windows Vista® Ultimate<br>Microsoft® Windows Vista® Business<br>Microsoft® Windows Vista® Enterprise<br>Microsoft® Windows® XP Professional SP3<br>Microsoft® Windows® XP Home SP3 |  |
|              | CPU                        | Intel® Core™2 Duo 2 GHz or more recommended   |  |  |  |
|              | Memory<br>Requirements     | 1 GB or more recommended*2  |  |  |  |
| Hard Disc    | c Free Space               | [Installation] 26 GB or more*4 free disk space, [Opera  | ation] 512 MB or more free virtual memory  |  |  |
| Disc Drive   | Э                          | DVD-ROM supported disc drive  |  |  |  |
| Display      |                            | Resolution 1024 × 768 pixels or more  |  |  |  |
| Connectio    | on to PLC                  | Optional connection cable and interface are necessary.         [PC Communication Port]         Connectable from Ethernet port or RS-232C port.         FX5U PLC       : Directly connectable by Ethernet, or connectable by RS-232C communication expansion adapter or RS-232C communication expansion board.         FX5U PLC       : Directly connectable by Ethernet or connectable by RS-232C communication expansion adapter.         Refer to the "PC and PLC Connection Method" for the details of connection method and required cable types. |  |  |  |
| Compatik     | ole CPU module             | FX5U, FX5UC (Refer to the specific catalog or manual for details on FX Series, L Series, Q Series, and iQ-R Series modules.)  |  |  |  |

\*1: 64-bit versions of Windows Vista<sup>®</sup> and Windows<sup>®</sup> XP are not supported. 32-bit version of Microsoft<sup>®</sup> Windows<sup>®</sup> 10 IoT Enterprise 2016 LTSB is not supported. \*2: 2 GB or more recommended for 64-bit version

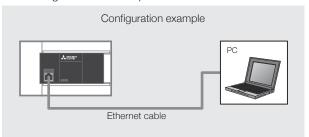
\*3: iQ Works is not supported.

\*4: 17 GB or more for installing only GX Works3

#### PC and PLC Connection Method and Required Equipment

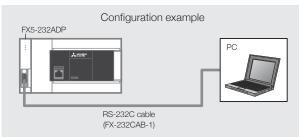
#### In case of connection between Ethernet port on the PC side

Connecting to the Ethernet port

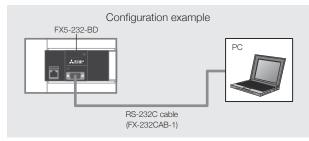


#### ♦ In case of connection between RS-232C port on the PC side

(1) Connection with the RS-232C port attached to PLC (using FX5-232ADP)



(2) Connection with the RS-232C port attached to PLC (using FX5-232-BD)



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# **Compatible Versions of Software**

The followings are compatible versions of each software.

New versions may be required due to addition of functions and products. Please refer to the manuals for more details.

| Category   | Turpo     | Compatible version   |                      |   |  |
|--|-----------|----------------------|----------------------|---|--|
| Calegory   | Туре      | FX5U                 | FX5UC                | Precautions   |  |
| Software for PLC                                     | iQ Works  |                      | Ver. 2.07H or above  | Use the latest version when new   |  |
| Soliware for PLC                                     | GX Works3 | Ver. 1.007H or above | Ver. 1.007H or above | functions are added.  |  |
| Software for GOT<br>(GOT1000 series, GOT2000 series) | GT Works3 | Ver. 1.126G or above | Ver. 1.126G or above | Compatible to the device scope.<br>Refer to the GOT manual for other<br>compatible items. |  |



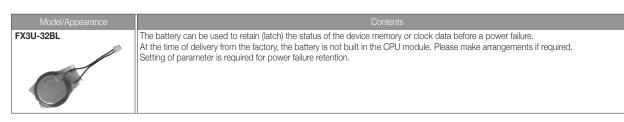
# **Option/Related Products**

We are pleased to offer you a wide variety of our products including SD memory cards, batteries, connection cables for PLC as well as interfaces for signal exchange.

#### **SD Memory Card**

| Model/Appearance              |                     |          | Contents         |
|-------------------------------|---------------------|----------|------------------|
| NZ1MEM-2GBSD<br>NZ1MEM-4GBSD  | NZ1MEM-2GBSD        | Туре     | SD memory card   |
| NZ1MEM-8GBSD<br>NZ1MEM-16GBSD | INZ IWEW-20030      | Capacity | 2 GB             |
|                               | NZ1MEM-4GBSD        | Туре     | SDHC memory card |
|                               | INZ IIWEWI-4003D    | Capacity | 4 GB             |
|                               | NZ1MEM-8GBSD        | Туре     | SDHC memory card |
|                               | INZ IIWIEIWI-OGIBSD | Capacity | 8 GB             |
|                               | NZ1MEM-16GBSD       | Туре     | SDHC memory card |
|                               |                     | Capacity | 16 GB            |

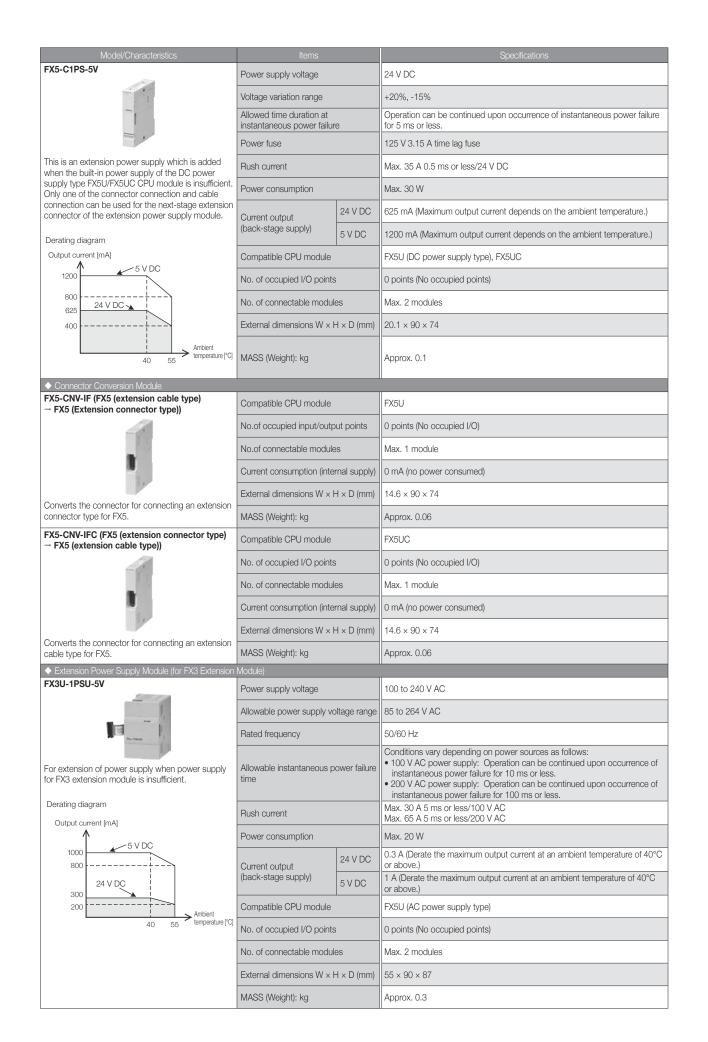
#### Battery



#### **Extension Device**

The extension cable for connecting to the right side of the front-stage device has been attached to the extension module (extension cable type).

| Model/Characteristics  | Items  |            | Specifications   |  |  |
|--|--|------------|--|--|--|
| ♦ Bus Conversion Module  |  |            |  |  |  |
| FX5-CNV-BUS (FX5 (extension cable type)<br>→ FX3 extension)  | Compatible CPU module                          |            | FX5U, FX5UC<br>Connection with FX5UC requires FX5-CNV-IFC or FX5-C1PS-5V.                    |  |  |
|  | No. of occupied I/O points                     |            | 8 points (countable either by input or output)   |  |  |
| r n  | No.of connectable modules                      |            | Max. 1 module  |  |  |
|  | Current consumption (internal supply)          |            | 5 V DC 150 mA  |  |  |
| Conversion module for connecting FX3 extension module to FX5U and FX5UC CPU modules.   | External dimensions $W \times H$               | H × D (mm) | 16 × 90 × 83   |  |  |
| module to FX50 and FX50C CP0 modules.  | MASS (Weight): kg                              |            | Approx. 0.1  |  |  |
| FX5-CNV-BUSC (FX5 (extension connector type)<br>→ FX3 extension)   | Compatible CPU module                          |            | FX5U, FX5UC<br>Connection with FX5Urequires FX5-CNV-IF.                                      |  |  |
| all and a second | No. of occupied I/O points                     | 6          | 8 points (countable either by input or output)   |  |  |
|  | No. of connectable modul                       | es         | Max. 1 module  |  |  |
| - 1 - 1 - 1 - 1 - 1 - 1 - 1 - 1 - 1 - 1  | Current consumption (internal supply)          |            | 5 V DC 150 mA  |  |  |
|  | External dimensions $W \times H \times D$ (mm) |            | 16 × 90 × 83   |  |  |
| Conversion module for connecting FX3 extension<br>modules to FX5U and FX5UC CPU modules.   | MASS (Weight): kg                              |            | Approx. 0.1  |  |  |
| <ul> <li>Extension Power Supply Module</li> </ul>  |  |            |  |  |  |
| FX5-1PSU-5V  | Rated power supply voltage                     |            | 100 to 240 V AC  |  |  |
| -  | Allowable power supply voltage range           |            | 85 to 264 V AC   |  |  |
|  | Rated frequency                                |            | 50/60 Hz   |  |  |
|  | Allowable instantaneous power failure time     |            | Operation can be continued upon occurrence of instantaneous power failure for 10 ms or less. |  |  |
| Module for extending power supply if FX5U (AC power supply type) CPU module's internal power   | Power fuse                                     |            | 250 V 3.15 A time lag fuse   |  |  |
| supply is insufficient. Extension cable is enclosed.   | Rush current                                   |            | Max. 25 A 5 ms or less/100 V DC<br>Max. 50 A 5 ms or less/200 V DC                           |  |  |
| Derating diagram   | Power consumption                              |            | Max. 20 W  |  |  |
| Output current [mA]  | Current output                                 | 24 V DC    | 300 mA (Maximum output current depends on the ambient temperature.)                          |  |  |
|  | (back-stage supply)                            | 5 V DC     | 1200 mA (Maximum output current depends on the ambient temperature.)                         |  |  |
| 800  | Compatible CPU module                          |            | FX5U (AC power supply type)  |  |  |
| 300  | No. of occupied I/O points                     |            | 0 points (No occupied points)  |  |  |
| $40  55 \rightarrow \text{Ambient}$  | No. of connectable modules                     |            | Max. 2 modules   |  |  |
|  | External dimensions $W \times H \times D$ (mm) |            | 50 × 90 × 83   |  |  |
|  | MASS (Weight): kg                              |            | Approx. 0.3  |  |  |



#### Extension Module Options (Extended Extension Cables/Connector Conversion Adapters)

FX5 extension modules (extension cable type) are equipped with the extension cable for connection to the right side of the front-stage device.

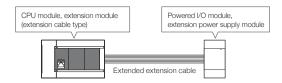
If intending extension of the connection distance or two-row placement of PLCs, an optional "Extended extension cable" is required. Only a single extended extension cable can be used per system.

#### ♦ Extended extension cable

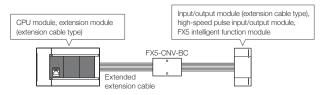
| Model                                | Specifications   |
|--------------------------------------|--|
| FX5-30EC (30 cm)<br>FX5-65EC (65 cm) |  |
|                                      | Only a single cable can be used per system. Depending on the CPU module to be used or the device to be connected with, the following connection conversion adapter (FX5-CNV-BC) is required. [Connector conversion adapter required] When the connection destination is an input/output module (extension cable type), high-speed pulse I/O module, or FX5 intelligent function module |
| FX5-CNV-BC                           | Connector conversion adapter<br>This connects between an extension cable and an extension cable type module when an extended extension cable is used.  |

#### $\bigcirc$ Main connection methods

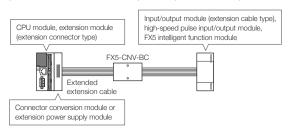
1) Connections with the Powered I/O module and FX5 extension power supply module (extension cable type)



2) Connections with the input/output module (extension cable type) and FX5 intelligent function module



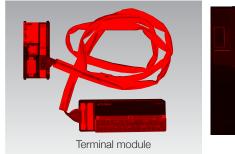
3) Connections with the input/output module (extension cable type) and FX5 intelligent function module



#### **Terminal Module**

This allows conversion of the connector of the FX5UC CPU module or the I/O module (extension connector type) to the screw terminal block, resulting in the reduced number of man-hours for I/O wiring.

Using an internal type of I/O element enables driving of a heavy load by a relay or a transistor.



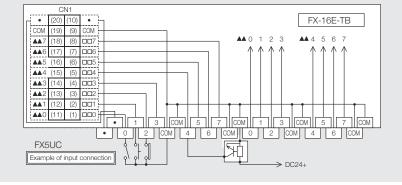
♦ List of Terminal Modules (Refer to the next page for the details of connection cables and optional connectors.)

| Model              | No. of input points                             | No. of output points                            | Function   |
|--------------------|---|---|--|
| FX-16E-TB          | Input 16 points or output 16 points             |   | Directly connected to the I/O terminal of PLC.             |
| FX-32E-TB          | Input 32 points or output 32 points (Division p | oossible: input 16 points and output 16 points) | Using this module instead of the PLC terminals or relaying |
| FX-16E-TB/UL       | Input 16 points or output 16 points             |   | a wiring of I/O device located remotely from PLC enables   |
| FX-32E-TB/UL       | Input 32 points or output 32 points (Division p | oossible: input 16 points and output 16 points) | reducing of the I/O wiring man-hours.                      |
| FX-16EYR-TB        | -   | 16  | Relay Output Type  |
| FX-16EYS-TB        | -   | 16  | Triac Output Type  |
| FX-16EYT-TB        | -   | 16  | Transistor Output Type (Sink output)                       |
| FX-16EYR-ES-TB/UL  | -   | 16  | Relay Output Type  |
| FX-16EYS-ES-TB/UL  | -   | 16  | Triac Output Type  |
| FX-16EYT-ES-TB/UL  | -   | 16  | Transistor Output Type (Sink output)                       |
| FX-16EYT-ESS-TB/UL | -   | 16  | Transistor Output Type (Source output)                     |

### Specifications PLC Direct Connection (FX-16E-TB, FX-32E-TB)

Since it is for direct connection of PLC I/O terminal, no electrical components are built in.

Electrical specifications are equivalent to that of the connected CPU module or connector type I/O module. A drawing on the right shows the internal connection of FX-16E-TB. In case of FX-32E-TB, CN2 is provided with the same connection.



#### 2. Output (FX-16EY -TB)

|                                | Model   | Relay output<br>FX-16EYR-TB                       | Triac output<br>FX-16EYS-TB   | Transistor output (Sink output)<br>FX-16EYT-TB   |
|--------------------------------|---|---|---|--|
| I/O circuit d                  | configuration   | CN1 connector side Load side                      | 3.3 kΩ     24 V DC     36 Ω       7 mA     7 mA       LED     COMn       Photothyristor     0.015 μF       CN1 connector side     Load side | $\begin{array}{c} 3.3 \text{ k} \Omega \text{ Photocoupler} \\ \hline \\ \text{LED} \\ 24 \text{ V DC} \\ \text{CN1 connector side} \\ \hline \\ \text{Load side} \end{array}$ |
| Load voltage                   |   | 250 V AC 30 V DC or less                          | 85 V to 242 V AC  | 5 V to 30 V DC   |
| Circuit isola                  | ircuit isolation Mechanical isolation   |   | Photocoupler isolation Photocoupler isolation   |  |
| Operation                      | Operation display An LED is turned on when applying an electrical current to a relay coil |   | An LED is turned on when applying an electrical current to a photothyristor   | An LED is turned on when applying an electrical current to a photocoupler  |
| Max load                       | Resistance load   | 2 A/1 point 8 A/4 points                          | 0.3 A/1 point 0.8 A/4 points  | 0.5 A/1 point 0.8 A/4 points   |
| Max. load                      | Inductive load  | 80 VA   | 15 VA/100 V AC, 36VA/240 V AC   | 12 W/24 V DC   |
| Open circuit leakage current – |   | -   | 1 mA/A100 V AC, 2 mA/200 V AC   | 0.1 mA/30 V DC   |
| Min. load 5 V                  |   | 5 V DC, 2 mA (reference value)                    | 0.4 VA/100 V AC, 1.6 VA/200 V AC  | -  |
| Response                       | sponse OFF → ON Approx. 10 ms   |   | 2 ms or less  | 0.2 ms or less   |
| time                           | ON → OFF  | Approx. 10 ms                                     | 12 ms or less   | 1.5 ms or less   |
| Input signa                    | al current  | 5 mA/24 V DC for each point (current consumption) | 7 mA/24 V DC for each point<br>(current consumption) 7 mA/24 V DC for each point<br>(current consumption)                                   |  |

#### **Option/Related Products**

#### I/O Cable

| Model/Appearance                               | Contents   |
|--|--|
| FX-16E-500CAB-S (5 m)                          | ● General-purpose I/O Cable  |
|  | A 20-pin connector attached to one end of bulk wire                  |
| FX-16E-150CAB (1.5 m)                          | I/O Cable for Terminal Module  |
| FX-16E-300CAB (3 m)<br>FX-16E-500CAB (5 m)     | A 20-pin connector attached to both ends of a flat cable (with tube) |
|  |  |
| FX-16E-150CAB-R (1.5 m)                        | ● I/O Cable for Terminal Module                                      |
| FX-16E-300CAB-R (3 m)<br>FX-16E-500CAB-R (5 m) | A 20-pin connector attached to both ends of round multi core cable   |
|  |  |

#### I/O Connector

Model/Appearance Connector for self-manufactured I/O cable 20-pin type (electric wire or crimp tool is not enclosed.) FX2C-I/O-CON •Flat Cable Connector AWG28 (0.1 mm<sup>2</sup>): A set of 10 pcs Crimp connector: FRC2-A020-3OS 1.27-pitch 20 cores Crimp tool: Separately arrange the tool manufactured by DDK Ltd.
 357J-4674D Main Module 357J-4664N Attachment (1) FX2C-I/O-CON-S (2) FX2C-I/O-CON-SA (1) Connector for single wires (1) Connector for single wires AWG22 (0.3 mm<sup>2</sup>): 5 sets Housing: HU-200S2-001 Crimp contact: HU-411S Crimp tool: A product manufactured by DDK Ltd. is separately required. 357J-5538 (2) Connector for single wires AWG20 (0.5 mm<sup>2</sup>): 5 sets
Housing: HU-200S2-001
Crimp contact: HU-411SA 11 23 Crimp tool: A product manufactured by DDK Ltd. is separately required. 357J-13963

| Contents   |  |
|--|--|
| red I/O cable: 40-pin type (electric wire or crimp   |  |
| (1) Soldered type connector<br>(straight protrusion)<br>Twist wire 0.088 to 0.3 mm <sup>2</sup> (AWG28 to 22)  |  |
| (2) Crimped type connector<br>(straight protrusion)<br>Twist wire 0.088 to 0.24 mm² (AWG28 to 24   |  |
| (3) Soldered type connector<br>(both straight/inclined protrusion type)<br>Twist wire 0.088 to 0.3 mm <sup>2</sup> (AWG28 to 22)   |  |
|  |  |
| <ul> <li>(1) Connector for single wires<br/>AWG22 (0.3 mm<sup>3</sup>): 2 sets</li> <li>Housing: HU-400S2-001</li> <li>Crimp contact: HU-411S</li> <li>Crimp tool: A product manufactured<br/>by DDK Ltd. is separately required. 357J-5538</li> </ul>   |  |
| <ul> <li>(2) Connector for single wires<br/>AWG20 (0.5 mm<sup>2</sup>): 2 sets</li> <li>Housing: HU-400S2-001</li> <li>Crimp contact: HU-411SA</li> <li>Crimp tool: A product manufactured<br/>by DDK Ltd. is separately required. 357J-13963</li> </ul> |  |
|  |  |

\*: Select wires with a sheath outside diameter of 1.3 mm or less when using 40 wires. Select wires suitable to the current value used.

#### **Power Cable**

| Model/Appearance      | Contents  |
|-----------------------|---|
| FX2NC-100MPCB (1 m)   | CPU Module Power Cable     Cable for providing 24 V DC power supply to the FX5UC CPU module.  |
|                       | Comes with the FX5UC CPU modules and intelligent function modules*.   |
| FX2NC-100BPCB (1 m)   | Power Cable   |
|                       | Cable for supplying 24 V DC input power supply to an extension connector type input module or input/output module.<br>Offered as an accessory of FX5UC-□MT/D.                                     |
|                       | It is necessary to purchase this cable separately when using an extension connector type input module or input/output module in the FX5U system.  |
| FX2NC-10BPCB1 (0.1 m) | Power Supply Transition Cable   |
| $\sim$                | Cable for crossover wiring of 24 V DC input power supply to two or more extension connector type input modules or input/output modules.<br>Offered as an accessory of FX5-C□EX/D and FX5-C32ET/D. |
|                       |   |

\*: There are some exception models. For details, refer to the manual.

### Related products Reduced wiring and man-hour saving machines for programmable controllers (FA goods) [manufactured by Mitsubishi Electric Engineering]

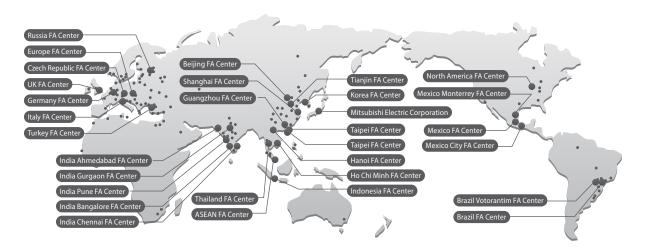
| Model name/external appearance             | Description  |
|--|--|
| FA-CBLQ75PM2J3 (2 m)                       | Connection cable   |
| FA-CBLQ75M2J3 (-P) (2 m)                   | Mitsubishi Electric MR-J3-A/J4-A series  |
|  | Connectable models   |
|  | FA-CBLQ75PM2J3: FX5-20PG-P<br>FA-CBLQ75M2J3 (-P): FX5-20PG-D   |
| FA-CBLQ75G2 (-P) (2 m)                     | Connection cable   |
|  | General-purpose stepping motor, discrete wire cable for servo amplifier  |
|  | Connectable models   |
|  | FX5-20PG-P, FX5-20PG-D   |
| FA-LTBQ75DP                                | Positioning signal conversion module   |
|  | Converts the external device connection signal of the positioning module to the terminal block and converts the signal between |
|  | the servo amplifiers to the connect.   |
| FA-CBL05Q7 (0.5 m)<br>FA-CBL10Q7 (1 m)     | Positioning signal conversion module   |
|  | Connection cable between positioning signal conversion modules   |
| FA-CBLQ7PM1J3 (1 m)<br>FA-CBLQ7DM1J3 (1 m) | OPositioning signal conversion module  |
|  | Connection cable between servo amplifiers (for Mitsubishi Electric MR-J3-A/J4-A series)  |
| FA-CBLQ7DG1 (1 m)                          | Positioning signal conversion module   |
|  | Connection cable between servo amplifiers (for general-purpose stepping motor and servo amplifier)                             |

### **Overseas service system**

Mitsubishi Electric's Micro PLC Series is a worldwide programmable controller that is used in more than 50 countries all over the world.

For local after-sales services in the overseas countries, "Mitsubishi Electric Global FA Centers" timely provide the best possible products, high technology and reliability services to our customers.

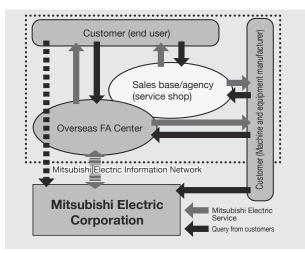
#### **Global FA Center**



### FA Global Service Network "Place contact our FA Center first."

For consultation and questions, please contact our FA centers in each country.

With our FA centers in each region of the world as key stations, we provide various services to customers while working closely with local sales offices, branches and agencies.



#### Detailed information on overseas service

 "FA global service" (KK001-EN) Service contents and contact information of our FA centers are detailed.

For more information on overseas support, please request this document.



### Certifications

MELSEC iQ-F Series conforms to European Standards (EN) and North American Standards (UL/cUL). Using MELSEC iQ-F Series can reduce the workload to make machines/equipment conform to EN and UL/cUL standards.

#### ○ Compatible with international standards

The MELSEC iQ-F series conforms to CE marking (Europe) and UL/cUL standard (USA. Canada) and therefore can be used for overseas facilities.



#### ♦ EN standards: Compliance with EC Directives/CE marking

EC directives are issued by the European Council of Ministers for the purpose of unifying European national regulations and smoothing distribution of safe guaranteed products. Approximately 20 types of major EC directives concerning product safety have been issued.

Attachment of a CE mark (CE marking) is mandatory on specific products before they may be distributed in the EU. The EMC Directive (Electromagnetic Compatibility Directive) and LVD Directive (Low Voltage Directive) apply to the programmable controller, which is labeled as an electrical part of a machine product under the EC Directives/

1) EMC Directive

The EMC Directive is a directive that requires products to have "Capacity to prevent output of obstructive noise that adversely affects external devices: Emission damage" and "Capacity to not malfunction due to obstructive noise from external source: Immunity".

2) LVD Directive (Low Voltage Directive)

The LVD Directive is enforced to distribute safe products that will not harm or damage people, objects or assets, etc. With the programmable controller, this means a product that does not pose a risk of electric shock, fire or injury, etc.



#### ○ UL/cUL Standards

UL is the United State's main private safety testing and certification agency for ensuring public safety.

UL sets the safety standards for a variety of fields. Strict reviews and testing are performed following the standards set forth by UL. Only products which pass these tests are allowed to carry the UL Mark.

As opposed to the EN Standards, the UL Standards do not have a legally binding effect. However, they are broadly used as the U.S. safety standards, and are an essential condition for selling products into the U.S.

UL is recognized as a certifying and testing agency by the Canadian Standards Association (CSA). Products evaluated and certified by UL in accordance with Canadian standards are permitted to carry the cUL Mark.

[Precautions on the use in UL/cUL Class I, Division 2 environment]

Products<sup>\*</sup> marking Cl. I, DIV.2 indicating that they can be used in the Class I, Division 2 (filling in a flammable environment in case of abnormalities) on the rating plate can be used in Class I, Division 2 Group A, B, C, and D only. They can be used regardless of the display as long as they do not reach the danger.

Note that when using a product in Class I, Division 2 environment, the following measures need to be taken for the risk of explosion.

- As this product is an open-type device, attach it to the control board suitable for the installation environment and, for opening, to the control board which requires a tool or key.
- Substitution of products other than Class I, Division 2 compatible may result in degradation of Class I, Division 2 compliance. Therefore, do not substitute products other than compatible products.
- Do not disconnect/connect the device or disconnect the external connection terminal except when the power is turned off or where there is no danger.
- Do not open the battery except where it is out of reach of danger.



- \*: UL explosion-proof standard compliant products are as follows. (Manufactured in October 2017 and after)
   FX5CPU module
- FX501 C Introduct
   FX5UC-32MT/D, FX5UC-32MT/DSS, FX5UC-64MT/D, FX5UC-64MT/DSS, FX5UC-96MT/D, and FX5UC-96MT/DSS
   FX5 extension module

FX5-C16EX/D, FX5-C16EX/DS, FX5-C16EYT/D, FX5-C16EYT/DSS, FX5-C32EX/D, FX5-C32EX/DS, FX5-C32EYT/D, FX5-C32EYT/DSS, FX5-C32ET/D, FX5-C32ET/DSS, FX5-C32ET/DSS,

#### $\diamond$ Ship standards

The MELSEC iQ-F series complies with the shipping standards of each country.

It can be used for ship-related machinery and equipment.

| Standard abbreviation | Standard name                         | Target country |
|-----------------------|---------------------------------------|----------------|
| DNV GL                | Det Norske Veritas Germanischer Lloyd | Norway/Germany |
| RINA                  | REGISTRO ITALIANO NAVALE              | Italy          |
| ABS                   | American Bureau of Shipping           | U.S.A.         |
| LR                    | Lloyd's Register of Shipping          | U.K.           |
| BV                    | Bureau Veritas                        | France         |
| NK                    | Nippon Kaiji Kyokai                   | Japan          |
| KR                    | Korea Ship Association                | Korea          |

#### "ISO09001" international standard for quality-assurance system

Mitsubishi Electric Corporation Nagoya Works has acquired "ISO9001" international standard for quality-assurance system for the development/manufacture on the whole from order reception to shipment of all series of micro sequencer. Of the ISO9000 series by which the International Organization for Standardization (ISO) defines the standards of quality-assurance systems, "ISO9001" assumes a wide range of quality-assurance systems related to development, manufacture, materials, quality and sales. The MELSEC iQ-F Series is manufactured under the control system based on an internationally recognized quality-assurance system. It is also used as a registration site of "ISO14001" environmental management system.

#### ♦ Korean Certification Mark (KC Mark)

- The KC mark, which is a safety certification mark required to be affixed to the specified products distributed in Korea (products required to be legally certificated for safety, quality, environment, etc.), indicates compliance with various requirements.
- KC mark is indicated on FA products, which conform to the Radio Act. Note that other standards are not applicable.

#### List of compatible products

|                                 |         |          |      |    | 1 |      |      |       |      |   |   |  |          |        |          |        |    |           |         |         |         |          |    |
|---------------------------------|---------|----------|------|----|---|------|------|-------|------|---|---|--|----------|--------|----------|--------|----|-----------|---------|---------|---------|----------|----|
| Model                           | C       | E        | UL   | KC |   | DNIV | Ship | appro | vals |   |   | Model  | C        | E      | UL       | KC     |    | DW        | Ship    | o appro | ovals   |          |    |
| MOUEI                           |         |          | cUL  | RC |   |      | LR   |       | RINA |   |   | MOdel  | EMC      |        | cUL      |        |    | DNV<br>GL |         |         | RINA    | NK       | KR |
| ◆FX5U CPU modules               | _       |          |      |    |   | 02   |      | _     | _    |   |   | ◆FX5 intelligent funct                             | tion mod | lule   |          |        | _  | 02        | _       |         |         | _        |    |
| FX5U-32MR/ES                    | 0       | 0        | 0    | 0  | 0 | 0    | 0    | 0     | 0    | 0 | 0 | FX5-4AD  | 0        |        | 0        | 0      | 0  | 0         | 0       | 0       | _       | 0        | _  |
| FX5U-32MT/ES                    | 0       | 0        | 0    | 0  | 0 | 0    | 0    | 0     | 0    | 0 | 0 | FX5-4DA  | 0        |        | 0        | 0      | 0  | 0         | 0       | 0       | _       | 0        | _  |
| FX5U-32MT/ESS                   | 0       | 0        | 0    | 0  | 0 | 0    | 0    | 0     | 0    | 0 | 0 | FX5-8AD  | 0        |        | 0        | 0      | 0  | 0         | 0       | 0       | 0       | 0        | 0  |
| FX5U-32MR/DS                    | 0       | 0        | 0    | 0  | 0 | 0    | 0    | 0     | 0    | 0 | 0 | FX5-4LC  | 0        |        | 0        | 0      | _  | —         |         | _       | _       | —        | _  |
| FX5U-32MT/DS                    | 0       |          | 0    | 0  | 0 | 0    | 0    | 0     | 0    | 0 | 0 | FX5-20PG-P   | 0        |        | 0        | 0      | _  | _         | -       | -       | _       | _        | _  |
| FX5U-32MT/DSS                   | 0       |          | 0    | 0  | 0 | 0    | 0    | 0     | 0    | 0 | 0 | FX5-20PG-D   | 0        |        | 0        | 0      | -  | —         | -       | -       | _       | —        | _  |
| FX5U-64MR/ES                    | 0       | 0        | 0    | 0  | 0 | 0    | 0    | 0     | 0    | 0 | 0 | FX5-40SSC-S  | 0        |        | 0        | 0      | -  | -         | -       | -       | —       | -        | _  |
| FX5U-64MT/ES                    | 0       | 0        | 0    | 0  | 0 | 0    | 0    | 0     | 0    | 0 | 0 | FX5-80SSC-S  | 0        |        | 0        | 0      | —  | -         | -       | -       | —       | -        | _  |
| FX5U-64MT/ESS                   | 0       | 0        | 0    | 0  | 0 | 0    | 0    | 0     | 0    | 0 | 0 | FX5-ENET   | 0        |        | 0        | 0      | -  | -         | -       | -       | _       | -        | _  |
| FX5U-64MR/DS                    | 0       | 0        | 0    | 0  | 0 | 0    | 0    | 0     | 0    | 0 | 0 | FX5-ENET/IP  | 0        |        | 0<br>0*1 | 0      | _  | _         | -       | -       | _       | -        | _  |
| FX5U-64MT/DS<br>FX5U-64MT/DSS   | 0       |          | 0    | 0  | 0 | 0    | 0    | 0     | 0    | 0 | 0 | FX5-CCL-MS<br>FX5-CCLIEF                           | 0        |        | 0.       | 0      | 0  | 0         | 0       | 0       | _       | 0        | _  |
| FX5U-80MR/ES                    | 0       | 0        | 0    | 0  | 0 | 0    | 0    | 0     | 0    | 0 | 0 | FX5-ASL-M  | 0        |        | 0        | 0      | _  | _         | _       | _       | _       | _        | _  |
| FX5U-80MT/ES                    | 0       | 0        | 0    | 0  | 0 | 0    | 0    | 0     | 0    | 0 | 0 | FX5-DP-M   |          |        | 0        | 0      | _  | _         | _       | _       | _       | _        | _  |
| FX5U-80MT/ESS                   | 0       | 0        | 0    | 0  | 0 | 0    | 0    | 0     | 0    | 0 | 0 | ◆FX5 extension pow                                 |          |        |          |        |    |           |         |         |         |          | _  |
| FX5U-80MR/DS                    | 0       | 0        | 0    | 0  | 0 | 0    | 0    | 0     | 0    | 0 | 0 | FX5-1PSU-5V  |          |        |          | 0      | 0  | 0         | 0       | 0       | 0       | 0        | 0  |
| FX5U-80MT/DS                    | 0       |          | 0    | 0  | 0 | 0    | 0    | 0     | 0    | 0 | 0 | FX5-C1PS-5V  | 0        |        | 0        | 0      | 0  | 0         | 0       | 0       | 0       | 0        | 0  |
| FX5U-80MT/DSS                   | 0       |          | 0    | 0  | 0 | 0    | 0    | 0     | 0    | 0 | 0 | ◆FX5 bus conversion                                |          |        |          |        |    |           |         |         |         |          |    |
| ◆FX5UC CPU module               | -       |          |      |    |   |      |      | -     | _    |   |   | FX5-CNV-BUS  | 0        |        | 0        | 0      | 0  | 0         | 0       | 0       | 0       | 0        | 0  |
| FX5UC-32MR/DS-TS                | 0       | 0        | 0    | 0  | - | -    | -    | -     | -    | - | - | FX5-CNV-BUSC                                       | 0        |        | 0        | 0      | 0  | 0         | 0       | 0       | 0       | 0        | 0  |
| FX5UC-32MT/D                    | 0       |          | 0    | 0  | 0 | 0    | 0    | 0     | 0    | 0 | 0 | ◆FX5 connector con                                 |          |        | L        |        |    |           |         |         |         |          |    |
| FX5UC-32MT/DS-TS                | 0       |          | 0    | 0  | 0 | 0    | 0    | 0     | 0    | 0 | 0 | FX5-CNV-IF   | 0        |        | 0        | 0      | 0  | 0         | 0       | 0       | 0       | 0        | 0  |
| FX5UC-32MT/DSS                  | 0       |          | 0    | 0  | 0 | 0    | 0    | 0     | 0    | 0 | 0 | FX5-CNV-IFC  | 0        |        | 0        | 0      | 0  | 0         | 0       | 0       | 0       | 0        | 0  |
| FX5UC-32MT/DSS-TS               | 0       |          | 0    | 0  | 0 | 0    | 0    | 0     | 0    | 0 | 0 | ◆FX5 connector con                                 | version  | adapte | r        |        |    |           |         |         |         |          |    |
| FX5UC-64MT/D                    | 0       |          | 0    | 0  | 0 | 0    | 0    | 0     | 0    | 0 | 0 | FX5-CNV-BC   | 0        |        | -        | 0      | 0  | 0         | 0       | 0       | 0       | 0        | 0  |
| FX5UC-64MT/DSS                  | 0       |          | 0    | 0  | 0 | 0    | 0    | 0     | 0    | 0 | 0 | ◆FX5 extended extended                             | nsion ca | ble    |          |        |    |           |         |         |         |          |    |
| FX5UC-96MT/D                    | 0       |          | 0    | 0  | 0 | 0    | 0    | 0     | 0    | 0 | 0 | FX5-30EC   | 0        |        | _        | —      | -  | _         | _       | _       | —       | -        | _  |
| FX5UC-96MT/DSS                  | 0       |          | 0    | 0  | 0 | 0    | 0    | 0     | 0    | 0 | 0 | FX5-65EC   | 0        |        |          | _      |    |           |         |         | _       | -        | _  |
| ◆FX5 I/O modules (ter           | minal k | block ty | /pe) |    |   |      |      |       |      |   |   | ◆FX5 expansion ada                                 | pter     |        |          |        |    |           |         |         |         |          |    |
| FX5-8EX/ES                      | 0       |          | 0    | 0  | 0 | 0    | 0    | 0     | 0    | 0 | 0 | FX5-4AD-ADP  | 0        |        | 0        | 0      | 0  | 0         | 0       | 0       | 0       | 0        | 0  |
| FX5-8EYR/ES                     | 0       | 0        | 0    | 0  | 0 | 0    | 0    | 0     | 0    | 0 | 0 | FX5-4AD-PT-ADP                                     | 0        |        | 0        | 0      | 0  | 0         | 0       | 0       | 0       | 0        | 0  |
| FX5-8EYT/ES                     | 0       |          | 0    | 0  | 0 | 0    | 0    | 0     | 0    | 0 | 0 | FX5-4AD-TC-ADP                                     | 0        |        | 0        | 0      | 0  | 0         | 0       | 0       | 0       | 0        | 0  |
| FX5-8EYT/ESS                    | 0       |          | 0    | 0  | 0 | 0    | 0    | 0     | 0    | 0 | 0 | FX5-4DA-ADP  | 0        |        | 0*2      | 0      | 0  | 0         | 0       | 0       | 0       | 0        | 0  |
| FX5-16EX/ES                     | 0       |          | 0    | 0  | 0 | 0    | 0    | 0     | 0    | 0 | 0 | FX5-232ADP<br>FX5-485ADP                           | 0        |        | 0        | 0      | 0  | 0         | 0       | 0       | 0       | 0        | 0  |
| FX5-16EYR/ES<br>FX5-16EYT/ES    | 0       |          | 0    | 0  | 0 | 0    | 0    | 0     | 0    | 0 | 0 |  |          |        |          |        |    |           |         |         |         | 0        | 0  |
| FX5-16EYT/ESS                   | 0       |          | 0    | 0  | 0 | 0    | 0    | 0     | 0    | 0 | 0 | ◆FX5U expansion bo<br>FX5-232-BD                   |          |        | _        | 0      | 0  | 0         | 0       | 0       | 0       | 0        | 0  |
| FX5-16ET/ES-H                   | 0       |          | 0    | 0  | 0 | 0    | 0    | 0     | 0    | 0 | 0 | FX5-485-BD   | 0        |        | _        | 0      | 0  | 0         | 0       | 0       | 0       | 0        | 0  |
| FX5-16ET/ESS-H                  | 0       |          | 0    | 0  | 0 | 0    | 0    | 0     | 0    | 0 | 0 | FX5-422-BD-GOT                                     | 0        |        | -        | 0      | 0  | 0         | 0       | 0       | 0       | 0        | 0  |
| FX5-16ER/ES                     | 0       | 0        | 0    | 0  | 0 | 0    | 0    | 0     | 0    | 0 | 0 | O : Compliant with s                               |          |        |          |        |    |           | -       |         |         | 0        |    |
| FX5-16ET/ES                     | 0       |          | 0    | 0  | 0 | 0    | 0    | 0     | 0    | 0 | 0 | *1: The products (p                                |          |        |          |        |    |           |         |         |         | nd afte  | r  |
| FX5-16ET/ESS                    | 0       |          | 0    | 0  | 0 | 0    | 0    | 0     | 0    | 0 | 0 | complies with t<br>*2: The products (p             | he UL s  | tanda  | rds (UL  | _, cUL | ). | ife et u  | e el in | lune 0  | 010 00  | d office |    |
| FX5-32ER/ES                     | 0       | 0        | 0    | 0  | 0 | 0    | 0    | 0     | 0    | 0 | 0 | *2: The products (p<br>complies with the products) |          |        |          |        |    | uactu     | eu in c | June 2  | u io ar | iu afte  |    |
| FX5-32ET/ES                     | 0       | 0        | 0    | 0  | 0 | 0    | 0    | 0     | 0    | 0 | 0 |  |          |        |          |        |    |           |         |         |         |          |    |
| FX5-32ET/ESS                    | 0       | 0        | 0    | 0  | 0 | 0    | 0    | 0     | 0    | 0 | 0 |  |          |        |          |        |    |           |         |         |         |          |    |
| FX5-32ER/DS                     | 0       | 0        | 0    | 0  | 0 | 0    | 0    | 0     | 0    | 0 | 0 |  |          |        |          |        |    |           |         |         |         |          |    |
| FX5-32ET/DS                     | 0       |          | 0    | 0  | 0 | 0    | 0    | 0     | 0    | 0 | 0 |  |          |        |          |        |    |           |         |         |         |          |    |
| FX5-32ET/DSS                    | 0       |          | 0    | 0  | 0 | 0    | 0    | 0     | 0    | 0 | 0 |  |          |        |          |        |    |           |         |         |         |          |    |
| ◆FX5 I/O modules (co            |         |          | 1    |    |   |      |      |       |      |   |   |  |          |        |          |        |    |           |         |         |         |          |    |
| FX5-C16EX/D                     | 0       |          | 0    | 0  | 0 | 0    | 0    | 0     | 0    | 0 | 0 |  |          |        |          |        |    |           |         |         |         |          |    |
| FX5-C16EX/DS                    | 0       |          | 0    | 0  | 0 | 0    | 0    | 0     | 0    | 0 | 0 |  |          |        |          |        |    |           |         |         |         |          |    |
| FX5-C16EYT/D                    | 0       |          | 0    | 0  | 0 | 0    | 0    | 0     | 0    | 0 | 0 |  |          |        |          |        |    |           |         |         |         |          |    |
| FX5-C16EYT/DSS                  | 0       |          | 0    | 0  | 0 | 0    | 0    | 0     | 0    | 0 | 0 |  |          |        |          |        |    |           |         |         |         |          |    |
| FX5-C16EYR/D-TS                 | 0       | 0        | 0    | 0  | - | -    | -    | -     | -    | _ | - |  |          |        |          |        |    |           |         |         |         |          |    |
| FX5-C32EX/D                     | 0       |          | 0    | 0  | 0 | 0    | 0    | 0     | 0    | 0 | 0 |  |          |        |          |        |    |           |         |         |         |          |    |
| FX5-C32EX/DS                    | 0       |          | 0    | 0  | 0 | 0    | 0    | 0     | 0    | 0 | 0 |  |          |        |          |        |    |           |         |         |         |          |    |
| FX5-C32EX/DS-TS<br>FX5-C32EYT/D | 0       |          | 0    | 0  | 0 | 0    | 0    | 0     | 0    | 0 | 0 |  |          |        |          |        |    |           |         |         |         |          |    |
| FX5-C32EYT/D-TS                 | 0       |          | 0    | 0  | 0 | 0    | 0    | 0     | 0    | 0 | 0 |  |          |        |          |        |    |           |         |         |         |          |    |
| FX5-C32EYT/DSS                  | 0       |          | 0    | 0  | 0 | 0    | 0    | 0     | 0    | 0 | 0 |  |          |        |          |        |    |           |         |         |         |          |    |
| FX5-C32EYT/DSS-TS               | 0       |          | 0    | 0  | 0 | 0    | 0    | 0     | 0    | 0 | 0 |  |          |        |          |        |    |           |         |         |         |          |    |
| FX5-C32ET/D                     | 0       |          | 0    | 0  | 0 | 0    | 0    | 0     | 0    | 0 | 0 |  |          |        |          |        |    |           |         |         |         |          |    |
| FX5-C32ET/DS-TS                 | 0       |          | 0    | 0  | 0 | 0    | 0    | 0     | 0    | 0 | 0 |  |          |        |          |        |    |           |         |         |         |          |    |
| FX5-C32ET/DSS                   | 0       |          | 0    | 0  | 0 | 0    | 0    | 0     | 0    | 0 | 0 |  |          |        |          |        |    |           |         |         |         |          |    |
| FX5-C32ET/DSS-TS                | 0       |          | 0    | 0  | 0 | 0    | 0    | 0     | 0    | 0 | 0 |  |          |        |          |        |    |           |         |         |         |          |    |
|                                 | 2       |          | -    |    |   | -    | -    | -     | -    | - | - |  |          |        |          |        |    |           |         |         |         |          |    |



### Performance specifications



#### ◇ FX5U/FX5UC CPU module performance specifications

|                                  | Items                                | Specifications   |
|----------------------------------|--------------------------------------|--|
| Control system                   |                                      | Stored-program repetitive operation  |
| Input/output control system      |                                      | Refresh system (Direct access input/output allowed by specification of direct access input/output [DX, DY])  |
|                                  | Programming language                 | Ladder diagram (LD), structured text (ST), function block diagram/ladder language (FBD/LD)                   |
|                                  | Programming expansion function       | Function block (FB), function (FUN), label programming (local/global)  |
|                                  | Constant scan                        | 0.2 to 2000 ms (can be set in 0.1 ms increments)   |
| Programming specifications       | Fixed cycle interrupt                | 1 to 60000 ms (can be set in 1 ms increments)  |
|                                  | Timer performance specifications     | 100 ms, 10 ms, 1 ms  |
|                                  | No. of program executions            | 32   |
|                                  | No. of FB files                      | 16 (Up to 15 for user)   |
| Operation specifications         | Execution type                       | Standby type, initial execution type, scan execution type, fixed-cycle execution type, event execution type  |
| Operation specifications         | Interrupt type                       | Internal timer interrupt, input interruption, high-speed comparison match interrupt, interrupt from module*1 |
| Instruction processing time      | LD X0                                | 34 ns*2  |
| instruction processing time      | MOV D0 D1                            | 34 ns*2  |
|                                  | Program capacity                     | 64 k/128 k steps (128 kbytes/256 kbytes, flash memory)   |
| Mamanu appaoitu                  | SD memory card                       | Memory card capacity (SD/SDHC memory card: Max. 16 Gbytes)   |
| Memory capacity                  | Device/label memory                  | 120 kbytes   |
|                                  | Data memory/standard ROM             | 5 Mbytes   |
| Flash memory (Flash ROM) w       | rite count                           | Max. 20000 times   |
|                                  | Device/label memory                  | 1  |
|                                  | Data memory                          |  |
| File storage capacity            | P: No. of program files              | P: 32, FB: 16  |
|                                  | FB: No. of FB files                  |  |
|                                  | SD memory card                       | 2 Gbytes: 511*4, 4 G/8 G/16 Gbytes: 65534*4  |
| Clock function                   | Display data                         | Year, month, day, hour, minute, second, day of week (leap year automatic detection)                          |
|                                  | Precision                            | Monthly difference: ±45 sec at 25°C (typical value)  |
|                                  | (1) No. of input/output points       | 256 points or less/384 points or less*3  |
| No. of input/output points       | (2) No. of remote I/O points         | 384 points or less/512 points or less*3  |
|                                  | Total No. of points of (1) and (2)   | 512 points or less   |
| Power failure retention          | Retention method                     | Large-capacity capacitor   |
| (Clock data*5)                   | Retention time                       | 10 days (Ambient temperature: 25°C (77°F))   |
| Power failure retention (Device) | Capacity for power failure retention | 12 K words maximum*6   |

\*1: Interrupt from the intelligent function module and high-speed pulse input/output module.

\*2: When the program capacity is 64 k steps.
\*3: Supported by FX5U/FX5UC CPU modules Ver. 1.100 or later and by GX Works3 Ver. 1.047Z or later.
\*4: The value listed above indicates the number of files stored in the root folder.

\*41 the value listed above indicates the number of lise stored in the root folder.
 \*5: Clock data is retained using the power accumulated in a large-capacity capacitor incorporated into the PLC. When voltage of the large-capacity capacitor drops, clock data is no longer accurately retained. The retention period of a fully charged capacitor (electricity is conducted across the PLC for at least 30 minutes) is 10 days (ambient temperature: 25°C (77°F)). How long the capacitor can hold the data depends on the operating ambient temperature. When the operating ambient temperature is high, the holding period is short.
 \*6: All devices in the (high-speed) device area can be held against power failure. Devices in the (standard) device area can be held also when the optional battery is mounted.

#### Oumber of device points

|                               |                              |                         |    |  | Max. number of points  |  |  |  |
|-------------------------------|------------------------------|-------------------------|----|--|--|--|--|--|
|                               | Input relay (X)              |                         | 8  | 1024 points or less                                      | The total number of X and Y assigned to input/output points is up to 256 points/ |  |  |  |
|                               | Output relay (Y)             |                         | 8  | 1024 points or less                                      | 384 points*1.  |  |  |  |
|                               | Internal relay (M)           |                         | 10 | 32768 points (can be chang                               | ged with parameter)*2  |  |  |  |
|                               | Latch relay (L)              |                         | 10 | 32768 points (can be chang                               | ged with parameter)*2  |  |  |  |
|                               | Link relay (B)               |                         | 16 | 32768 points (can be chang                               | ged with parameter)*2  |  |  |  |
|                               | Annunciator (F)              |                         | 10 | 32768 points (can be chang                               | ged with parameter)*2  |  |  |  |
|                               | Link special relay           | (SB)                    | 16 | 32768 points (can be chang                               | ged with parameter)*2  |  |  |  |
| No. of user device points     | Step relay (S)               |                         | 10 | 4096 points (fixed)                                      |  |  |  |  |
| Into. of user device points   | Timer system                 | Timer (T)               | 10 | 1024 points (can be change                               | ed with parameter)*2   |  |  |  |
|                               | Accumulation<br>timer system | Accumulation timer (ST) | 10 | 1024 points (can be change                               | ed with parameter)*2   |  |  |  |
|                               | Counter quators              | Counter (C)             | 10 | 1024 points (can be change                               |  |  |  |  |
|                               | Counter system               | Long counter (LC)       | 10 | 1024 points (can be change                               | ed with parameter)*2   |  |  |  |
|                               | Data register (D)            |                         | 10 | 8000 points (can be changed with parameter)*2            |  |  |  |  |
|                               | Link register (W)            |                         | 16 | 32768 points (can be changed with parameter)*2           |  |  |  |  |
|                               | Link special regis           | ter (SW)                | 16 | 32768 points (can be chang                               | ged with parameter)*2  |  |  |  |
| No. of system device points   | Special relay (SM            | )                       | 10 | 10000 points (fixed)                                     |  |  |  |  |
| Ind. of system device points  | Special register (S          | SD)                     | 10 | 12000 points (fixed)                                     |  |  |  |  |
| Module access device          | Intelligent function         |                         | 10 | 65536 points (designated b                               | y U□\G□)   |  |  |  |
| No. of index register points  | Index register (Z)*          | k3                      | 10 | 24 points  |  |  |  |  |
| INO. OF INDEX register points | Long index regist            | er (LZ)*3               | 10 | 12 points  |  |  |  |  |
| No. of file register points   | File register (R)            |                         | 10 | 32768 points (can be chang                               |  |  |  |  |
| TND. OF THE TEGISTER POINTS   | Extended file regi           | ster (ER)               | 10 | 32768 points (are stored in                              | SD memory card)  |  |  |  |
| No. of nesting points         | Nesting (N)                  |                         | 10 | 15 points (fixed)  |  |  |  |  |
| No. of pointer points         | Pointer (P)                  |                         | 10 | 4096 points  |  |  |  |  |
| No. of pointer points         | Interrupt pointer (          | l)                      | 10 | 178 points (fixed)                                       |  |  |  |  |
|                               | Decimal constant             | Signed                  | -  | 16 bits: -32768 to +32767,<br>32 bits: -2147483648 to +2 | 147483647  |  |  |  |
| Othere                        | (K)                          | Unsigned                | -  | 16 bits: 0 to 65535,<br>32 bits: 0 to 4294967295         |  |  |  |  |
| Others                        | Hexadecimal con              | stant (H)               | _  | 16 bits: 0 to FFFF,<br>32 bits: 0 to FFFFFFFF            |  |  |  |  |
|                               | Real constant (E)            | Single precision        | _  | E-3.40282347+38 to E-1.17                                | 549435-38, 0, E1.17549435-38 to E3.40282347+38                                   |  |  |  |
|                               | Character string             |                         | —  | Shift-JIS code max. 255 sir                              | gle-byte characters (256 including NULL)   |  |  |  |

\*1: Supported by FX5U/FX5UC CPU modules Ver. 1.100 or later and by GX Works3 Ver. 1.047Z or later.
 \*2: Can be changed with parameters within the capacity range of the CPU built-in memory.
 \*3: Total of the index register (Z) and long index register (LZ) is maximum 24 words.

### List of instructions

#### $\bigcirc$ CPU module application instruction

|                      | Instruction<br>symbol |  | CPU r | oatible<br>nodule |     |        |
|----------------------|-----------------------|--|-------|-------------------|-----|--------|
|                      | -                     |  | FX5U  | FX5UC             |     |        |
|                      | ROR(P)                | 16-bit data right rotation                                 | 0     | 0                 |     |        |
|                      | RCR(P)                | Right rotation with 16-bit data carry                      | 0     | 0                 |     |        |
|                      | ROL(P)                | 16-bit data left rotation                                  | 0     | 0                 |     |        |
| Rotation             | RCL(P)                | Left rotation with 16-bit data carry                       | 0     | 0                 |     |        |
| i lotation           | DROR(P)               | 32-bit data right rotation                                 | 0     | 0                 |     |        |
|                      | DRCR(P)               | Right rotation with 32-bit data carry                      | 0     | 0                 |     |        |
|                      | DROL(P)               | 32-bit data left rotation                                  | 0     | 0                 |     |        |
|                      | DRCL(P)               | Left rotation with bit data carry                          | 0     | 0                 |     |        |
| Program              | CJ(P)                 | Pointer branch   | 0     | 0                 |     |        |
| branch               | GOEND                 | Jump to END  | 0     | 0                 |     |        |
|                      | DI                    | Interrupt disable  | 0     | 0                 |     |        |
|                      | EI                    | Interrupt enable   | 0     | 0                 |     |        |
| Ducanon              | DI                    | Interrupt disable when lower than specified priority       | 0     | 0                 |     |        |
| Program<br>execution | IMASK                 | Interrupt program mask                                     | 0     | 0                 |     |        |
| control              | SIMASK                |  |       |                   |     |        |
|                      |                       | Specified interrupt pointer disable/enable                 | 0     | 0                 |     |        |
|                      | IRET                  | Return from interrupt program                              | 0     | 0                 |     |        |
|                      | WDT(P)                | WDT reset  | 0     | 0                 |     |        |
|                      | FOR                   | Executed (n) times between ROM instruction and             | 0     | 0                 |     |        |
|                      | NEXT                  | NEXT instruction   | 0     | 0                 |     |        |
| Structured           | BREAK(P)              | FOR to NEXT forced end                                     | 0     | 0                 |     |        |
| instruction          | CALL(P)               | Subroutine program call                                    | 0     | 0                 |     |        |
|                      | RET                   | Return from subroutine program                             | 0     | 0                 |     |        |
|                      | SRET                  | neturn ion subioutine program                              | 0     | 0                 |     |        |
|                      | XCALL                 | Subroutine program call                                    | 0     | 0                 |     |        |
|                      | SFRD(P)               | First-in data read from data table                         | 0     | 0                 |     |        |
|                      | POP(P)                | Last-in data read from data table                          | 0     | 0                 |     |        |
| Data table           | SFWR(P)               | Data write to data table                                   | 0     | 0                 |     |        |
| operation            | FINS(P)               | Data insertion to data table                               | 0     | 0                 |     |        |
|                      | FDEL(P)               | Data delete from data table                                | 0     | 0                 |     |        |
|                      | LD\$=                 | Character string comparison LD (S1) = (S2)                 | 0     | 0                 |     |        |
|                      | LD\$<>                | Character string comparison LD (S1) $>$ (S2)               | 0     | 0                 |     |        |
|                      | LD\$>                 | • • • • • •  | 0     | 0                 |     |        |
|                      | LD\$><br>LD\$<=       | Character string comparison LD (S1) > (S2)                 |       |                   |     |        |
|                      | · ·                   | Character string comparison LD (S1) $\leq$ (S2)            | 0     | 0                 |     |        |
|                      | LD\$<                 | Character string comparison LD (S1) < (S2)                 | 0     | 0                 |     |        |
|                      | LD\$>=                | Character string comparison LD (S1) >= (S2)                | 0     | 0                 |     | Actual |
|                      | AND\$=                | Character string comparison AND (S1) = (S2)                | 0     | 0                 |     | number |
|                      | AND\$<>               | Character string comparison AND (S1) <> (S2)               | 0     | 0                 |     |        |
|                      | AND\$>                | Character string comparison AND (S1) > (S2)                | 0     | 0                 |     |        |
|                      | AND\$<=               | Character string comparison AND (S1) <= (S2)               | 0     | 0                 |     |        |
|                      | AND\$<                | Character string comparison AND (S1) < (S2)                | 0     | 0                 |     |        |
|                      | AND\$>=               | Character string comparison AND (S1) >= (S2)               | 0     | 0                 |     |        |
|                      | OR\$=                 | Character string comparison OR (S1) = (S2)                 | 0     | 0                 |     |        |
|                      | OR\$<>                | Character string comparison OR (S1) <> (S2)                | 0     | 0                 |     |        |
|                      | OR\$>                 | Character string comparison OR (S1) > (S2)                 | 0     | 0                 |     |        |
|                      | OR\$<=                | Character string comparison OR (S1) <= (S2)                | 0     | 0                 |     |        |
| Character            | OR\$<                 | Character string comparison OR (S1) < (S2)                 | 0     | 0                 |     |        |
| Character<br>string  | OR\$>=                | Character string comparison OR $(S1) >= (S2)$              | 0     | 0                 |     |        |
| processing           | \$+(P)                | Combination of character strings                           | 0     | 0                 |     |        |
|                      | \$MOV(P)              | J  |       |                   |     |        |
|                      |                       | Transfer of character string                               | 0     | 0                 |     |        |
|                      | BINDA(P)(_U)          | BIN 16-bit data → Decimal ASCII conversion                 | 0     | 0                 |     |        |
|                      | DBINDA(P)(_U)         | BIN 32-bit data → Decimal ASCII conversion                 | 0     | 0                 |     |        |
|                      | ASCI(P)               | HEX code data → ASCII conversion                           | 0     | 0                 |     |        |
|                      | STR(P)(_U)            | BIN 16-bit data → Character string conversion              | 0     | 0                 |     |        |
|                      | DSTR(P)(_U)           | BIN 32-bit data → Character string conversion              | 0     | 0                 |     |        |
|                      | ESTR(P)               | Single precision actual number →                           | 0     | 0                 |     |        |
|                      | DESTR(P)              | Character string conversion                                | 0     | 0                 | , I |        |
|                      | LEN(P)                | Detection of character string length                       | 0     | 0                 |     |        |
|                      | RIGHT(P)              | Extraction from right side of character string             | 0     | 0                 |     |        |
|                      | LEFT(P)               | Extraction from left side of character string              | 0     | 0                 | , I |        |
|                      | MIDR(P)               | Extraction of any part from the middle of character string | 0     | 0                 |     |        |
|                      | MIDW(P)               | Replacement of any part in the middle of character string  | 0     | 0                 |     |        |
|                      |                       | Character string search                                    | 0     | 0                 |     |        |
|                      | INSTR(P)              |  |       |                   |     |        |
|                      | INSTR(P)<br>STRINS(P) | Character string insertion                                 | 0     | 0                 |     |        |

| icatior | Instruction  | Function  |   |                       |
|---------|--|---|---|-----------------------|
|         | symbol   | T unction   | FX5U  | FX5U0                 |
|         | LDE\$=   | Single precision actual number comparison LDE (S1) = (S2)   | 0   | 0                     |
|         | LDE\$<>  | Single precision actual number comparison LDE (S1) <> (S2)  | 0   | 0                     |
|         | LDE\$>   | Single precision actual number comparison LDE (S1) > (S2)   | 0   | 0                     |
|         | LDE\$<=  | Single precision actual number comparison LDE (S1) <= (S2)  | 0   | 0                     |
|         | LDE\$<   | Single precision actual number comparison LDE (S1) > (S2)   | 0   | 0                     |
|         | LDE\$>=  | Single precision actual number comparison LDE (S1) $>=$ (S2)  | 0   | 0                     |
|         | ANDE\$=  | Single precision actual number comparison ANDE $(S1) = (S2)$  | 0   | 0                     |
|         | ANDE\$<>   | Single precision actual number comparison ANDE $(S1) < (S2)$  | 0   | 0                     |
|         | ANDE\$>  | Single precision actual number comparison ANDE (S1) < (S2)  | 0   | 0                     |
|         |  |   |   |                       |
|         | ANDE\$<=   | Single precision actual number comparison ANDE (S1) <= (S2)   | 0   | 0                     |
|         | ANDE\$<  | Single precision actual number comparison ANDE (S1) < (S2)  | 0   | 0                     |
|         | ANDE\$>=   | Single precision actual number comparison ANDE (S1) >= (S2)   | 0   | 0                     |
|         | ORE\$=   | Single precision actual number comparison ORE (S1) = (S2)   | 0   | 0                     |
|         | ORE\$<>  | Single precision actual number comparison ORE (S1) <> (S2)  | 0   | 0                     |
|         | ORE\$>   | Single precision actual number comparison ORE (S1) > (S2)   | 0   | 0                     |
|         | ORE\$<=  | Single precision actual number comparison ORE (S1) <= (S2)  | 0   | 0                     |
|         | ORE\$<   | Single precision actual number comparison ORE (S1) < (S2)   | 0   | 0                     |
|         | ORE\$>=  | Single precision actual number comparison ORE (S1) >= (S2)  | 0   | 0                     |
|         | DECMP(P)   | Single precision actual number comparison   | 0   | 0                     |
|         | DEZCP(P)   | Binary floating point bandwidth comparison  | 0   | 0                     |
|         | E+(P)  | Single precision actual number addition   | 0   | 0                     |
|         | E-(P)  | Single precision actual number subtraction  | 0   | 0                     |
|         |  | 0 1   | 0   | 0                     |
|         | DEADD(P)   | Single precision actual number addition   |   |                       |
|         | DESUB(P)   | Single precision actual number subtraction  | 0   | 0                     |
|         | E*(P)  | Single precision actual number multiplication   | 0   | 0                     |
|         | E/(P)  | Single precision actual number division   | 0   | 0                     |
|         | DEMUL(P)   | Single precision actual number multiplication   | 0   | 0                     |
|         | DEDIV(P)   | Single precision actual number division   | 0   | 0                     |
|         | INT2FLT(P)   | Signed BIN 16-bit data →  | 0   | 0                     |
|         |  | Single precision actual number conversion   |   |                       |
|         | UINT2FLT(P)  | Unsigned BIN 16-bit data →<br>Single precision actual number conversion   | 0   | 0                     |
|         | DINT2FLT(P)  | Signed BIN 32-bit data →<br>Single-precision real number conversion   | 0   | 0                     |
|         | UDINT2FLT(P)   | Unsigned BIN 32-bit data →<br>Single precision actual number conversion   | 0   | 0                     |
| r       | EVAL(P)  | Character string →  | 0   | 0                     |
|         | DEVAL(P)   | Single precision actual number conversion   | 0   | 0                     |
|         | DEBCD(P)   | Binary floating point → Decimal floating point conversion   | 0   | 0                     |
|         | DEBIN(P)   | Decimal floating point → Binary floating point conversion   | 0   | 0                     |
|         | ENEG(P)  |   | 0   | 0                     |
|         | DENEG(P)   | Reverse of single precision actual number sign  | 0   | 0                     |
|         | EMOV(P)  |   | 0   | 0                     |
|         | DEMOV(P)   | Transfer of single precision actual number data   | 0   | 0                     |
|         |  |   | -   | -                     |
|         |  | Single precision actual number SIN operation  | 0   | 0                     |
|         | SIN(P)   | -   |   | 0                     |
|         | DSIN(P)  |   | 0   |                       |
|         | DSIN(P)<br>COS(P)  | Single precision actual number COS operation  | 0   | 0                     |
|         | DSIN(P)<br>COS(P)<br>DCOS(P)   | Single precision actual number COS operation  | 0   | 0                     |
|         | DSIN(P)<br>COS(P)<br>DCOS(P)<br>TAN(P)   |   | 0   |                       |
|         | DSIN(P)<br>COS(P)<br>DCOS(P)   | Single precision actual number COS operation  | 0   | 0                     |
|         | DSIN(P)<br>COS(P)<br>DCOS(P)<br>TAN(P)<br>DTAN(P)<br>ASIN(P)   | Single precision actual number TAN operation  | 0   | 0                     |
|         | DSIN(P)<br>COS(P)<br>DCOS(P)<br>TAN(P)<br>DTAN(P)  |   | 0<br>0<br>0   | 0                     |
|         | DSIN(P)<br>COS(P)<br>DCOS(P)<br>TAN(P)<br>DTAN(P)<br>ASIN(P)   | Single precision actual number TAN operation<br>Single precision actual number SIN-1 operation  | 0<br>0<br>0<br>0  | 0<br>0<br>0           |
|         | DSIN(P)<br>COS(P)<br>DCOS(P)<br>TAN(P)<br>DTAN(P)<br>ASIN(P)<br>DASIN(P)   | Single precision actual number TAN operation  | 0<br>0<br>0<br>0  | 0<br>0<br>0<br>0      |
|         | DSIN(P)<br>COS(P)<br>DCOS(P)<br>TAN(P)<br>DTAN(P)<br>ASIN(P)<br>DASIN(P)<br>ACOS(P)  | Single precision actual number TAN operation<br>Single precision actual number SIN-1 operation<br>Single precision actual number COS-1 Operation  | 0<br>0<br>0<br>0<br>0   | 0<br>0<br>0<br>0      |
|         | DSIN(P)<br>COS(P)<br>DCOS(P)<br>TAN(P)<br>DTAN(P)<br>ASIN(P)<br>ASIN(P)<br>ACOS(P)<br>DACOS(P)<br>ATAN(P)  | Single precision actual number TAN operation<br>Single precision actual number SIN-1 operation  |   | 0<br>0<br>0<br>0<br>0 |
|         | DSIN(P)<br>COS(P)<br>DCOS(P)<br>TAN(P)<br>DTAN(P)<br>ASIN(P)<br>ACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DATAN(P)  | Single precision actual number TAN operation Single precision actual number SIN <sup>-1</sup> operation Single precision actual number COS <sup>-1</sup> Operation Single precision accuracy TAN <sup>-1</sup> operation  | 0<br>0<br>0<br>0<br>0<br>0<br>0<br>0<br>0<br>0                |                       |
|         | DSIN(P)<br>COS(P)<br>DCOS(P)<br>TAN(P)<br>DTAN(P)<br>ASIN(P)<br>ACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DATAN(P)<br>RAD(P)  | Single precision actual number TAN operation<br>Single precision actual number SIN-1 operation<br>Single precision actual number COS-1 Operation  | 0<br>0<br>0<br>0<br>0<br>0<br>0<br>0<br>0<br>0<br>0           |                       |
|         | DSIN(P)<br>COS(P)<br>DCOS(P)<br>TAN(P)<br>DTAN(P)<br>ASIN(P)<br>DASIN(P)<br>DASIN(P)<br>DACOS(P)<br>DACOS(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)   | Single precision actual number TAN operation Single precision actual number SIN-1 operation Single precision actual number COS-1 Operation Single precision accuracy TAN-1 operation Single precision actual number angle Radian conversion   | 0<br>0<br>0<br>0<br>0<br>0<br>0<br>0<br>0<br>0<br>0<br>0<br>0 |                       |
|         | DSIN(P)<br>COS(P)<br>DCOS(P)<br>TAN(P)<br>DTAN(P)<br>ASIN(P)<br>DASIN(P)<br>DASIN(P)<br>DACOS(P)<br>DACOS(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAD(P)<br>DEG(P)   | Single precision actual number TAN operation Single precision actual number SIN-1 operation Single precision actual number COS-1 Operation Single precision accuracy TAN-1 operation Single precision actual number angle Radian conversion Single precision actual number radian   |   |                       |
|         | DSIN(P)<br>COS(P)<br>DCOS(P)<br>TAN(P)<br>DTAN(P)<br>ASIN(P)<br>DASIN(P)<br>ACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DRAD(P)<br>DEG(P)   | Single precision actual number TAN operation Single precision actual number SIN-1 operation Single precision actual number COS-1 Operation Single precision accuracy TAN-1 operation Single precision actual number angle Radian conversion   |   |                       |
|         | DSIN(P)<br>COS(P)<br>DCOS(P)<br>TAN(P)<br>DTAN(P)<br>DASIN(P)<br>ACOS(P)<br>ACOS(P)<br>ACOS(P)<br>ATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DEG(P)<br>DDEG(P)<br>DESQR(P)   | Single precision actual number TAN operation Single precision actual number SIN-1 operation Single precision actual number COS-1 Operation Single precision accuracy TAN-1 operation Single precision actual number angle Radian conversion Single precision actual number radian   |   |                       |
|         | DSIN(P)<br>COS(P)<br>DCOS(P)<br>TAN(P)<br>DTAN(P)<br>DASIN(P)<br>ASIN(P)<br>ACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DATAN(P)<br>RAD(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN | Single precision actual number TAN operation Single precision actual number SIN <sup>-1</sup> operation Single precision actual number COS <sup>-1</sup> Operation Single precision accuracy TAN <sup>-1</sup> operation Single precision actual number angle → Radian conversion Single precision actual number radian → Angle conversion  |   |                       |
|         | DSIN(P)<br>COS(P)<br>DCOS(P)<br>TAN(P)<br>DTAN(P)<br>DASIN(P)<br>DASIN(P)<br>ACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DATAN(P)<br>RAD(P)<br>DATAN(P)<br>RAD(P)<br>DEG(P)<br>DEG(P)<br>DEG(P)<br>DESQR(P)<br>ESQRT(P)   | Single precision actual number TAN operation Single precision actual number SIN <sup>-1</sup> operation Single precision actual number COS <sup>-1</sup> Operation Single precision accuracy TAN <sup>-1</sup> operation Single precision actual number angle → Radian conversion Single precision actual number radian → Angle conversion  |   |                       |
|         | DSIN(P)<br>COS(P)<br>DCOS(P)<br>TAN(P)<br>DTAN(P)<br>DASIN(P)<br>ASIN(P)<br>ACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DESOR(P)<br>ESQRT(P)<br>EXP(P)  | Single precision actual number TAN operation Single precision actual number SIN <sup>-1</sup> operation Single precision actual number COS <sup>-1</sup> Operation Single precision accuracy TAN <sup>-1</sup> operation Single precision actual number angle → Radian conversion Single precision actual number radian → Angle conversion Square root of single precision actual number  |   |                       |
|         | DSIN(P)<br>COS(P)<br>DCOS(P)<br>TAN(P)<br>DTAN(P)<br>ASIN(P)<br>ACOS(P)<br>DACOS(P)<br>ATAN(P)<br>BAD(P)<br>DACOS(P)<br>DATAN(P)<br>BAD(P)<br>DEG(P)<br>DEG(P)<br>DEG(P)<br>DESQR(P)<br>ESQRT(P)<br>EXP(P)<br>DEXP(P)<br>LOG(P)  | Single precision actual number TAN operation Single precision actual number SIN <sup>-1</sup> operation Single precision actual number COS <sup>-1</sup> Operation Single precision actual number angle Radian conversion Single precision actual number radian Angle conversion Square root of single precision actual number Index operation of single precision actual number  |   |                       |
|         | DSIN(P)<br>COS(P)<br>DCOS(P)<br>TAN(P)<br>DTAN(P)<br>DASIN(P)<br>ASIN(P)<br>ACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DESOR(P)<br>ESQRT(P)<br>EXP(P)  | Single precision actual number TAN operation Single precision actual number SIN <sup>-1</sup> operation Single precision actual number COS <sup>-1</sup> Operation Single precision accuracy TAN <sup>-1</sup> operation Single precision actual number angle → Radian conversion Single precision actual number radian → Angle conversion Square root of single precision actual number  |   |                       |
|         | DSIN(P)<br>COS(P)<br>DCOS(P)<br>TAN(P)<br>DTAN(P)<br>ASIN(P)<br>ACOS(P)<br>DACOS(P)<br>ATAN(P)<br>BAD(P)<br>DACOS(P)<br>DATAN(P)<br>BAD(P)<br>DEG(P)<br>DEG(P)<br>DEG(P)<br>DESQR(P)<br>ESQRT(P)<br>EXP(P)<br>DEXP(P)<br>LOG(P)  | Single precision actual number TAN operation Single precision actual number SIN <sup>-1</sup> operation Single precision actual number COS <sup>-1</sup> Operation Single precision actual number angle Radian conversion Single precision actual number radian Angle conversion Square root of single precision actual number Index operation of single precision actual number  |   |                       |
|         | DSIN(P)<br>COS(P)<br>DCOS(P)<br>TAN(P)<br>DTAN(P)<br>ASIN(P)<br>DASIN(P)<br>DASIN(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACO   | Single precision actual number TAN operation Single precision actual number SIN <sup>-1</sup> operation Single precision actual number COS <sup>-1</sup> Operation Single precision actual number angle → Radian conversion Single precision actual number radian → Angle conversion Square root of single precision actual number Index operation of single precision actual number Exponentiation operation of single precision actual number |   |                       |
|         | DSIN(P)<br>COS(P)<br>DCOS(P)<br>TAN(P)<br>DAAN(P)<br>ASIN(P)<br>DASIN(P)<br>ACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DACOS(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DEG(P)<br>DEG(P)<br>DEG(P)<br>DESQRT(P)<br>ESQRT(P)<br>ESQRT(P)<br>ESQRT(P)<br>ESQRT(P)<br>DCOG(P)<br>DLOGE(P)<br>POW(P)  | Single precision actual number TAN operation Single precision actual number SIN <sup>-1</sup> operation Single precision actual number COS <sup>-1</sup> Operation Single precision actual number COS <sup>-1</sup> Operation Single precision actual number angle → Radian conversion Single precision actual number radian → Angle conversion Square root of single precision actual number Index operation of single precision actual number |   |                       |
|         | DSIN(P)<br>COS(P)<br>DCOS(P)<br>TAN(P)<br>DTAN(P)<br>DATAN(P)<br>ACOS(P)<br>ACOS(P)<br>ACOS(P)<br>DACOS(P)<br>ATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P)<br>DATAN(P   | Single precision actual number TAN operation Single precision actual number SIN <sup>-1</sup> operation Single precision actual number COS <sup>-1</sup> Operation Single precision actual number angle → Radian conversion Single precision actual number radian → Angle conversion Square root of single precision actual number Index operation of single precision actual number Exponentiation operation of single precision actual number |   |                       |

For sequence instructions and basic instructions, refer to manuals.

| 01 10 11            | Instruction  |   |      |                 |
|---------------------|--------------|---|------|-----------------|
| Classification      | symbol       |   | FX5U | nodule<br>FX5UC |
| Random              | RND(P)       | Random number generation  | 0    | 0               |
| number              | ZPUSH(P)     | Collective saving of index register   | 0    | 0               |
|                     | ZPOP(P)      | Corrective return of index register   | 0    | 0               |
| Index register      |              | Selection and saving of index register/long index                                     | _    |                 |
| operation           | ZPUSH(P)     | register  | 0    | 0               |
|                     | ZPOP(P)      | Selection and return of index register/long index                                     | 0    | 0               |
|                     |              | register  | 0    | 0               |
|                     | LIMIT(P)(_U) | BIN 16-bit data upper-/lower-limit control BIN 32-bit data upper-/lower-limit control | 0    | 0               |
|                     | BAND(P)(_U)  | BIN 32-bit data dead band control   | 0    | 0               |
|                     | DBAND(P)(U)  | BIN 32-bit data dead band control   | 0    | 0               |
|                     | ZONE(P)(_U)  |   | 0    | 0               |
| Data control        | DZONE(P)( U) | BIN 32-bit data zone control  | 0    | 0               |
|                     | SCL(P)(_U)   | BIN 16-bit unit scaling (point-specific coordinate data)                              | 0    | 0               |
|                     | DSCL(P)(_U)  | BIN 32-bit unit scaling (point-specific coordinate data)                              | 0    | 0               |
|                     | SCL2(P)(_U)  | BIN 16-bit unit scaling (X-/Y-specific coordinate data)                               | 0    | 0               |
|                     | DSCL2(P)(_U) | BIN 32-bit unit scaling (X-/Y-specific coordinate data)                               | 0    | 0               |
|                     | TTMR         | Teaching timer  | 0    | 0               |
| Special timer       | STMR         | Special function timer  | 0    | 0               |
| Special counter     | UDCNTF       | Signed 32-bit up/down counter   | 0    | 0               |
| Shortcut<br>control | ROTC         | Rotary table shortcut control   | 0    | 0               |
| Inclination signal  | RAMPF        | Control inclination signal  | 0    | 0               |
|                     | SPD          | Measurement of BIN 16-bit pulse density   | 0    | 0               |
|                     | DSPD         | Measurement of BIN 32-bit pulse density   | 0    | 0               |
|                     | PLSY         | BIN 16-bit pulse output   | 0    | 0               |
| Pulse system        | DPLSY        | BIN 32-bit pulse output   | 0    | 0               |
|                     | PWM          | BIN 16 pulse width modulation   | 0    | 0               |
|                     | DPWM         | BIN 32-bit pulse width modulation   | 0    | 0               |
| Matrix input        | MTR          | Matrix input  | 0    | 0               |
| Initial state       | IST          | Initial state   | 0    | 0               |
| During              | ABSD         | BIN 16-bit data absolute method   | 0    | 0               |
| Drum<br>sequence    | DABSD        | BIN 32-bit data absolute method   | 0    | 0               |
| Coquerioo           | INCD         | Relative method   | 0    | 0               |
| Check code          | CCD(P)       | Check code  | 0    | 0               |
|                     | SERMM(P)     | Data processing instruction   | 0    | 0               |
|                     | DSERMM(P)    | 32-bit data search  | 0    | 0               |
|                     | SUM(P)       | 16-bit data bit check   | 0    | 0               |
|                     | DSUM(P)      | 32-bit data bit check   | 0    | 0               |
|                     | BON(P)       | Bit detection of 16-bit data  | 0    | 0               |
|                     | DBON(P)      | Bit detection of 32-bit data  | 0    | 0               |
|                     | MAX(P)(_U)   | Search for maximum value of 16-bit data   | 0    | 0               |
|                     | DMAX(P)(_U)  | Search for maximum value of 32-bit data   | 0    | 0               |
| Data                | MIN(P)(_U)   | Search for minimum value of 16-bit data   | 0    | 0               |
| processing          | DMIN(P)(_U)  | Search for minimum value of 32-bit data<br>16-bit data sort                           | 0    | 0               |
| instruction         | SORTTBL(_U)  |   | 0    | 0               |
|                     | SORTTBL2(_U) |   | -    | -               |
|                     | -            | 32-bit data alignment 2<br>16-bit data total value calculation                        | 0    | 0               |
|                     |              | 32-bit data total value calculation   | 0    | 0               |
|                     | MEAN(P)(_U)  | 16-bit data average value calculation   | 0    | 0               |
|                     |              | 32-bit data average value calculation   | 0    | 0               |
|                     | SQRT(P)      | Calculation of 16-bit square root   | 0    | 0               |
|                     | DSQRT(P)     | Calculation of 32-bit square root   | 0    | 0               |
|                     | CRC(P)       | CRC calculation   | 0    | 0               |
| Indirect            |              |   |      |                 |
| address read        | ADRSET(P)    | Indirect address read   | 0    | 0               |

| Symbol         PURCION         DOPUMOU           TROP         Clock data read         O         A           TWRIP         Clock data read         O         A           TSUB(P)         Subtraction of clock data         O         A           TSUB(P)         Subtraction of clock data         O         A           HTOSP)         Houriminute/second)         O         A           DHTOSP(P)         Subtraction of clock data         O         A           DHTOSP(P)         Subtraction of clock data         O         A           DHTOSP(P)         Tob-tridata conversion of time data         O         A           BSTOH(P)         16-bit data conversion of time data         O         A           (EDDTS->         Date comparison LDDT (S1) - (S2)         O         A           LDDTS->         Date comparison LDDT (S1) - (S2)         O         A           LDDTS->         Date comparison ANDDT (S1) - (S2)         O         A           ANDDTS->         Date comparison ANDDT (S1) - (S2)         O         A           ANDDTS->         Date comparison ANDT (S1) - (S2)         O         A           ANDDTS->         Date comparison ANDT (S1) - (S2)         O         A           ANDDTS-> <th></th> <th>In the other</th> <th></th> <th>Com</th> <th>oatible</th>  |           | In the other          |   | Com   | oatible |
|---|-----------|-----------------------|---|-------|---------|
| For Jock         Clock data read         0         1           TRDIP         Clock data write         0         1           TADDP         Addition of clock data         0         1           TSUBP         Subtraction of clock data         0         1           HTOSP         16-bit data conversion of time data<br>(hour/minute/second - second)         0         1           BTOHP         16-bit data conversion of time data<br>(second - hour/minute/second)         0         1           DSTOHP         16-bit data conversion of time data<br>(second - hour/minute/second)         0         1           DDTS>         Date comparison LDDT (S1) = (S2)         0         1           LDDTS>         Date comparison LDDT (S1) = (S2)         0         1           LDDTS>         Date comparison LDT (S1) = (S2)         0         1           LDDTS>         Date comparison ANDDT (S1) = (S2)         0         1           LDDTS>         Date comparison ANDDT (S1) = (S2)         0         1           ANDDTS>         Date comparison ANDDT (S1) = (S2)         0         1           ANDDTS>         Date comparison ANDDT (S1) = (S2)         0         1           ANDDTS>         Date comparison ANDDT (S1) = (S2)         0         1           ORDTS>   |           | Instruction<br>symbol |   | CPU r | nodule  |
| For dock         Clock data write         O         Image: Clock data           TADD(P)         Addition of clock data         O         Image: Clock data           HTOS(P)         Clock data conversion of time data<br>(hour/minute/second)         O         Image: Clock data           DHTOS(P)         Clock data conversion of time data<br>(bour/minute/second)         O         Image: Clock data           STOH(P)         Second - hour/minute/second)         O         Image: Clock data           DSTOH(P)         Second - hour/minute/second)         O         Image: Clock data           DDTS>         Date comparison LDDT (S1) - (S2)         O         Image: Clock data           LDDTS>         Date comparison LDDT (S1) - (S2)         O         Image: Clock data           LDDTS>         Date comparison LDDT (S1) - (S2)         O         Image: Clock data           ANDDTS>         Date comparison ANDDT (S1) - (S2)         O         Image: Clock data           ANDDTS>         Date comparison ANDDT (S1) - (S2)         O         Image: Clock data           ANDDTS>         Date comparison ANDDT (S1) - (S2)         O         Image: Clock data           ANDDTS>         Date comparison ANDT (S1) - (S2)         O         Image: Clock data           ANDDTS>         Date comparison ANDT (S1) - (S2)         O   |           |                       |   |       | FX5UC   |
| For dock         Addition of clock data         O         I           TSUB(P)         Subtraction of clock data         O         I           HTOS(P)         16-bit data conversion of time data<br>(hour/minute/second)         O         I           BHTOS(P)         16-bit data conversion of time data<br>(second = hour/minute/second)         O         I           STOH(P)         16-bit data conversion of time data<br>(second = hour/minute/second)         O         I           DBTOH(P)         2bett data conversion of time data<br>(second = hour/minute/second)         O         I           LDDTS=         Date comparison LDDT (S1) = (S2)         O         I           LDDTS=         Date comparison LDDT (S1) = (S2)         O         I           LDDTS=         Date comparison LDDT (S1) = (S2)         O         I           LDDTS=         Date comparison LDDT (S1) = (S2)         O         I           ANDDTS=         Date comparison ANDDT (S1) = (S2)         O         I           ANDDTS=         Date comparison ANDDT (S1) = (S2)         O         I           ANDDTS=         Date comparison ANDDT (S1) = (S2)         O         I           ANDDTS=         Date comparison ANDT (S1) = (S2)         O         I           ANDDTS=         Date comparison CPDT (S1) = (S2)   |           |                       |   |       | 0       |
| For dock         Subtraction of clock data         O         Introstip           HTOS(P)         Ro-thit data conversion of time data         O         Introstip           DHTOS(P)         Ro-thit data conversion of time data         O         Introstip           STOH(P)         Re-thit data conversion of time data         O         Introstip           DBTOH(P)         Re-thit data conversion of time data         O         Introstip           DDTS>         Date comparison LDDT (S1) = (S2)         O         Introstip           LDDTS>         Date comparison LDDT (S1) = (S2)         O         Introstip           LDDTS>         Date comparison LDDT (S1) = (S2)         O         Introstip           LDDTS>         Date comparison LDDT (S1) = (S2)         O         Introstip           LDDTS>         Date comparison ANDDT (S1) = (S2)         O         Introstip           ANDDTS>         Date comparison ANDDT (S1) = (S2)         O         Introstip           ANDDTS>         Date comparison ANDDT (S1) = (S2)         O         Introstip           ANDDTS>         Date comparison ANDT (S1) = (S2)         O         Introstip           ANDTS>         Date comparison CPDT (S1) = (S2)         O         Introstip           ANDTS>         Date comparison CPDT (S1) = (S2  |           |                       |   |       | 0       |
| For along         16-bit data conversion of time data<br>(hour/minute/second) = second)         0         1           BHTOS(P)         2-bit data conversion of time data<br>(second = hour/minute/second)         0         1           BSTOH(P)         32-bit data conversion of time data<br>(second = hour/minute/second)         0         1           DSTOH(P)         32-bit data conversion of time data<br>(second = hour/minute/second)         0         1           LDDTS=         Date comparison LDDT (S1) - (S2)         0         1           LDDTS>         Date comparison LDDT (S1) - (S2)         0         1           LDDTS>         Date comparison LDDT (S1) - (S2)         0         1           LDDTS>         Date comparison LDDT (S1) - (S2)         0         1           LDDTS>         Date comparison ANDDT (S1) - (S2)         0         1           ANDDTS>         Date comparison ANDDT (S1) - (S2)         0         1           ANDDTS>         Date comparison ANDDT (S1) - (S2)         0         1           ANDDTS>         Date comparison ANDDT (S1) - (S2)         0         1           ORDTS>         Date comparison CPDT (S1) > (S2)         0         1           ORDTS>         Date comparison CPDT (S1) > (S2)         0         1           ORDTS>         Date comparison C  |           | <u> </u>              |   |       | 0       |
| HICS(P)         (hour/minute/second - second)         O         0           DHTOS(P)         (32-bit data conversion of time data<br>(second - hour/minute/second)         O         0           DSTOH(P)         12-bit data conversion of time data<br>(second - hour/minute/second)         O         0           DDTS=         Data comparison LDDT (S1) - (S2)         O         0           LDDTS=         Data comparison LDDT (S1) - (S2)         O         0           LDDTS=         Data comparison LDDT (S1) - (S2)         O         0           LDDTS=         Data comparison LDDT (S1) - (S2)         O         0           LDDTS=         Data comparison LDDT (S1) - (S2)         O         0           ANDDTS=         Data comparison ANDDT (S1) - (S2)         O         0           ANDDTS=         Data comparison ANDDT (S1) - (S2)         O         0           ANDDTS=         Data comparison ANDDT (S1) - (S2)         O         0           ANDDTS=         Data comparison ANDT (S1) - (S2)         O         0           ANDDTS=         Data comparison ANDT (S1) - (S2)         O         0           ORDTS=         Data comparison ORDT (S1) - (S2)         O         0           ORDTS=         Data comparison ORDT (S1) - (S2)         O         0   |           | TSUB(P)               |   | 0     | 0       |
| For LOS(*)         (hour/minute/second)         O         A           STOH(P)         16-bit data conversion of time data<br>(second - hour/minute/second)         O         A           DSTOH(P)         32-bit data conversion of time data<br>(second - hour/minute/second)         O         A           LDDTS-         Data comparison LDDT (S1) - (S2)         O         A           LDDTS-         Data comparison LDDT (S1) - (S2)         O         A           LDDTS-         Data comparison LDDT (S1) - (S2)         O         A           LDDTS-         Data comparison LDDT (S1) - (S2)         O         A           LDDTS-         Data comparison LDDT (S1) - (S2)         O         A           ANDDTS-         Data comparison ANDDT (S1) - (S2)         O         A           ANDDTS-         Data comparison ANDDT (S1) - (S2)         O         A           ANDDTS-         Data comparison ANDDT (S1) - (S2)         O         A           ANDDTS-         Data comparison ANDT (S1) - (S2)         O         A           ANDDTS-         Data comparison ANDT (S1) - (S2)         O         A           ANDDTS-         Data comparison ANDT (S1) - (S2)         O         A           ORDTS-         Data comparison ANDT (S1) - (S2)         O         A   |           | HTOS(P)               |   | 0     | 0       |
| SICH(P)         (second - hour/minute/second)         O         A           DSTOH(P)         32-bit data conversion of time data         O         A           LDDTS=         Date comparison LDDT (S1) = (S2)         O         A           LDDTS=         Date comparison LDDT (S1) > (S2)         O         A           LDDTS=         Date comparison LDDT (S1) > (S2)         O         A           LDDTS=         Date comparison LDDT (S1) > (S2)         O         A           LDDTS=         Date comparison LDDT (S1) > (S2)         O         A           ADDDTS=         Date comparison ANDDT (S1) = (S2)         O         A           ANDDTS=         Date comparison ANDDT (S1) = (S2)         O         A           ANDDTS=         Date comparison ANDDT (S1) = (S2)         O         A           ANDDTS=         Date comparison ANDDT (S1) = (S2)         O         A           ANDDTS=         Date comparison ANDT (S1) = (S2)         O         A           ANDDTS=         Date comparison ANDT (S1) = (S2)         O         A           ANDDTS=         Date comparison ANDT (S1) = (S2)         O         A           ORDTS=         Date comparison ANDT (S1) = (S2)         O         A           ORDTS=         Date comparison CRT  |           | DHTOS(P)              |   | 0     | 0       |
| IDSIGN(P)         (second - hour/minute/second)         O         A           IDDTS-         Date comparison IDDT (S1) - (S2)         O         A           IDDTS-         Date comparison IDDT (S1) - (S2)         O         A           IDDTS-         Date comparison IDDT (S1) - (S2)         O         A           IDDTS-         Date comparison IDDT (S1) - (S2)         O         A           IDDTS-         Date comparison ANDDT (S1) - (S2)         O         A           ANDDTS-         Date comparison ANDDT (S1) - (S2)         O         A           ANDDTS-         Date comparison ANDDT (S1) - (S2)         O         A           ANDDTS-         Date comparison ANDDT (S1) - (S2)         O         A           ANDDTS-         Date comparison ANDT (S1) - (S2)         O         A           ANDDTS-         Date comparison ANDT (S1) - (S2)         O         A           ANDTS-         Date comparison ORDT (S1) - (S2)         O         A           ANDTS-         Date comparison ORDT (S1) - (S2)         O         A           ORDTS-         Date comparison ORDT (S1) - (S2)         O         A           ORDTS-         Date comparison ORDT (S1) - (S2)         O         A           ORDTS-         Date comparison ORDT (S1)   |           | STOH(P)               |   | 0     | 0       |
| IDDT\$         Date comparison LDDT (S1) < (S2)   |           | DSTOH(P)              |   | 0     | 0       |
| LDDT\$>         Date comparison LDDT (\$1) > (\$2)         0         0           LDDT\$         Date comparison LDDT (\$1) < (\$2)  |           | LDDT\$=               | Date comparison LDDT (S1) = (S2)                          | 0     | 0       |
| IDDT%         Date comparison LDDT (S1) <   |           | LDDT\$<>              | Date comparison LDDT (S1) <> (S2)                         | 0     | 0       |
| LDDT\$         Date comparison LDDT (S1) < (S2)   |           | LDDT\$>               | Date comparison LDDT (S1) > (S2)                          | 0     | 0       |
| IDDT\$>=         Date comparison LDDT (S1) >= (S2)         0         0           ANDDT\$=         Date comparison ANDDT (S1) < (S2)   |           | LDDT\$<=              | Date comparison LDDT (S1) <= (S2)                         | 0     | 0       |
| ANDDTS=         Date comparison ANDDT (S1) = (S2)         0         0           ANDDTS>         Date comparison ANDDT (S1) <> (S2)         0         0           ANDDTS>         Date comparison ANDDT (S1) <> (S2)         0         0           ANDDTS>         Date comparison ANDDT (S1) << (S2)  |           | LDDT\$<               | Date comparison LDDT (S1) < (S2)                          | 0     | 0       |
| ANDDTS=         Date comparison ANDDT (S1) = (S2)         0         0           ANDDTS>         Date comparison ANDDT (S1) > (S2)         0         0           ANDDTS>         Date comparison ANDDT (S1) > (S2)         0         0           ANDDTS>         Date comparison ANDDT (S1) > (S2)         0         0           ANDDTS>         Date comparison ANDDT (S1) > (S2)         0         0           ANDDTS>         Date comparison ORDT (S1) > (S2)         0         0           ORDTS>         Date comparison ORDT (S1) > (S2)         0         0           ORDTS>         Date comparison ORDT (S1) > (S2)         0         0           ORDTS>         Date comparison ORDT (S1) > (S2)         0         0           ORDTS>         Date comparison ORDT (S1) > (S2)         0         0           ORDTS>         Date comparison DTM (S1) > (S2)         0         0           DTMS>         Time comparison LDTM (S1) > (S2)         0         0           DTMS>         Time comparison LDTM (S1) > (S2)         0         0           DTMS>         Time comparison ANDTM (S1) > (S2)         0         0           DTMS>         Time comparison ANDTM (S1) > (S2)         0         0           ANDTMS>         Time comparison ANDTM (S1   |           | LDDT\$>=              | Date comparison LDDT (S1) >= (S2)                         | 0     | 0       |
| ANDDT\$>         Date comparison ANDDT (S1) > (S2)         0         0           ANDDT\$         Date comparison ANDDT (S1) <= (S2)   |           | ANDDT\$=              |   | 0     | 0       |
| ANDDT\$         Date comparison ANDDT (S1) <= (S2)  |           | ANDDT\$<>             | Date comparison ANDDT (S1) <> (S2)                        | 0     | 0       |
| ANDDT\$         Date comparison ANDDT (S1) <= (S2)  |           | ANDDT\$>              | Date comparison ANDDT (S1) > (S2)                         | 0     | 0       |
| ANDDT\$         Date comparison ANDDT (S1) < (S2)   |           | ANDDT\$<=             |   | 0     | 0       |
| ANDDT\$>=         Date comparison ANDDT (S1) >= (S2)         0         0           ORDT\$=         Date comparison ORDT (S1) = (S2)         0         0           ORDT\$>         Date comparison ORDT (S1) >> (S2)         0         0           ORDT\$>         Date comparison ORDT (S1) >> (S2)         0         0           ORDT\$>         Date comparison ORDT (S1) >> (S2)         0         0           ORDT\$>         Date comparison ORDT (S1) >> (S2)         0         0           ORDT\$>         Date comparison ORDT (S1) >> (S2)         0         0           ORDT\$>         Date comparison DDTM (S1) >> (S2)         0         0           DDTM\$>         Time comparison LDTM (S1) >> (S2)         0         0           DDTM\$>         Time comparison LDTM (S1) >> (S2)         0         0           DDTM\$>         Time comparison LDTM (S1) >> (S2)         0         0           ANDTM\$>         Time comparison ANDTM (S1) >> (S2)         0         0           ANDTM\$>         Time comparison ANDTM (S1) >> (S2)         0         0           ANDTM\$>         Time comparison ANDTM (S1) >< (S2)   |           |                       |   |       | 0       |
| ORDTS         Date comparison ORDT (S1) = (S2)         O         O           For clock         ORDTS         Date comparison ORDT (S1) <> (S2)         O         O           ORDTS         Date comparison ORDT (S1) <> (S2)         O         O         O           ORDTS         Date comparison ORDT (S1) <> (S2)         O         O         O           ORDTS         Date comparison ORDT (S1) <= (S2)  |           |                       |   |       | 0       |
| For clock         ORD15         Date comparison ORD1 [S1] <> [S2]         O         O           ORD15>         Date comparison ORD1 [S1] <> [S2]         O         O           ORD15>         Date comparison ORD1 [S1] << [S2]   |           |                       |   |       | 0       |
| Initial         Date comparison ORDT (S1) > (S2)         O         O           ORDTS>         Date comparison ORDT (S1) > (S2)         O         O           ORDTS>         Date comparison ORDT (S1) > (S2)         O         O           ORDTS>         Date comparison ORDT (S1) > (S2)         O         O           ORDTS>         Date comparison ORDT (S1) > (S2)         O         O           LDTMS>         Time comparison LDTM (S1) > (S2)         O         O           LDTMS>         Time comparison LDTM (S1) > (S2)         O         O           LDTMS>         Time comparison LDTM (S1) > (S2)         O         O           LDTMS>         Time comparison ADTM (S1) > (S2)         O         O           LDTMS>         Time comparison ADTM (S1) > (S2)         O         O           ANDTMS>         Time comparison ANDTM (S1) > (S2)         O         O           ANDTMS>         Time comparison ANDTM (S1) > (S2)         O         O           ANDTMS>         Time comparison ANDTM (S1) > (S2)         O         O           ANDTMS>         Time comparison ORTM (S1) > (S2)         O         O           ANDTMS>         Time comparison ORTM (S1) > (S2)         O         O           ORTMS>         Time comparison ORTM (S1)   | For clock |                       |   |       | 0       |
| $ \begin{array}{c c c c c c c c c c c c c c c c c c c $   | TOT GIOGR |                       |   |       | 0       |
| ORDT\$         Date comparison ORDT (\$1) < (\$2)         O         O           ORDT\$>=         Date comparison ORDT (\$1) >= (\$2)         O         O           LDTM\$=         Time comparison LDTM (\$1) = (\$2)         O         O           LDTM\$>         Time comparison LDTM (\$1) >> (\$2)         O         O           LDTM\$>         Time comparison LDTM (\$1) >> (\$2)         O         O           LDTM\$>         Time comparison LDTM (\$1) >> (\$2)         O         O           LDTM\$>         Time comparison LDTM (\$1) >= (\$2)         O         O           LDTM\$>         Time comparison LDTM (\$1) >= (\$2)         O         O           ANDTM\$         Time comparison ANDTM (\$1) >= (\$2)         O         O           ANDTM\$         Time comparison ANDTM (\$1) >= (\$2)         O         O           ANDTM\$         Time comparison ANDTM (\$1) >= (\$2)         O         O           ANDTM\$         Time comparison ANDTM (\$1) >= (\$2)         O         O           ANDTM\$         Time comparison ORTM (\$1) >= (\$2)         O         O           ORTM\$         Time comparison ORTM (\$1) >= (\$2)         O         O           ORTM\$         Time comparison ORTM (\$1) >= (\$2)         O         O           ORTM\$         Time comparison ORTM   |           | -                     |   |       |         |
| $ \begin{array}{c c c c c c c c c c c c c c c c c c c $   |           |                       |   |       | 0       |
| LDTM\$=         Time comparison LDTM (S1) = (S2)         0         0           LDTM\$         Time comparison LDTM (S1) > (S2)         0         0           LDTM\$         Time comparison LDTM (S1) > (S2)         0         0           LDTM\$         Time comparison LDTM (S1) > (S2)         0         0           LDTM\$         Time comparison LDTM (S1) > (S2)         0         0           LDTM\$         Time comparison LDTM (S1) > (S2)         0         0           LDTM\$         Time comparison ADTM (S1) > (S2)         0         0           ANDTM\$         Time comparison ANDTM (S1) > (S2)         0         0           ANDTM\$         Time comparison ANDTM (S1) > (S2)         0         0           ANDTM\$         Time comparison ANDTM (S1) > (S2)         0         0           ANDTM\$         Time comparison ANDTM (S1) > (S2)         0         0           ANDTM\$         Time comparison ORTM (S1) > (S2)         0         0           ORTM\$         Time comparison ORTM (S1) > (S2)         0         0           ORTM\$         Time comparison ORTM (S1) > (S2)         0         0           ORTM\$         Time comparison ORTM (S1) > (S2)         0         0           ORTM\$         Time comparison ORTM (S1) > (S2)   |           | -                     |   |       | 0       |
| LDTM\$<>         Time comparison LDTM (S1) <> (S2)         0         0           LDTM\$>         Time comparison LDTM (S1) > (S2)         0         0           LDTM\$         Time comparison LDTM (S1) > (S2)         0         0           LDTM\$         Time comparison LDTM (S1) > (S2)         0         0           LDTM\$         Time comparison LDTM (S1) > (S2)         0         0           LDTM\$         Time comparison ANDTM (S1) > (S2)         0         0           ANDTM\$         Time comparison ANDTM (S1) > (S2)         0         0           ANDTM\$         Time comparison ANDTM (S1) > (S2)         0         0           ANDTM\$         Time comparison ANDTM (S1) > (S2)         0         0           ANDTM\$         Time comparison ANDTM (S1) > (S2)         0         0           ANDTM\$         Time comparison ANDTM (S1) > (S2)         0         0           ORTM\$         Time comparison ORTM (S1) > (S2)         0         0           ORTM\$         Time comparison ORTM (S1) > (S2)         0         0           ORTM\$         Time comparison ORTM (S1) > (S2)         0         0           ORTM\$         Time comparison ORTM (S1) > (S2)         0         0           ORTM\$         Time comparison ORTM (S1) > (S2) <td></td> <td></td> <td></td> <td></td> <td>0</td>   |           |                       |   |       | 0       |
| LDTM\$>         Time comparison LDTM (\$1) > (\$2)         0         0           LDTM\$         Time comparison LDTM (\$1) > (\$2)         0         0           LDTM\$         Time comparison LDTM (\$1) >= (\$2)         0         0           LDTM\$         Time comparison LDTM (\$1) >= (\$2)         0         0           ANDTM\$         Time comparison ANDTM (\$1) >= (\$2)         0         0           ANDTM\$         Time comparison ANDTM (\$1) >= (\$2)         0         0           ANDTM\$         Time comparison ANDTM (\$1) >> (\$2)         0         0           ANDTM\$         Time comparison ANDTM (\$1) >> (\$2)         0         0           ANDTM\$         Time comparison ANDTM (\$1) >> (\$2)         0         0           ANDTM\$         Time comparison ANDTM (\$1) >> (\$2)         0         0           ANDTM\$         Time comparison ANDTM (\$1) >> (\$2)         0         0           ANDTM\$         Time comparison ORTM (\$1) >> (\$2)         0         0           ORTM\$         Time comparison ORTM (\$1) >> (\$2)         0         0           ORTM\$         Time comparison ORTM (\$1) >> (\$2)         0         0           ORTM\$         Time comparison ORTM (\$1) >> (\$2)         0         0           ORTM\$         Time comparison ORTM  |           |                       | Time comparison LDTM (S1) = (S2)                          | 0     | 0       |
| $ \begin{array}{c c c c c c c c c c c c c c c c c c c $   |           | LDTM\$<>              | Time comparison LDTM (S1) <> (S2)                         | 0     | 0       |
| ILDTM\$         Time comparison LDTM (\$1) < (\$2)  |           | LDTM\$>               | Time comparison LDTM (S1) > (S2)                          | 0     | 0       |
| $ \begin{array}{c c c c c c c c c c c c c c c c c c c $   |           | LDTM\$<=              | Time comparison LDTM (S1) <= (S2)                         | 0     | 0       |
| ANDTMS=         Time comparison ANDTM (S1) = (S2)         0           ANDTMS         Time comparison ANDTM (S1) $<$ (S2)         0           ANDTMS         Time comparison ANDTM (S1) $>$ (S2)         0           ORTMS         Time comparison ORTM (S1) $>$ (S2)         0           TCMP(P)         Clock data bandwidth comparison         0     <   |           | LDTM\$<               | Time comparison LDTM (S1) < (S2)                          | 0     | 0       |
| ANDTM\$<>         Time comparison ANDTM (S1) $\diamond$ (S2)         0         0           ANDTM\$         Time comparison ANDTM (S1) $\diamond$ (S2)         0         0           ANDTM\$         Time comparison ANDTM (S1) $\diamond$ (S2)         0         0           ANDTM\$         Time comparison ANDTM (S1) $\diamond$ (S2)         0         0           ANDTM\$         Time comparison ANDTM (S1) $\diamond$ (S2)         0         0           ANDTM\$         Time comparison ANDTM (S1) $\diamond$ (S2)         0         0           ORTM\$         Time comparison ORTM (S1) $\diamond$ (S2)         0         0           ORTM\$         Time comparison ORTM (S1) $\diamond$ (S2)         0         0           ORTM\$         Time comparison ORTM (S1) $\diamond$ (S2)         0         0           ORTM\$         Time comparison ORTM (S1) $\diamond$ (S2)         0         0           ORTM\$         Time comparison ORTM (S1) $\diamond$ (S2)         0         0           ORTM\$         Time comparison ORTM (S1) $\diamond$ (S2)         0         0           ORTM\$         Time comparison ORTM (S1) $\diamond$ (S2)         0         0           ORTM\$         Time comparison ORTM (S1) $\diamond$ (S2)         0         0           ORTM\$         Time comparison ORTM (S1) $\diamond$ (S2)         0         0           DUTY         Timini  |           | LDTM\$>=              | Time comparison LDTM (S1) >= (S2)                         | 0     | 0       |
| ANDTM\$>         Time comparison ANDTM (S1) > (S2)         0         0           ANDTM\$<   |           | ANDTM\$=              | Time comparison ANDTM (S1) = (S2)                         | 0     | 0       |
| ANDTM\$<=   |           | ANDTM\$<>             | Time comparison ANDTM (S1) <> (S2)                        | 0     | 0       |
| ANDTM\$         Time comparison ANDTM (S1) < (S2)   |           | ANDTM\$>              | Time comparison ANDTM (S1) > (S2)                         | 0     | 0       |
| ANDTM\$         Time comparison ANDTM (S1) < (S2)   |           | ANDTM\$<=             |   | 0     | 0       |
| $ \begin{array}{c c c c c c c c c c c c c c c c c c c $   |           |                       |   |       | 0       |
| ORTMS=         Time comparison ORTM (51) = (52)         0         0           ORTMS         Time comparison ORTM (51) < (52)  |           |                       |   |       | 0       |
| $ \begin{array}{c c c c c c } \hline \text{ORTMS} & \mbox{Time comparison ORTM (S1) } <> (S2) & \mbox{ORTMS} & \mbox{Time comparison ORTM (S1) } <(S2) & \mbox{ORTMS} & \mbox{Time comparison ORTM (S1) } <(S2) & \mbox{ORTMS} & \mbox{Time comparison ORTM (S1) } <(S2) & \mbox{ORTMS} & \mbox{Time comparison ORTM (S1) } <(S2) & \mbox{ORTMS} & \mbox{Time comparison ORTM (S1) } <(S2) & \mbox{ORTMS} & \mbox{Time comparison ORTM (S1) } <(S2) & \mbox{ORTMS} & \mbox{Time comparison ORTM (S1) } <(S2) & \mbox{ORTMS} & \mbox{Time comparison ORTM (S1) } <(S2) & \mbox{ORTMS} & \mbox{ORTMS} & \mbox{Time comparison ORTM (S1) } <(S2) & \mbox{ORTMS} & \mbox{ORTM} & \mbox{ORTMS} & \mb$ |           |                       |   |       | 0       |
| ORTM\$>         Time comparison ORTM (\$1) > (\$2)         O         O           ORTM\$<  |           |                       |   | -     | 0       |
| ORTM\$<=  |           |                       |   |       | 0       |
| ORTM\$         Time comparison ORTM (\$1) < (\$2)   |           |                       |   |       | 0       |
| $\begin{tabular}{ c c c c c } \hline ORTM$>= Time comparison ORTM (S1)>=(S2) & O & O & O & O & O & O & O & O & O & $  |           |                       |   |       |         |
| TCMP(P)         Clock data comparison         O         I           TZCP(P)         Clock data bandwidth comparison         O         I           Timing<br>measurement         DUTY         Timing pulse generation         O         I           HOURM         Hour meter (BIN 16-bit data)         O         O         I           DHOURM         Hour meter (BIN 32-bit data)         O         O         I           REF(P)         VO refresh         O <td< td=""><td></td><td></td><td></td><td></td><td>0</td></td<>   |           |                       |   |       | 0       |
| TZCP(P)         Clock data bandwidth comparison         O         Iming           Timing         DUTY         Timing pulse generation         O         O           HOURM         Hour meter (BIN 16-bit data)         O         O         O           DHOURM         Hour meter (BIN 32-bit data)         O         O         O         O           REF(P)         VO refresh         O  |           |                       |   |       | 0       |
| DUTY         Timing pulse generation         O         O           HOURM         Hour meter (BIN 16-bit data)         O         O           DHOURM         Hour meter (BIN 16-bit data)         O         O           DHOURM         Hour meter (BIN 32-bit data)         O         O           REF(P)         IVO refresh         O         O           FROM(P)         Read of 1-word data from other module (16-bit specified)         O         O           DFROM(P)         Read of 2-word data from other module (16-bit specified)         O         O           DTO(P)         Write of 1-word data from other module (16-bit specified)         O         O           DTO(P)         Write of 1-word data from other module (16-bit specified)         O         O           DTO(P)         Write of 1-word data from other module (16-bit specified)         O         O           DTO(P)         Read of 1-word data from other module (32-bit specified)         O         O           DTO(P)         Read of 1-word data from other module (32-bit specified)         O         O  |           |                       |   |       | 0       |
| Timing<br>measurement         HOURM         Hour meter (BIN 16-bit data)         O         I           DHOURM         Hour meter (BIN 32-bit data)         O <td></td> <td></td> <td></td> <td></td> <td>0</td>   |           |                       |   |       | 0       |
| Inductivity         Hour meter (BIN 16-bit data)         O         O           DHOURIM         Hour meter (BIN 32-bit data)         O         O         O           REF(P)         VO refresh         O         O         O         O         O           RFS(P)         VO refresh         O   | Timina    |                       |   |       | 0       |
| DHOURM         Hour meter (BIN 32-bit data)         O         O           REF(P)         VO refresh         O   |           |                       | · · · · · · · · · · · · · · · · · · ·                     |       | 0       |
| RFS(P)         I/U retrest           FROM(P)         Read of 1-word data from other module (16-bit specified)         0           DFROM(P)         Read of 2-word data from other module (16-bit specified)         0           Module         TO(P)         Write of 1-word data from other module (16-bit specified)         0           TO(P)         Write of 2-word data from other module (16-bit specified)         0         0           FROMD(P)         Read of 2-word data from other module (16-bit specified)         0         0           DTO(P)         Write of 2-word data from other module (16-bit specified)         0         0           DFROMD(P)         Read of 1-word data from other module (32-bit specified)         0         0  |           |                       | Hour meter (BIN 32-bit data)                              |       | 0       |
| IRFS(P)         O         O         O           FROM(P)         Read of 1-word data from other module (16-bit specified)         O         O           DFROM(P)         Read of 2-word data from other module (16-bit specified)         O         O           Module         TO(P)         Write of 1-word data from other module (16-bit specified)         O         O           DTO(P)         Write of 2-word data from other module (16-bit specified)         O         O         O           FROMD(P)         Read of 1-word data from other module (32-bit specified)         O         O         O           DTO(P)         Read of 1-word data from other module (32-bit specified)         O         O         O  |           | REF(P)                | I/O refresh   | 0     | 0       |
| DFROM(P)         Read of 2-word data from other module (16-bit specified)         O           Module         TO(P)         Write of 1-word data from other module (16-bit specified)         O           DTO(P)         Write of 2-word data from other module (16-bit specified)         O         O           PROMD(P)         Read of 1-word data from other module (16-bit specified)         O         O           DFROMD(P)         Read of 1-word data from other module (32-bit specified)         O         O  |           | RFS(P)                | i o rondati   | 0     | 0       |
| Module<br>access         TO(P)         Write of 1-word data from other module (16-bit specified)         O           DTO(P)         Write of 2-word data from other module (16-bit specified)         O         O           FROMD(P)         Read of 1-word data from other module (32-bit specified)         O         O           DFROMD(P)         Read of 2-word data from other module (32-bit specified)         O         O  |           |                       |   | 0     | 0       |
| access DTO(P) Write of 2-word data from other module (16-bit specified) O FROMD(P) Read of 1-word data from other module (32-bit specified) O FROMD(P) Read of 2-word data from other module (32-bit specified) O FROMD(P) Read of 2-word data from other module (32-bit specified) O   |           | DFROM(P)              | Read of 2-word data from other module (16-bit specified)  | 0     | 0       |
| access DTO(P) Write of 2-word data from other module (16-bit specified) O FROMD(P) Read of 1-word data from other module (32-bit specified) O FROMD(P) Read of 2-word data from other module (32-bit specified) O FROMD(P) Read of 2-word data from other module (32-bit specified) O   | Module    | TO(P)                 | Write of 1-word data from other module (16-bit specified) | 0     | 0       |
| FROMD(P)         Read of 1-word data from other module (32-bit specified)         O         O           DFROMD(P)         Read of 2-word data from other module (32-bit specified)         O         O  |           | DTO(P)                | Write of 2-word data from other module (16-bit specified) | 0     | 0       |
| DFROMD(P) Read of 2-word data from other module (32-bit specified)  |           |                       |   | 0     | 0       |
|   |           |                       |   |       | 0       |
| TOD(P) Write of 1-word data from other module (32-bit specified)  |           | TOD(P)                | Write of 1-word data from other module (32-bit specified) |       | 0       |
|   |           |                       |   |       | 0       |

#### $\Diamond$ Step ladder instruction

|  |             | Instruction<br>symbol |                      | Comp<br>CPU n |   |
|--|-------------|-----------------------|----------------------|---------------|---|
|  |             | Symbol                |                      | FX5U          |   |
|  | Step ladder | STL                   | Start of step ladder | 0             | 0 |
|  |             | RETSTL                | End of step ladder   | 0             | 0 |

#### ♦ Built-in Ethernet function instruction

| Classification                                | Instruction   | Function   |   |       |
|---|---------------|--|---|-------|
|   | symbol        |  |   | FX5UC |
| Built-in Ethernet                             | SP.SOCOPEN    | Connection establishment   | 0 | 0     |
| function<br>instruction                       | SP.SOCCLOSE   | Connection disconnection   | 0 | 0     |
|   | SP.SOCRCV     | Read of received data during END processing                                      | 0 | 0     |
| Socket<br>Communication                       | SP.SOCSND     | Data transmission  | 0 | 0     |
| function                                      | SP.SOCCINF    | Read of connection information   | 0 | 0     |
|   | S(P).SOCRDATA | Read of received data of socket communication                                    | 0 | 0     |
| Communication<br>protocol support<br>function | SP.ECPRTCL    | Execution of registration protocol of<br>communication protocol support function | 0 | 0     |
| SLMP frame transmission                       | SP.SLMPSND    | SLMP message transmission to SLMP-compatible device                              | 0 | 0     |
|   | GP.OPEN       | Connection establishment   | 0 | 0     |
| Ethernet module                               | GP.CLOSE      | Connection disconnection   | 0 | 0     |
| Linemet module                                | GP.SOCRCV     | Read of received data  | 0 | 0     |
|   | GP.SOCSND     | Data transmission  | 0 | 0     |

#### ◇ PID control instruction

| Classification | Instruction<br>symbol | Function      |   | Function CPU mod |  |  |
|----------------|-----------------------|---------------|---|------------------|--|--|
|                | Symbol                |               |   | FX5UC            |  |  |
| PID control    | PID                   | PID operation | 0 | 0                |  |  |

#### ♦ List of module dedicated instructions

| GP.READ         Reading data from the PLC of another station         O         O           GP.SREAD         Reading data from the PLC of another station         O         O           GP.WRTE         Writing data to the PLC of another station         O         O           GP.SWRTE         Writing data to the PLC of another station         O         O           GP.SEND         Transmission of data to the PLC of another station         O         O           GP.SEND         Transmission of data to the PLC of another station         O         O           GP.PEX/V         Reception of data from the PLC of another station         O         O           GP.CPXFIE         Parameter setting         O  | Classification   | Instruction<br>symbol | Function  | Comp<br>CPU n<br>FX5U | oatible<br>nodule<br>FX5UC |
|---|------------------|-----------------------|---|-----------------------|----------------------------|
| CC-Link Elect<br>GPWRITE         (Aread-notice is issued.)         O         O           GPWRITE         Writing data to the PLC of another station         O         O           GPSWRITE         Writing data to the PLC of another station         O         O           GPSWRITE         Transmission of data to the PLC of another station         O         O           GPSEND         Transmission of data to the PLC of another station         O         O           GPPCOPASET         Parameter setting         O         O         O           GPPLUINI         Own station number setting         O         O         O           DHSCS         32-bit comparison reset         O  |                  | GP.READ               | Reading data from the PLC of another station      | 0                     | 0                          |
| GPWRITE         Writing data to the PLC of another station         O         O           GPSWRITE         Writing data to the PLC of another station         O         O           GPSWRITE         Writing data to the PLC of another station         O         O           GPSWRITE         Parasmission of data to the PLC of another station         O         O           GPSEND         Tarsmission of data to the PLC of another station         O         O           GPREOV         Paraenter setting         O         O         O           GPPLORN         24-bit data comparison set         O         O         O           DHSCR         32-bit data bandwidth comparison         O         O         O           High-speed<br>transfer of<br>counter         Start and stop of 32-bit data high speed input/<br>output function         O         O         O           High-speed<br>transfer of<br>communication         HOMOV(P)         High-speed transfer of 16-bit data current value         O         O         O           NVR         Inverter operation control         O         O         O         O         O           Home position return whith 2-bit data data data transfer 2         O         O         O         O         O           Romenunication         NOE         Inverter parameter   |                  | GP.SREAD              |   | 0                     | 0                          |
| CC-Link IE field<br>network         GP:SWNTE         Writing data to the PLC of another station<br>(A write notice is issued.)         O         O           GPSEND         Transmission of data to the PLC of another station<br>(GP).COMSET         O         O           GPRECV         Reception of data to the PLC of another station<br>(GP).UINI         O         O           Might speed<br>counter         DHSCS         32-bit data comparison set         O         O           DHSC         32-bit data comparison reset         O         O         O           High-speed<br>counter         DHSC         32-bit data comparison reset         O         O           HOEN(P)         Start and stop of 16-bit data high speed input/<br>output function         O         O         O           HIGN-speed<br>communication         HCMOV(P)         High-speed transfer of 16-bit data current value         O         O           NPL         Start and stop of 12-bit data ligh speed input/<br>output function         O         O         O           HIGN-speed<br>transfer of<br>communication         HCMOV(P)         High-speed transfer of 32-bit data current value         O         O           NPL         Inverter parameter write         O         O         O         O           NVR         Inverter parameter write         O         O         O  |                  | GP.WBITE              | · · · · · ·                                       | 0                     | 0                          |
| Construction         GPSEND         Transmission of data to the PLC of another station         O         O           GPRECV         Reception of data from the PLC of another station         O <td></td> <td>GP.SWRITE</td> <td>Writing data to the PLC of another station</td> <td>0</td> <td>0</td>  |                  | GP.SWRITE             | Writing data to the PLC of another station        | 0                     | 0                          |
| GIP.CCPASET         Parameter setting         O         O           GIP.UINI         Own station number setting         O         O           High speed<br>counter         DHSCR         32-bit data comparison set         O         O           DHSCR         32-bit data comparison set         O         O         O           DHSCR         32-bit data bandwidth comparison         O         O         O           DHSCR         32-bit data bandwidth comparison         O         O         O         O           DHOEN(P)         Start and stop of 16-bit data high speed input/<br>output function         O         O         O         O         O           High-speed<br>ransfer of<br>current value         DHOEN(P)         High-speed transfer of 16-bit data current value         O   | TICLWOIK         | GP.SEND               | 1   | 0                     | 0                          |
| GIP.CPASET         Parameter setting         O         O           GIP.UINI         Own station number setting         O         O           High speed<br>counter         DHSCS         32-bit data comparison set         O         O           DHSCR         32-bit data bandwidth comparison         O         O         O           DHSCR         32-bit data bandwidth comparison         O         O         O           DHSCR         32-bit data bandwidth comparison         O         O         O           DHOEN(P)         Start and stop of 16-bit data high speed input/<br>output function         O         O         O           High-speed<br>transfer of<br>current value         DHOEN(P)         High-speed transfer of 16-bit data current value         O         O           Newter         RS2         Serial data transfer 2         O         O         O           Inverter         RS2         Serial data transfer 32-bit data current value         O         O           Inverter         NDR         Inverter parameter mad         O         O         O         O           Inverter         NDR         Inverter parameter write         O         O         O         O         O           Inverter parameter write         O         O  |                  | GP.RECV               | Reception of data from the PLC of another station | 0                     | 0                          |
| G(P)_UIN         Own station number setting         O         O           High speed<br>counter         DHSCS         32-bit data comparison set         O         O           High speed<br>counter         DHSCR         32-bit data bandwidth comparison         O         O           HiDEN(P)         Start and stop of 16-bit data high speed input/<br>output function         O         O         O           HiDEN(P)         Start and stop of 32-bit data current value         O         O         O           HiDEN(P)         Start and stop of 32-bit data current value         O         O         O           HiDMOV(P)         High-speed transfer of 16-bit data current value         O         O         O           DHCMOV(P)         High-speed transfer of 32-bit data current value         O         O         O           Inverter         RS2         Serial data transfer 2         O         O         O           Inverter         NDR         Inverter parameter write         O         O         O         O           Inverter         NDR         Inverter parameter write         O         O         O         O           Inverter         MODBUS         ADPRW         MODBUS data read/write         O         O         O         O         O </td <td></td> <td>G(P).CCPASET</td> <td></td> <td>-</td> <td>0</td>   |                  | G(P).CCPASET          |   | -                     | 0                          |
| High speed<br>counter         DHSCS         32-bit data comparison reset         O         O           High speed<br>counter         DHSCR         32-bit data bandwidth comparison         O         O           High-speed<br>transfer of<br>current value         Start and stop of 16-bit data high speed input/<br>output function         O         O           High-speed<br>transfer of<br>current value         High-speed transfer of 16-bit data current value         O         O           High-speed<br>communication         High-speed transfer of 16-bit data current value         O         O           NCK         Inverter operation control         O         O           NVR         Inverter operation control         O         O           NWR         Inverter parameter read         O         O           MODBUS         SP)-CPRTCL.         Execution of communication protocol registered<br>by engineering tool         O           MODBUS         SP)-CPRTCL.         Execution of communication protocol registered<br>by engineering tool         O         O           DIVIT         16-bit data variable speed pulse         O         O         O           DDSZR         Home position return with 16-bit data dog search         O         O         O           DIVIT         16-bit data variable speed pulse         O         O         O <td></td> <td></td> <td>-</td> <td>0</td> <td>0</td>  |                  |                       | -   | 0                     | 0                          |
| High speed<br>counter         DHSCR         32-bit data bandwidth comparison         O         O           HIOEN(P)         Start and stop of 16-bit data high speed input/<br>output function         O         O         O           High-speed<br>transfer of<br>current value         HCMOV(P)         High-speed transfer of 16-bit data current value         O         O           External device<br>communication         RS2         Serial data transfer 2         O         O           Inverter<br>communication         NCK         Inverter operation monitor         O         O           INVER         Inverter operation control         O         O         O           INVER         Inverter parameter write         O         O         O           INVER         Inverter parameter write         O         O         O           MODBUS         ADPRW         MODBUS data read/write         O         O         O           Communication<br>protocol support<br>function         S(P), CPRTCL         Execution of communication protocol registered<br>by engineering tool         O         O           DDSZR         Home position return with 16-bit data data gasearch         O         O         O         O           DVIT         16-bit data interrupt positioning         O         O         O         O   |                  |                       | 0   |                       | 0                          |
| High speed<br>counter         DHSZ         32-bit data bandwidth comparison         O         O           HIOEN(P)         Start and stop of 18-bit data high speed input/<br>output function         O         O           High-speed<br>transfer of<br>current value         Start and stop of 32-bit data high speed input/<br>output function         O         O           High-speed<br>transfer of<br>current value         HCMOV(P)         High-speed transfer of 16-bit data current value         O         O           External device<br>communication         RS2         Serial data transfer 2         O         O         O           Inverter<br>communication         Inverter operation monitor         O         O         O         O           INVR         Inverter parameter read         O         O         O         O           INVR         Inverter parameter write         O         O         O         O           MODBUS         ADPRW         MODBUS data read/write         O         O         O           Communication<br>protocol support         SIP),CPRTCL         Execution of communication protocol registered<br>by engineering tool         O         O           DDVT         16-bit data interrupt positioning         O         O         O         O           DBSZR         Home position return with 16-bit data dag searc   |                  |                       |   |                       |                            |
| High speed<br>counter         Image: Start and stop of 16-bit data high speed input/<br>output function         Image: Start and stop of 32-bit data high speed input/<br>output function         Image: Start and stop of 32-bit data high speed input/<br>output function         Image: Start and stop of 32-bit data current value         Image: Start and stop of 32-bit data current value         Image: Start and stop of 32-bit data current value         Image: Start and stop of 32-bit data current value         Image: Start and stop of 32-bit data current value         Image: Start and stop of 32-bit data current value         Image: Start and stop of 32-bit data current value         Image: Start and stop of 32-bit data current value         Image: Start and stop of 32-bit data current value         Image: Start and stop of 32-bit data current value         Image: Start and stop of 32-bit data current value         Image: Start and stop of 32-bit data current value         Image: Start and stop of 32-bit data current value         Image: Start and stop of 32-bit data current value         Image: Start and stop of 32-bit data current value         Image: Start and stop of 32-bit data current value         Image: Start and stop of 32-bit data current value         Image: Start and stop of 32-bit data current value         Image: Start and stop of 32-bit data current value         Image: Start and stop of 32-bit data fut ansfer 2         Image: Start and stop of 32-bit data fut ansfer 2         Image: Start and stop of 32-bit data fut ansfer 2         Image: Start and stop of 32-bit data fut ansfer 2         Image: Start and stop of 32-bit data fut and stop of 32-bit data fut and stop of 32-bit data fut ansfer 2 <thimage: 32-bit="" and="" data="" fut<="" of="" start="" stop="" td=""><td></td><td></td><td></td><td>-</td><td>-</td></thimage:> |                  |                       |   | -                     | -                          |
| Industry         output function         O         O           High-speed<br>transfer of<br>current value         Serial and stop of 32-bit data high speed input/<br>output function         O         O           High-speed<br>transfer of<br>current value         HCMOV(P)         High-speed transfer of 16-bit data current value         O         O           External device<br>communication         RS2         Serial data transfer 2         O         O           Inverter<br>communication         NCK         Inverter operation monitor         O         O           INVDR         Inverter parameter read         O         O         O           INVDR         Inverter parameter read         O         O         O           MMC         Multiple commands of inverter         O         O         O           MMC         Multiple communication protocol support<br>function         SIP).CPRTCL         Execution of communication protocol registered<br>by engineering tool         O         O           DDSZR         Home position return with 16-bit data dog search         O         O         O         O           DVTT         16-bit data interrupt positioning         O         O         O         O           DVTT         16-bit data variable speed pulse         O         O         O         O      <  |                  |                       |   |                       |                            |
| DHIOEN(P)         output function         O         O           High-speed<br>transfer of<br>current value         HCMOV(P)         High-speed transfer of 16-bit data current value         O         O           External device<br>communication         RS2         Serial data transfer 2         O         O         O           Inverter         NCK         Inverter operation control         O         O         O           Inverter         NVR         Inverter parameter read         O         O         O           NVR         Inverter parameter read         O         O         O         O           NVR         Inverter parameter write         O         O         O         O           MODBUS         ADPRW         MODBUS data read/write         O         O         O           Communication<br>function         SP)-CPRTCL         Execution of communication protocol registered<br>by engineering tool         O         O         O           DDSZR         Home position return with 16-bit data dog search         O         O         O         O           DBVT         16-bit data interrupt positioning         O         O         O         O         O           DPWUL         Multiple axis simultaneous drive positioning         O         O  | counter          | HIOEN(P)              | output function                                   | 0                     | 0                          |
| Inster of<br>current value         Improvide unsues or round data current value <t< td=""><td></td><td>DHIOEN(P)</td><td></td><td>0</td><td>0</td></t<>  |                  | DHIOEN(P)             |   | 0                     | 0                          |
| current value         DHCMOV(P)         High-speed transfer of 32-bit data current value         O         O           External device<br>communication         RS2         Serial data transfer 2         O         O           INVER         Inverter operation monitor         O         O         O           INVER         Inverter operation control         O         O         O           INVER         Inverter parameter read         O         O         O           INVER         Inverter parameter batch write         O         O         O           INVER         Inverter parameter batch write         O         O         O           MODBUS         ADPRW         MODBUS data read/write         O         O         O           Communication<br>function         S(P).CPRTCL         Execution of communication protocol registered<br>by engineering tool         O         O         O           DDSZR         Home position return with 16-bit data dog search         O         O         O         O           DDVT         32-bit data interrupt positioning         O         O         O         O           DDVT         32-bit data interrupt positioning         O         O         O         O           DDVT         32-bit data variable sp  |                  | HCMOV(P)              | High-speed transfer of 16-bit data current value  | 0                     | 0                          |
| communication         HS2         Serial data transfer 2         O         O           Inverter         Inverter operation monitor         O         O           VDR         Inverter operation control         O         O           VRD         Inverter parameter read         O         O           INVRD         Inverter parameter write         O         O           INVRD         Inverter parameter batch write         O         O           MCDBUS         ADPRW         MODBUS data read/write         O         O           MCDBUS         ADPRW         MODBUS data read/write         O         O           Communication protocol support function         S(P).CPRTCL         Execution of communication protocol registered by engineering tool         O         O           DSZR         Home position return with 16-bit data dog search         O         O         O           DVT         16-bit data interrupt positioning         O         O         O         O           DVT         16-bit data interrupt positioning         O         O         O         O           DVT         16-bit data variable speed pulse         O         O         O         O           DVTUL         16-bit data relative positioning         O <td>current value</td> <td>DHCMOV(P)</td> <td>High-speed transfer of 32-bit data current value</td> <td>0</td> <td>0</td>  | current value    | DHCMOV(P)             | High-speed transfer of 32-bit data current value  | 0                     | 0                          |
| Inverter<br>communication         Inverter<br>IVER         Inverter operation control         O         O           IVRD         Inverter parameter read         O         O         O           IVRD         Inverter parameter write         O         O         O           IVRD         Inverter parameter write         O         O         O           MODBUS         ADPRW         MODBUS data read/write         O         O           Communication<br>protocol support<br>function         S(P).CPRTCL         Execution of communication protocol registered<br>by engineering tool         O         O           DSZR         Home position return with 16-bit data dog search         O         O         O           DDVT         16-bit data interrupt positioning         O         O         O         O           DVTT         32-bit data interrupt positioning         O         O         O         O           DPVTEL         Positioning by multiple-table operation         O         O         O         O           DRVTEL         Positioning by multiple-table operation         O         O         O         O           DRVTBL         Positioning by multiple-table operation         O         O         O         O         O         O         O   |                  |                       | Serial data transfer 2                            | 0                     | 0                          |
| Inverter<br>communication         INPUTE         Inverter parameter read         O         O           INWR         Inverter parameter write         O         O         O           INWR         Inverter parameter write         O         O         O           INURD         Mutiple commands of inverter         O         O         O           MODBUS         ADPRW         MODBUS data read/write         O         O         O           Communication         SIP).CPRTCL         Execution of communication protocol registered<br>by engineering tool         O         O         O           DDSZR         Home position return with 16-bit data dog search         O         O         O         O           DDVIT         16-bit data interrupt positioning         O         <  |                  |                       | Inverter operation monitor                        | -                     | -                          |
| Notes         WWR         Inverter parameter write         O         O           VEWR         Inverter parameter write         O         O         O           MODBUS         ADPRW         MUDBUS data read/write         O         O           Communication<br>protocol support<br>function         SP).CPRTCL         Execution of communication protocol registered<br>by engineering tool         O         O           DSZR         Home position return with 16-bit data dog search         O         O           DVT         16-bit data interrupt positioning         O         O           DVT         16-bit data interrupt positioning         O         O           DVT         32-bit data interrupt positioning         O         O           DVTT         16-bit data anterrupt positioning         O         O           DVTT         16-bit data interrupt positioning         O         O           DRVBL         Positioning by 1-table operation         O         O           DRVUL         Multiple axis simultaneous drive positioning         O         O           DRVU         16-bit data variable speed pulse         O         O           DRV         16-bit data variable speed pulse         O         O           DRVA         16-bit data variable speed  |                  | IVDR                  | Inverter operation control                        | 0                     | 0                          |
| Norm         Norm         Norm         Norm         O         O           INSUM         Inverter parameter batch write         O         O         O           INMC         Multiple commands of inverter         O         O         O           MCDBUS         ADPRW         MODBUS data read/write         O         O         O           Communication<br>protocol support         S(P).CPRTCL         Execution of communication protocol registered<br>by engineering tool         O         O         O           DSZR         Home position return with 16-bit data dog search         O         O         O         O           DDSZR         Home position return with 32-bit data dog search         O<   | Inverter         | IVRD                  | Inverter parameter read                           | 0                     | 0                          |
| INMC         Multiple commands of inverter         O         O           MODBUS         ADPRW         MODBUS data read/write         O         O           Communication<br>protocol support<br>function         S(P),CPRTCL         Execution of communication protocol registered<br>by engineering tool         O         O           DSZR         Home position return with 16-bit data dog search         O         O           DDSZR         Home position return with 32-bit data dog search         O         O           DVTT         16-bit data interrupt positioning         O         O           DVTT         32-bit data interrupt positioning         O         O           DVTT         16-bit data variable speed pulse         O         O           DRVTBL         Positioning by 1-table operation         O         O           DRVTUL         Multiple axis simultaneous drive positioning         O         O           DRVMUL         Multiple axis simultaneous drive positioning         O         O           DRVMUL         Multiple axis absolute positioning         O         O           DRV         16-bit data variable speed pulse         O         O           DRV         32-bit data absolute positioning         O         O           DRVA         16-bit data absolu   | communication    | IVWR                  | Inverter parameter write                          | 0                     | 0                          |
| MODBUS         ADPRW         MODBUS data read/write         O         O           Communication protocol support function         SP).CPRTCL         Execution of communication protocol registered by engineering tool         O         O           DSZR         Home position return with 16-bit data dog search         O         O           DDSZR         Home position return with 32-bit data dog search         O         O           DVT         16-bit data interrupt positioning         O         O         O           DVTT         16-bit data interrupt positioning         O         O         O         O           DVTT         32-bit data interrupt positioning         O <td></td> <td>IVBWR</td> <td>Inverter parameter batch write</td> <td>0</td> <td>0</td>   |                  | IVBWR                 | Inverter parameter batch write                    | 0                     | 0                          |
| Communication<br>protocol support<br>function         S(P).CPRTCL         Execution of communication protocol registered<br>by engineering tool         O         O           DSZR         Home position return with 16-bit data dog search         O         O           DDSZR         Home position return with 16-bit data dog search         O         O           DDVT         16-bit data interrupt positioning         O         O           DVT         16-bit data interrupt positioning         O         O           DVT         32-bit data interrupt positioning         O         O           DRVTBL         Positioning by 1-table operation         O         O           DRVTEL         Positioning by multiple-table operation         O         O           DRVMUL         Multiple axis simultaneous drive positioning         O         O           DRVMUL         Multiple axis simultaneous drive positioning         O         O           DRV         16-bit data variable speed pulse         O         O           DRV         16-bit data absolute positioning         O         O           DRV         32-bit data variable speed pulse         O         O           DRV         32-bit data absolute positioning         O         O           DRV         32-bit data absolute position  |                  | IVMC                  | Multiple commands of inverter                     | 0                     | 0                          |
| protocol support<br>function         S(P).CPRTCL         Execution of communication protocol registered<br>by engineering tool         O         O           DSZR         Home position return with 16-bit data dog search         O         O           DDSZR         Home position return with 32-bit data dog search         O         O           DDT         16-bit data interrupt positioning         O         O           DVIT         32-bit data interrupt positioning         O         O           DRVTBL         Positioning by 1-table operation         O         O           DRVTBL         Positioning by multiple-table operation         O         O           DRVMUL         Multiple axis simultaneous drive positioning         O         O           DRVMUL         Multiple axis simultaneous drive positioning         O         O           DRVMUL         Multiple axis simultaneous drive positioning         O         O           DRV         16-bit data variable speed pulse         O         O           DRV         16-bit data and be poet positioning         O         O           DRVA         32-bit data variable speed pulse         O         O           DRVA         16-bit data absolute positioning         O         O           DRVA         32-bit data absolute po   | MODBUS           | ADPRW                 | MODBUS data read/write                            | 0                     | 0                          |
| Positioning         O         O           DDS2R         Home position return with 32-bit data dog search         O         O           DVT         16-bit data interrupt positioning         O         O           DDVT         32-bit data interrupt positioning         O         O           DVT         16-bit data interrupt positioning         O         O           DDVT         32-bit data interrupt positioning         O         O           DRVTBL         Positioning by nultiple-table operation         O         O           DRVTBL         Positioning by multiple-table operation         O         O           DABS         32-bit data variable speed pulse         O         O           PLSV         16-bit data variable speed pulse         O         O           DRVI         16-bit data variable speed pulse         O         O           DRV         32-bit data variable speed pulse         O         O           DRV         32-bit data variable speed pulse         O         O           DRV         16-bit data absolute positioning         O         O           DRVA         16-bit data absolute positioning         O         O           DRVA         32-bit data absolute positioning         O         O   | protocol support | S(P).CPRTCL           |   | 0                     | 0                          |
| Positioning         O         O           DVT         16-bit data interrupt positioning         O         O           DDVT         32-bit data interrupt positioning         O         O           TBL         Positioning by 1-table operation         O         O           DRVTEL         Positioning by multiple-table operation         O         O           DRVTBL         Positioning by multiple-table operation         O         O           DRVMUL         Multiple axis simultaneous drive positioning         O         O           DRVMUL         Multiple axis simultaneous drive positioning         O         O           DRVS         32-bit data variable speed pulse         O         O         O           DPLSV         32-bit data variable speed pulse         O         O         O           DRVI         16-bit data relative positioning         O         O         O         O           DRVA         16-bit data absolute positioning         O         O         O         O         O           DRVA         16-bit data absolute positioning         O         O         O         O         O         O         O         O         O         O         O         O         O         O  |                  | DSZR                  | Home position return with 16-bit data dog search  | 0                     | 0                          |
| Positioning         O         O           TBL         Positioning by 1-table operation         O         O           TBL         Positioning by 1-table operation         O         O           DRVTBL         Positioning by multiple-table operation         O         O           DRVTBL         Positioning by multiple-table operation         O         O           DRVMUL         Multiple axis simultaneous drive positioning         O         O           DABS         32-bit data ABS current value read         O         O           DPLVX         32-bit data variable speed pulse         O         O           DPLV         16-bit data relative positioning         O         O           DRV1         16-bit data relative positioning         O         O           DRV1         16-bit data relative positioning         O         O           DRV1         32-bit data absolute positioning         O         O           DRVA         16-bit data relative positioning         O         O           DRVA         32-bit data absolute positioning         O         O           DRVA         32-bit data absolute positioning         O         O           GPRSTR11         GABRST1         Absolute position restoration of specified axis   |                  | DDSZR                 | Home position return with 32-bit data dog search  | 0                     | 0                          |
| TBL         Positioning by 1-table operation         O         O           DRVTBL         Positioning by multiple-table operation         O         O           DRVMUL         Multiple axis simultaneous drive positioning         O         O           DRVMUL         Multiple axis simultaneous drive positioning         O         O           DABS         32-bit data ABS current value read         O         O           PLSV         16-bit data variable speed pulse         O         O           DRV         16-bit data relative positioning         O         O           DRV         16-bit data relative positioning         O         O           DRVA         16-bit data absolute positioning         O         O           DRVA         16-bit data absolute positioning         O         O           DRVA         32-bit data absolute positioning         O         O           DRVA         32-bit data absolute positioning         O         O           DRVA         32-bit data absolute positioning         O         O           GABRST1         Absolute position restoration of specified axis         O         O           GPPSTRT12         Starting the positioning of specified axis         O         O           GPPENWRT   |                  | DVIT                  | 16-bit data interrupt positioning                 | 0                     | 0                          |
| TBL         Positioning by 1-table operation         O         O           DRVTBL         Positioning by multiple-table operation         O         O           DRVMUL         Multiple axis simultaneous drive positioning         O         O           DRVMUL         Multiple axis simultaneous drive positioning         O         O           DABS         32-bit data ABS current value read         O         O           PLSV         16-bit data variable speed pulse         O         O           DRV         16-bit data relative positioning         O         O           DRV         16-bit data relative positioning         O         O           DRVA         16-bit data absolute positioning         O         O           DRVA         16-bit data absolute positioning         O         O           DRVA         32-bit data absolute positioning         O         O           DRVA         32-bit data absolute positioning         O         O           DRVA         32-bit data absolute positioning         O         O           GABRST1         Absolute position restoration of specified axis         O         O           GPPSTRT12         Starting the positioning of specified axis         O         O           GPPENWRT   |                  | DDVIT                 | 32-bit data interrupt positioning                 | 0                     | 0                          |
| DRVMUL         Multiple axis simultaneous drive positioning         O         O           DABS         32-bit data ABS current value read         O         O           PLSV         16-bit data variable speed pulse         O         O           DPLSV         32-bit data variable speed pulse         O         O           DPLSV         32-bit data variable speed pulse         O         O           DPLV         16-bit data variable speed pulse         O         O           DRV         16-bit data variable speed pulse         O         O           DRV         16-bit data variable speed pulse         O         O           DRVA         32-bit data relative positioning         O         O           DDRVA         32-bit data absolute positioning         O         O           DDRVA         32-bit data absolute positioning         O         O           GABRST1         Absolute position restoration of specified axis         O         O           GPPSTRT12         Starting the positioning of specified axis         O         O           GPTEACH1         feaching of specified axis         O         O         O           GPPFWRT         Backing up the module         O         O         O           GPPINIT </td <td></td> <td>TBL</td> <td></td> <td>0</td> <td>0</td>  |                  | TBL                   |   | 0                     | 0                          |
| Positioning         DABS         32-bit data ABS current value read         O         O           PLSV         16-bit data variable speed pulse         O         O           DPLSV         32-bit data variable speed pulse         O         O           DPLSV         32-bit data variable speed pulse         O         O           DPLV         32-bit data variable speed pulse         O         O           DDRVI         32-bit data relative positioning         O         O           DDRVI         32-bit data absolute positioning         O         O           DRVA         16-bit data absolute positioning         O         O           DRVA         32-bit data absolute positioning         O         O           GABRST1         Absolute position restoration of specified axis         O         O           GPPSTRT12         Starting the positioning of specified axis         O         O           GP.TEACH1         reaching of specified axis         O         O           GP.PFWRT         Backing up the module         O         O           GP.PINIT         Module initialization         O         O   |                  | DRVTBL                | Positioning by multiple-table operation           | 0                     | 0                          |
| Positioning         PLSV         16-bit data variable speed pulse         O         O           DPLSV         32-bit data variable speed pulse         O         O         O           DRVI         16-bit data variable speed pulse         O         O         O           DDRVI         32-bit data variable speed pulse         O         O         O           DDRVI         32-bit data relative positioning         O         O         O           DRVA         16-bit data absolute positioning         O         O         O         O           DRVA         32-bit data absolute positioning         O <td></td> <td>DRVMUL</td> <td>Multiple axis simultaneous drive positioning</td> <td>0</td> <td>0</td>  |                  | DRVMUL                | Multiple axis simultaneous drive positioning      | 0                     | 0                          |
| Positioning         DPLSV         32-bit data variable speed pulse         O         O           DPV         32-bit data variable speed pulse         O         O         O           DRV         16-bit data variable speed pulse         O         O         O           DRV         32-bit data variable speed pulse         O         O         O           DRV         32-bit data relative positioning         O         O         O           DRVA         16-bit data absolute positioning         O         O         O         O           DRVA         16-bit data absolute positioning         O  |                  | DABS                  | 32-bit data ABS current value read                | 0                     | 0                          |
| Positioning         DPLSV         32-bit data variable speed pulse         O         O           DRVI         16-bit data relative positioning         O         O           DDRVI         32-bit data relative positioning         O         O           DDRVA         16-bit data relative positioning         O         O           DRVA         16-bit data absolute positioning         O         O           DRVA         32-bit data absolute positioning         O         O           GABRST1         Absolute position restoration of specified axis         O         O           GP.PSTRT12         Starting the positioning of specified axis         O         O           GP.PSTRT12         Eaching of specified axis         O         O           GP.PFWRT         Backing up the module         O         O           GP.PINIT         Module initialization         O         O           BFM split read/         RBFM         BFM split read         O         O   |                  | PLSV                  | 16-bit data variable speed pulse                  | 0                     | 0                          |
| Positioning         DRV         16-bit data relative positioning         O         O           DDRV         32-bit data relative positioning         O         O           DRVA         16-bit data relative positioning         O         O           DDRVA         32-bit data relative positioning         O         O           DDRVA         32-bit data absolute positioning         O         O           DDRVA         32-bit data absolute positioning         O         O           GABRST1         Absolute position restoration of specified axis         O         O           GPPSTRT2         Starting the positioning of specified axis         O         O           GP.TEACH1         Teaching of specified axis         O         O           GP.PINIT         Backing up the module         O         O           GP.PINIT         Module initialization         O         O  |                  | DPLSV                 |   | 0                     | 0                          |
| DDRVI     32-bit data relative positioning     O     O       DRVA     16-bit data absolute positioning     O     O       DDRVA     32-bit data absolute positioning     O     O       DDRVA     32-bit data absolute positioning     O     O       GABRST1     Absolute position restoration of specified axis     O     O       GPPSTRT1     GPPSTRT1     Starting the positioning of specified axis     O     O       GPTEACH1     Teaching of specified axis     O     O     O       GPPNWRT     Backing up the module     O     O       GPPINIT     Module initialization     O     O       BFM split read/     RBFM     BFM split read     O     O   | Positioning      | DRVI                  |   |                       | 0                          |
| DRVA         16-bit data absolute positioning         O         O           DRVA         32-bit data absolute positioning         O         O           GABRST1         Absolute position restoration of specified axis         O         O           GPPSTRT1         Starting the positioning of specified axis         O         O           GPPSTRT2         Starting the positioning of specified axis         O         O           GPPSTRT2         Starting the positioning of specified axis         O         O           GPPETACH2         Teaching of specified axis         O         O           GP.PFWRT         Backing up the module         O         O           GP.PINIT         Module initialization         O         O           BFM split read/         BFM         BFM split read         O   | r ositioning     |                       |   |                       |                            |
| DDRVA         32-bit data absolute positioning         O         O           GABRST1         Absolute position restoration of specified axis         O         O           GPPSTRT1         Starting the positioning of specified axis         O         O           GPPSTRT2         Starting the positioning of specified axis         O         O           GPTEACH1         Teaching of specified axis         O         O           GP.PFWRT         Backing up the module         O         O           GP.PINIT         Module initialization         O         O           BFM split read/         BFM         BFM split read         O         O   |                  |                       |   | -                     | -                          |
| G.ABRST1<br>G.ABRST2         Absolute position restoration of specified axis         O         O           GPPSTRT1<br>GPPSTRT2         Starting the positioning of specified axis         O         O           GPTEACH1<br>GP.FTRACH2         Teaching of specified axis         O         O           GP.FTWRT         Backing up the module         O         O           GP.PFWRT         Backing up the module         O         O           BFM split read/         RBFM         BFM split read         O         O  |                  |                       |   |                       |                            |
| GPPSTRT1<br>GPPSTRT2         Starting the positioning of specified axis         O         O           GP.TEACH1<br>GP.TEACH2         Teaching of specified axis         O         O         O           GP.PFWRT         Backing up the module         O         O         O           GP.PINIT         Module initialization         O         O         O           BFM split read/         RBFM         BFM split read         O         O   |                  | G.ABRST1              |   |                       |                            |
| GP.TEACH2         leaching of specified axis         O         O           GP.PFWRT         Backing up the module         O         O           GP.PFWRT         Module initialization         O         O           BFM split read/         RBFM         BFM split read         O         O  |                  | GP.PSTRT1             | Starting the positioning of specified axis        | 0                     | 0                          |
| GP.PFWRT         Backing up the module         O         O           GP.PINIT         Module initialization         O         O           BFM split read/         RBFM         BFM split read         O         O   |                  |                       | Teaching of specified axis                        | 0                     | 0                          |
| GP.PINT         Module initialization         O         O           BFM split read/         RBFM         BFM split read         O         O   |                  | GP.PFWRT              | Backing up the module                             | 0                     | 0                          |
| BFM split read/ RBFM BFM split read O O   |                  | GP.PINIT              |   | 0                     | 0                          |
|   | BEM split read/  |                       |   | 0                     | 0                          |
|   |                  | WBFM                  |   |                       |                            |

## **Special devices**

Typical special relays and special registers are described below. For details, refer to manual.

#### List of special relays

#### ◇ Diagnostic information

| No.  | Name   | FX5U | FX5UC |
|------|--|------|-------|
| SM0  | Latest self diagnosis error (including annunciator ON)     | 0    | 0     |
| SM1  | Latest self diagnosis error (not including annunciator ON) | 0    | 0     |
| SM50 | Error reset  | 0    | 0     |
| SM51 | Battery low latch  | 0    | 0     |
| SM52 | Battery low  | 0    | 0     |
| SM53 | AC/DC DOWN   | 0    | 0     |
| SM56 | Operation error  | 0    | 0     |
| SM61 | I/O module verify error                                    | 0    | 0     |
| SM62 | Annunciator  | 0    | 0     |

#### **♦** System information

| No.   | Name                    | FX5U | FX5UC |
|-------|-------------------------|------|-------|
| SM203 | STOP contact            | 0    | 0     |
| SM204 | PAUSE contact           | 0    | 0     |
| SM210 | Clock data set request  | 0    | 0     |
| SM211 | Clock data set error    | 0    | 0     |
| SM213 | Clock data read request | 0    | 0     |

#### ♦ System clock

| No.   | Name                             | FX5U | FX5UC |
|-------|----------------------------------|------|-------|
| SM400 | Always ON                        | 0    | 0     |
| SM401 | Always OFF                       | 0    | 0     |
| SM402 | After RUN, ON for one scan only  | 0    | 0     |
| SM403 | After RUN, OFF for one scan only | 0    | 0     |
| SM409 | 0.01 sec. clock                  | 0    | 0     |
| SM410 | 0.1 sec. clock                   | 0    | 0     |
| SM411 | 0.2 sec. clock                   | 0    | 0     |
| SM412 | 1 sec. clock                     | 0    | 0     |
| SM413 | 2 sec. clock                     | 0    | 0     |
| SM414 | 2n sec. clock                    | 0    | 0     |
| SM415 | 2n ms clock                      | 0    | 0     |

#### $\diamond$ Instruction related

| No.   | Name                                      |   | FX5UC |
|-------|---|---|-------|
| SM700 | Carry flag                                | 0 | 0     |
| SM701 | Output character count switching          | 0 | 0     |
| SM703 | Sort order                                | 0 | 0     |
| SM704 | Block comparison                          | 0 | 0     |
| SM709 | DT/TM instruction improper data detection | 0 | 0     |

#### ◇ For serial communication

| No.    | Name  | FX5U | FX5UC |
|--------|---|------|-------|
| SM8500 | Serial communication error (ch1)                | 0    | 0     |
| SM8560 | Data transfer delayed (ch1)                     | 0    | 0     |
| SM8561 | Data transfer flag (ch1)                        | 0    | 0     |
| SM8562 | Receive completion flag (ch1)                   | 0    | 0     |
| SM8563 | Carrier detection flag (ch1)                    | 0    | 0     |
| SM8564 | Data set ready flag (ch1)                       | 0    | 0     |
| SM8565 | Time-out check flag (ch1)                       | 0    | 0     |
| SM8740 | Station No. setting SD latch enabled (ch1)      | 0    | 0     |
| SM8800 | MODBUS RTU communication (ch1)                  | 0    | 0     |
| SM8801 | Retry (ch1)                                     | 0    | 0     |
| SM8802 | Timeout (ch1)                                   | 0    | 0     |
| SM8861 | Host station No. setting SD latch enabled (ch1) | 0    | 0     |
| SM8920 | Inverter communication (ch1)                    | 0    | 0     |
| SM8921 | IVBWR instruction error (ch1)                   | 0    | 0     |
| SM9040 | Data communication error (Master station)       | 0    | 0     |
| SM9041 | Data communication error (Slave station No.1)   | 0    | 0     |

#### ◇ FX compatible area

| No.    | Name  | FX5U |   |
|--------|---|------|---|
| SM8000 | RUN monitor NO contact                                  | 0    | 0 |
| SM8001 | RUN monitor NC contact                                  | 0    | 0 |
| SM8002 | Initial pulse NO contact                                | 0    | 0 |
| SM8003 | Initial pulse NC contact                                | 0    | 0 |
| SM8004 | Error occurrence  | 0    | 0 |
| SM8005 | Battery voltage low                                     | 0    | 0 |
| SM8006 | Battery error latch                                     | 0    | 0 |
| SM8007 | Momentary power failure                                 | 0    | 0 |
| SM8008 | Power failure detected                                  | 0    | 0 |
| SM8011 | 10 msec clock pulse                                     | 0    | 0 |
| SM8012 | 100 msec clock pulse                                    | 0    | 0 |
| SM8013 | 1 sec clock pulse                                       | 0    | 0 |
| SM8014 | 1 min clock pulse                                       | 0    | 0 |
| SM8015 | Clock stop and preset                                   | 0    | 0 |
| SM8016 | Time read display is stopped                            | 0    | 0 |
| SM8017 | ±30 seconds correction                                  | 0    | 0 |
| SM8019 | Real time clock error                                   | 0    | 0 |
| SM8020 | Zero  | 0    | 0 |
| SM8021 | Borrow  | 0    | 0 |
| SM8022 | Carry   | 0    | 0 |
| SM8023 | Real time clock access error                            | 0    | 0 |
| SM8026 | Operation stop mode with one ramp output instruction    | 0    | 0 |
| SM8029 | Completion of instruction execution                     | 0    | 0 |
| SM8031 | Non-latch memory all clear                              | 0    | 0 |
| SM8032 | Latch memory all clear                                  | 0    | 0 |
| SM8033 | Memory hold function when RUN→ STOP                     | 0    | 0 |
| SM8034 | All outputs prohibited                                  | 0    | 0 |
| SM8039 | Constant scan mode                                      | 0    | 0 |
| SM8040 | For STL: Transition prohibited                          | 0    | 0 |
| SM8041 | For STL: Start of operation during automatic operation  | 0    | 0 |
| SM8042 | For STL: Start pulse                                    | 0    | 0 |
| SM8043 | For STL: Completion of home position return             | 0    | 0 |
| SM8044 | For STL: Home position condition                        | 0    | 0 |
| SM8045 | For STL: All output reset prohibited during mode switch | 0    | 0 |
| SM8046 | For STL: With STL state ON                              | 0    | 0 |
| SM8047 | For STL: STL monitor (SD8040 to SD8047) enabled         | 0    | 0 |
| SM8048 | Annunciator operation                                   | 0    | 0 |
| SM8049 | ON annunciator minimum number enabled                   | 0    | 0 |
| SM8063 | Serial communication error1 (ch1)                       | 0    | 0 |
| SM8067 | Operation error   | 0    | 0 |
| SM8068 | Operation error latch                                   | 0    | 0 |

#### List of special registers

#### $\Diamond$ Diagnostic information

| No. | Name  |   | FX5UC |
|-----|---|---|-------|
| SD0 | Latest self diagnosis error code                          | 0 | 0     |
| SD1 | Clock time for self diagnosis error occurrence (Year)     | 0 | 0     |
| SD2 | Clock time for self diagnosis error occurrence (Month)    | 0 | 0     |
| SD3 | Clock time for self diagnosis error occurrence (Day)      | 0 | 0     |
| SD4 | Clock time for self diagnosis error occurrence (Hour)     | 0 | 0     |
| SD5 | Clock time for self diagnosis error occurrence (Minute)   | 0 | 0     |
| SD6 | Clock time for self diagnosis error occurrence (Second)   | 0 | 0     |
| SD7 | Clock time for self diagnosis error occurrence (Day Week) | 0 | 0     |

#### $\diamond$ System information

| No.   | Name                  |   | FX5UC |
|-------|-----------------------|---|-------|
| SD203 | CPU Status            | 0 | 0     |
| SD210 | Clock Data (Year)     | 0 | 0     |
| SD211 | Clock Data (Month)    | 0 | 0     |
| SD212 | Clock Data (Day)      | 0 | 0     |
| SD213 | Clock Data (Hour)     | 0 | 0     |
| SD214 | Clock Data (Minute)   | 0 | 0     |
| SD215 | Clock Data (Second)   | 0 | 0     |
| SD216 | Clock Data (Day Week) | 0 | 0     |

#### $\diamond$ System clock

| No.   | Name                       |   | FX5UC |
|-------|----------------------------|---|-------|
| SD412 | One second counter         | 0 | 0     |
| SD414 | 2n second clock setting    | 0 | 0     |
| SD415 | 2n ms second clock setting | 0 | 0     |
| SD420 | Scan counter               | 0 | 0     |

#### ♦ Scan information

|       | Name                     |   | FX5UC |
|-------|--------------------------|---|-------|
| SD500 | Execution program number | 0 | 0     |
| SD520 | Current scan time (ms)   | 0 | 0     |
| SD521 | Current scan time (µs)   | 0 | 0     |
| SD522 | Minimum scan time (ms)   | 0 | 0     |
| SD523 | Minimum scan time (µs)   | 0 | 0     |
| SD524 | Maximum scan time (ms)   | 0 | 0     |
| SD525 | Maximum scan time (µs)   | 0 | 0     |

#### ◇ For serial communication

| No.    | Name  |   |   |
|--------|---|---|---|
| SD8500 | Serial communication error code (ch1)       | 0 | 0 |
| SD8501 | Serial communication error details (ch1)    | 0 | 0 |
| SD8502 | Serial communication setting (ch1)          | 0 | 0 |
| SD8503 | Serial communication operational mode (ch1) | 0 | 0 |

#### ◇ For built-in Ethernet

| No.     | Name                                    |   | FX5UC |
|---------|---|---|-------|
| SD10050 | Local node IP address [low-order]       | 0 | 0     |
| SD10051 | Local node IP address [high-order]      | 0 | 0     |
| SD10060 | Subnet mask [low-order]                 | 0 | 0     |
| SD10061 | Subnet mask [high-order]                | 0 | 0     |
| SD10064 | Default gateway IP address [low-order]  | 0 | 0     |
| SD10065 | Default gateway IP address [high-order] |   | 0     |
| SD10074 | Local node MAC address                  |   | 0     |
| SD10075 | Local node MAC address                  |   | 0     |
| SD10076 | Local node MAC address                  | 0 | 0     |
| SD10082 | Communication speed setting             | 0 | 0     |
| SD10084 | MELSOFT connection TCP port No.         | 0 | 0     |
| SD10086 | MELSOFT direct connection port No.      | 0 | 0     |

#### $\Diamond$ FX compatible area

| No.    | Name                                  | FX5U | FX5UC |
|--------|---------------------------------------|------|-------|
| SD8000 | Watch dog timer                       | 0    | 0     |
| SD8001 | PLC type and system version           | 0    | 0     |
| SD8005 | Battery voltage                       | 0    | 0     |
| SD8006 | Low battery voltage                   | 0    | 0     |
| SD8007 | Power failure count                   | 0    | 0     |
| SD8008 | Power failure detection period        | 0    | 0     |
| SD8010 | Current scan time                     | 0    | 0     |
| SD8011 | Minimum scan time                     | 0    | 0     |
| SD8012 | Maximum scan time                     | 0    | 0     |
| SD8013 | RTC: Seconds                          | 0    | 0     |
| SD8014 | RTC: Minute data                      | 0    | 0     |
| SD8015 | RTC: Hour data                        | 0    | 0     |
| SD8016 | RTC: Day data                         | 0    | 0     |
| SD8017 | RTC: Month data                       | 0    | 0     |
| SD8018 | RTC: Year data                        | 0    | 0     |
| SD8019 | RTC: Day of week data                 | 0    | 0     |
| SD8039 | Constant scan duration                | 0    | 0     |
| SD8040 | ON state number 1                     | 0    | 0     |
| SD8041 | ON state number 2                     | 0    | 0     |
| SD8042 | ON state number 3                     | 0    | 0     |
| SD8043 | ON state number 4                     | 0    | 0     |
| SD8044 | ON state number 5                     | 0    | 0     |
| SD8045 | ON state number 6                     | 0    | 0     |
| SD8046 | ON state number 7                     | 0    | 0     |
| SD8047 | ON state number 8                     | 0    | 0     |
| SD8049 | Lowest active Annunciator             | 0    | 0     |
| SD8063 | Serial communication error code (ch1) | 0    | 0     |
| SD8067 | Operation error                       | 0    | 0     |

### General, power supply, input/ output specifications

#### General specifications

| ltem                               |                         | Specifications                              |   |                                   |                                     |  |
|------------------------------------|-------------------------|---|---|-----------------------------------|-------------------------------------|--|
| ILEITI                             |                         | FX5U/FX5UC                                  |   |                                   |                                     |  |
| Operating ambient<br>temperature*1 | -20 to 55°C (-4 to 131° | -20 to 55°C (-4 to 131°F), non-freezing*2*3 |   |                                   |                                     |  |
| Storage ambient temperature        | -25 to 75°C (-13 to 16  | 7°F), non-freezing                          |   |                                   |                                     |  |
| Operating ambient humidity         | 5 to 95%RH, non-con     | densation*4                                 |   |                                   |                                     |  |
| Storage ambient humidity           | 5 to 95%RH, non-con     | densation                                   |   |                                   |                                     |  |
|                                    |                         | Frequency                                   | Acceleration  | Half amplitude                    | Sweep count                         |  |
|                                    | Installed on DIN rail   | 5 to 8.4 Hz                                 | -   | 1.75 mm                           |                                     |  |
| Vibration resistance*5*6           |                         | 8.4 to 150 Hz                               | 4.9 m/s <sup>2</sup>  | -                                 | 10 times each in X, Y, Z directions |  |
|                                    | Direct installing*12    | 5 to 8.4 Hz                                 | -   | 3.5 mm                            | (80 min in each direction)          |  |
|                                    |                         | 8.4 to 150 Hz                               | 9.8 m/s <sup>2</sup>  | -                                 |                                     |  |
| Shock resistance*5                 | 147 m/s², Action time:  | 11 ms, 3 times by half-si                   | ne pulse in each direction X, `   | r, and Z                          |                                     |  |
| Noise durability                   | By noise simulator at r | noise voltage of 1000 Vp-                   | p, noise width of 1 ms and pe   | eriod of 30 to 100 Hz             |                                     |  |
| Grounding                          | Class D grounding (gro  | ounding resistance: 100 0                   | cor less) <common groundir<="" td=""><td>ng with a heavy electrical system</td><td>n is not allowed.&gt; *7</td></common> | ng with a heavy electrical system | n is not allowed.> *7               |  |
| Working atmosphere                 | Free from corrosive or  | flammable gas and exce                      | ssive conductive dust   |                                   |                                     |  |
| Operating altitude*8               | 0 to 2000 m             | 0 to 2000 m                                 |   |                                   |                                     |  |
| Installation location              | Inside a control panel* | Inside a control panel*9                    |   |                                   |                                     |  |
| Overvoltage category*10            | II or less              | II or less                                  |   |                                   |                                     |  |
| Pollution degree*11                | 2 or less               |   |   |                                   |                                     |  |

\*1: The simultaneous ON ratio of available PLC inputs or outputs changes with respect to the ambient temperature. For details, refer to manuals of each product.
\*2: 0 to 55°C for products manufactured before June 2016. For intelligent function modules, refer to the manual of each product.

The following products cannot be used when the ambient temperature is less than 0°C: FX5-40SSC-S, FX5-80SSC-S, FX5-CNV-BUS, FX5-CNV-BUSC, battery (FX3U-32BL), SD memory cards (NZ1MEM-2GBSD, NZ1MEM-4GBSD, NZ1MEM-8GBSD,

NZ1MEM-16GBSD, L1MEM-2GBSD and L1MEM-4GBSD), FX3 extension modules, terminal modules and I/O cables (FX-16E-500CAB-S, FX-16E-□CAB and FX-16E-□CAB-R) \*3: The specifications are different in the use at less than 0°C. For details, refer to the manual of each product.

\*4: When used in a low-temperature environment, use in an environment with no sudden temperature changes. If there are sudden temperature changes because of opening/closing of the control panel or other reasons, condensation may occur, which may cause a fire, fault, or malfunction. Furthermore, use an air conditioner in dehumidifier mode to prevent condensation.

\*5: The criterion is shown in IEC61131-2

\*6: When the system has equipment which specification values are lower than above mentioned vibration resistance specification values, the vibration resistance specification of the whole system is corresponding to the lower specification.

\*7: For grounding, refer to manuals of each product.
\*8: The PLC cannot be used at a pressure higher than the atmospheric pressure to avoid damage.

\*9: The programmable controller is assumed to be installed in an environment equivalent to indoor. \*10: This indicates the section of the power supply to which the equipment is assumed to be connected between the public electrical power distribution network and the machinery within premises. Category II applies to equipment for which electrical power is supplied from fixed facilities. The surge voltage withstand level for up to the rated voltage of 300 V is 2500 V

\*11: This index indicates the degree to which conductive material is generated in the environment in which the equipment is used. Pollution level 2 is when only non-conductive pollution occurs. Temporary conductivity caused by condensation must be expected occasionally. \*12: Direct installation of FX5UC is not possible.

#### Or Power supply specifications Power supply specifications (FX5U CPU module, AC power supply type)

| Item –                                     |  | Specifications   |                 |                 |  |
|--|--|--|-----------------|-----------------|--|
|  |  | FX5U-32M□/E□   | FX5U-64M□/E□    | FX5U-80M□/E□    |  |
| Rated voltage                              |  | 100 to 240 V AC  |                 |                 |  |
| Allowable supp                             | ply voltage range  | 85 to 264 V AC   |                 |                 |  |
| Voltage fluctua                            | ation range  | -  |                 |                 |  |
| Frequency rati                             | ing  | 50/60 Hz   |                 |                 |  |
| Allowable instantaneous power failure time |  | Operation can be continued upon occurrence of instantaneous power failure for 10 ms or less. If the supply voltage is 200 V AC system,<br>change in the range from 10 to 100 ms can be made by the user program. |                 |                 |  |
| Power fuse                                 |  | 250 V 3.15 A Time-lag Fuse 250 V 5 A Time-lag Fuse   |                 |                 |  |
| In-rush current                            | t  | 25 A Max. 5 ms or less/100 V AC<br>50 A Max. 5 ms or less/200 V AC   |                 |                 |  |
| Power consun                               | nption*1   | 30 W   | 40 W            | 45 W            |  |
| 5 V DC interna                             | al power supply capacity*3   | 900 mA   | 1100 mA         | 1100 mA         |  |
| 24 V DC<br>service power<br>supply*2       | Supply capacity when service power supply is used for input circuit of the CPU module*4  | 400 mA (300 mA)  | 600 mA (300 mA) | 600 mA (300 mA) |  |
|  | Supply capacity when external power supply is used for input circuit of the CPU module*4 | 480 mA (380 mA)  | 740 mA (440 mA) | 770 mA (470 mA) |  |

\*1: The values show the state where the service power of 24 V DC is consumed to the maximum level in case that its configuration has the max. no. of connections provided to CPU module. (Including the current in an input circuit) \*2: When I/O modules are connected, they consume current from the 24 V DC service power supply, resulting in decrease of usable current. For details about the service power

supply, refer to the manual. \*3: The values designate power supply capacity for an intelligent function module, expansion adapter, and expansion board.

10

Specifications

#### • Power supply specifications (FX5U CPU module, DC power supply type)

| ltem                                       | Specifications  |                                 |                    |  |  |  |
|--|---|---------------------------------|--------------------|--|--|--|
| Itein                                      | FX5U-32M□/D□  | FX5U-64M□/D□                    | FX5U-80M□/D□       |  |  |  |
| Rated voltage                              | 24 V DC   |                                 |                    |  |  |  |
| Allowable supply voltage range             | 16.8 to 28.8 V DC   | 16.8 to 28.8 V DC               |                    |  |  |  |
| Allowable instantaneous power failure time | Operation can be continued upon occurrence of instantaneous power failure for 5 ms or less. |                                 |                    |  |  |  |
| Power fuse                                 | 250 V 3.15 A Time-lag Fuse  | 250 V 5 A Time-lag Fuse         |                    |  |  |  |
| In-rush current                            | 50 A Max. 0.5 ms or less/24 V DC  | 65 A Max. 20 ms or less/24 V DC |                    |  |  |  |
| Power consumption*1                        | 30 W  | 40 W                            | 45 W               |  |  |  |
| 5 V DC internal power supply capacity*2*3  | 900 mA (775 mA)   | 1100 mA (975 mA)*2              | 1100 mA (975 mA)*2 |  |  |  |
| 24 V DC internal power supply capacity*2   | 480 mA (360 mA)   | 740 mA (530 mA)*2               | 770 mA (560 mA)*2  |  |  |  |

\*1: The values show the state where power is consumed to the maximum level in case that the configuration has the max. no. of connections provided to CPU module.
 \*2: The values in the parentheses () indicate the power supply capacity to be resulted when the power supply voltage falls in the range from 16.8 to 19.2 V DC.
 \*3: The values designate power supply capacity for an intelligent function module, expansion adapter, and expansion board.

#### Power supply specifications (FX5UC CPU module)

| Item                                       | Specifications  |                                       |  |  |  |
|--|---|---------------------------------------|--|--|--|
| liem                                       | FX5UC-32M□/□  | FX5UC-64MT/                           | FX5UC-96MT/                            |  |  |
| Rated voltage                              | 24 V DC   |                                       |  |  |  |
| Allowable supply voltage range             | +20%, -15%  |                                       |  |  |  |
| Allowable instantaneous power failure time | Operation can be continued upon occurrence of instantaneous power failure for 5 ms or less. |                                       |  |  |  |
| Power fuse                                 | 125 V 3.15 A Time-lag Fuse  |                                       |  |  |  |
| In-rush current                            | 35 A Max. 0.5 ms or less/24 V DC 40 A Max. 0.5 ms or less/24 V DC                           |                                       |  |  |  |
| Power consumption*                         | 5 W/24 V DC (30 W/24 V DC +20%, -15%)   | 8 W/24 V DC (33 W/24 V DC +20%, -15%) | 11 W/24 V DC (36 W/24 V DC +20%, -15%) |  |  |
| 5 V DC internal power supply capacity      | 720 mA  |                                       |  |  |  |
| 24 V DC internal power supply capacity     | 500 mA  |                                       |  |  |  |

\*: The value results when the CPU module is used alone.

The values in the parentheses () result when the maximum no. of connections have been made to the CPU module. (External DC 24 V power supplies of extension modules are not included.)

#### Power supply specifications (FX5-4AD-ADP)

|   | Specifications   |
|---|--|
| Internal power feed<br>(A/D conversion circuit) | 24 V DC 20 mA<br>Power is internally fed from the 24 V DC power supply of the CPU<br>module. |
| Internal power feed                             | 5 V DC 10 mA<br>Power is internally fed from the 5 V DC power supply of the CPU<br>module.   |

#### Power Supply Specifications (FX5-4AD-PT-ADP)

| Item |                                    | Specifications   |  |
|------|------------------------------------|--|--|
|      |                                    | 24 V DC 20 mA<br>Power is internally fed from 24 V DC power supply of the CPU<br>module. |  |
|      | Internal power feed<br>(interface) | 5 V DC 10 mA<br>Power is internally fed from 5 V DC power supply of the CPU<br>module.   |  |

#### Power supply specifications (FX5-4DA-ADP)

| Item  | Specifications  |
|---|---|
| External power feed<br>(D/A conversion circuit) | 24 V DC +20%, -15% 160 mA<br>Power is externally fed from the power supply connector of the<br>adapter. |
| Internal power feed<br>(interface)              | 5 V DC 10 mA<br>Power is internally fed from the 5 V DC power supply of the CPU<br>module.              |

#### Power Supply Specifications (FX5-4AD-TC-ADP)

|   | Specifications   |  |
|---|--|--|
| Internal power feed<br>(A/D conversion circuit) | 24 V DC 20 mA<br>Power is internally fed from 24 V DC power supply of the CPU<br>module. |  |
| Internal power feed<br>(interface)              | 5 V DC 10 mA<br>Power is internally fed from 5 V DC power supply of the CPU<br>module.   |  |

### Input specifications Input specifications (FX5U CPU module)

|  | cations (FX5U CP     | ,   | Specifications             |   |
|--|----------------------|---|----------------------------|---|
|  | ltem                 | FX5U-32M□   | FX5U-64M□                  | FX5U-80M  |
| No. of input points  |                      | 16 points   | 32 points                  | 40 points   |
| Connection type  |                      | Removable terminal block (M   | 3 screws)                  |   |
| Input type   |                      | Sink/source   |                            |   |
| Input signal voltage   |                      | 24 V DC +20%, -15%  |                            |   |
| Input signal current   | X0 to X17            | 5.3 mA/24 V DC  |                            |   |
| input signa ourront  | X20 and subsequent   | 4.0 mA/24 V DC  |                            |   |
| Input impedance  | X0 to X17            | 4.3 kΩ  |                            |   |
| input inpodunoo  | X20 and subsequent   | 5.6 kΩ  |                            |   |
| ON input   | X0 to X17            | 3.5 mA or more  |                            |   |
| sensitive current  | X20 and subsequent   | 3.0 mA or more  |                            |   |
| OFF input sensitivity of   | 1                    | 1.5 mA or less  |                            |   |
|  | X0 to X5             | 200 kHz   | -                          |   |
| Input response   | X0 to X7             | -   | 200 kHz                    |   |
| frequency  | X6 to X17            | 10 kHz  | -                          |   |
|  | X10 to X17           | -   | 10 kHz                     |   |
|  | Waveform             | T1 (pulse width)  |                            | T2 (rise/fall time)   |
| Pulse waveform   | X0 to X5             | T1: 2.5 μs or more,<br>T2: 1.25 μs or less  | _                          |   |
|  | X0 to X7             | -   | T1: 2.5 µs or more, T2: 1. | 25 µs or less   |
|  | X6 to X17            | T1: 50 µs or more,<br>T2: 25 µs or less   | -                          |   |
|  | X10 to X17           |   | T1: 50 µs or more, T2: 25  | us or less  |
|  |                      | ON: 2.5 µs or less,   | 11. 30 με 0ι ποιe, 12: 25  | μο υποοο  |
|  | X0 to X5             | OFF: 2.5 µs or less   | -                          |   |
|  | X0 to X7             | -   | ON: 2.5 µs or less, OFF: 2 | 2.5 µs or less  |
| Input response time<br>(H/W filter delay)                                    | X6 to X17            | ON: 30 µs or less,<br>OFF: 50 µs or less  | -                          |   |
|  | X10 to X17           | -   | ON: 30 µs or less, OFF: 5  | 50 µs or less   |
|  | X20 and subsequent   | -   | ON: 50 µs or less, OFF: 1  |   |
| Input response time<br>(Digital filter setting value)<br>Input signal format |                      | None, 10 µs, 50 µs, 0.1 ms, 0.2 ms, 0.4 ms, 0.6 ms, 1 ms, 5 ms, 10 ms (initial values), 20 ms, 70 ms<br>When using this product in an environment with much noise, set the digital filter.<br>No-voltage contact input<br>Sink: NPN open collector transistor |                            |   |
|  |                      | Source: PNP open collector t  | ransistor                  |   |
| Input circuit isolation  |                      | Photo-coupler isolation   |                            |   |
| Input operation displa   | ly                   | LED is lit when input is on   |                            |   |
| Input circuit<br>configuration   | AC power supply type | - When using 24 V DC service<br>Sink input wiring   | Sour                       | ce input wiring<br>Fuie<br>V 100 to 240 V AC<br>V 24V<br>V 100 to 240 V AC<br>V 100 to 240 V AC |
|  | DC power supply type |   |                            | ce input wiring   |

#### • Input specifications (FX5UC CPU module)

|  |                    | FX5UC-32M□/□   | Specifications<br>FX5UC-64MT/□   | FX5UC-96MT/                   |
|--|--------------------|--|--|-------------------------------|
| No. of input points  |                    | 16 points  | 32 points  | 48 points                     |
| Connection type  |                    | Connector (FX5UC-DMT/D(SS))  | 1  |                               |
|  |                    | Spring clamp terminal block (FX  | 5UC-32M□/□-TS)   |                               |
| Input type   |                    | Sink (FX5UC-DMT/D)<br>Sink/source (FX5UC-DMT/DSS, FX5UC-32MT/DS(S)-TS)   |  |                               |
| Input signal voltage   |                    | 24 V DC +20%, -15%   |  |                               |
| Input signal current X0 to X17   |                    | 5.3 mA/24 V DC   |  |                               |
|  | X20 and subsequent | 4.0 mA/24 V DC   |  |                               |
| Input impedance  | X0 to X17          | 4.3 kΩ   |  |                               |
|  | X20 and subsequent | 5.6 kΩ<br>2.5 mÅ or more   |  |                               |
| ON input sensitivity<br>current X0 to X17<br>X20 and subsequent                            |                    | 3.5 mA or more<br>3.0 mA or more   |  |                               |
| OFF input sensitivity current  |                    | 1.5 mA or less   |  |                               |
| Input response<br>frequency  | X0 to X5           | 200 kHz –  |  |                               |
|  | X0 to X7           | -  | 200 kHz  |                               |
|  | X6 to X17          | 10 kHz   | -  |                               |
|  | X10 to X17         | _  | 10 kHz   |                               |
| Pulse waveform   | Waveform           | T1 (pulse width)   | L <sup>12</sup><br>T2 (rise  | /fall time)                   |
|  | X0 to X5           | T1: 2.5 μs or more,<br>T2: 1.25 μs or less   | -  |                               |
|  | X0 to X7           | -  | T1: 2.5 µs or more, T2: 1.25 µs or less  |                               |
|  | X6 to X17          | T1: 50 μs or more,<br>T2: 25 μs or less  | -  |                               |
|  | X10 to X17         | -  | T1: 50 µs or more, T2: 25 µs or less   |                               |
| Input response time<br>(H/W filter delay)  | X0 to X5           | ON: 2.5 µs or less,<br>OFF: 2.5 µs or less   | -  |                               |
|  | X0 to X7           | -  | ON: 2.5 µs or less, OFF: 2.5 µs or less  |                               |
|  | X6 to X17          | ON: 30 µs or less,<br>OFF: 50 µs or less   | -  |                               |
|  | X10 to X17         | -  | ON: 30 µs or less, OFF: 50 µs or less  |                               |
|  | X20 and subsequent | -  | ON: 50 µs or less, OFF: 150 µs or less   |                               |
| Input response time (Digital filter setting value) Input signal format (Input sensor form) |                    | None, 10 µs, 50 µs, 0.1 ms, 0.2 ms, 0.4 ms, 0.6 ms, 1 ms, 5 ms, 10 ms (initial values), 20 ms, 70 ms         When using this product in an environment with much noise, set the digital filter.         FX5UC-□MT/D         No-voltage contact input         NPN open collector transistor         FX5UC-□MT/DSS, FX5UC-32MD/□-TS         No-voltage contact input         Sink: NPN open collector transistor |  |                               |
| Innut airquit igalation  |                    | Source: PNP open collector transistor  |  |                               |
| Input circuit isolation<br>Input operation display   |                    | Photo-coupler isolation<br>LED is lit when input is on (DISP switch: IN)   |  |                               |
| Input circuit configuration  |                    | FX5UC-□MT/D       Sink input wiring         Input       Input         Input       COM         Input       Input         Input       Input         Input       Source input wiring  |  |                               |
|  |                    | Photocoupler<br>Photocoupler   | Fuse<br>Photocoupler<br>Photocoupler<br>Photocoupler<br>Photocoupler<br>Photocoupler<br>Photocoupler<br>Photocoupler<br>Photocoupler<br>Photocoupler<br>Photocoupler | €<br>COM0<br>+<br>Fuse<br>nce |

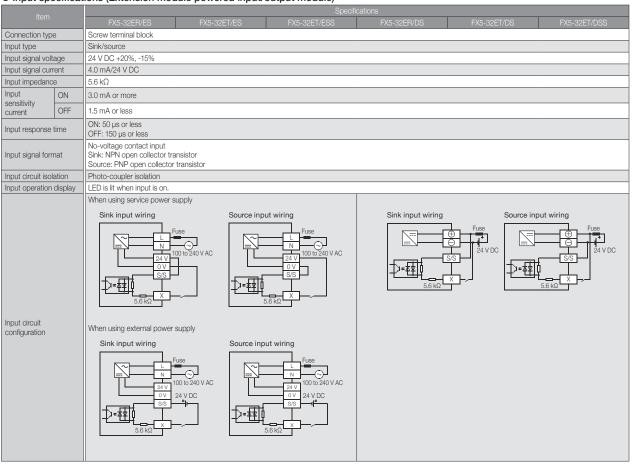
 $\star:$  Spring clamp terminal block type: The [COM0] terminal is the [S/S] terminal.

|   |        |  | Specifications  |             |                              |  |  |  |  |  |  |
|---|--------|--|---|-------------|------------------------------|--|--|--|--|--|--|
| ltem  |        | FX5-C16EX/D                              | FX5-C32EX/D   | FX5-C32ET/D | FX5-C16EX/DS                 | FX5-C32EX/DS   | FX5-C32ET/DSS  | FX5-C32EX/DS-TS,<br>FX5-C32ET/DS(S)-TS |  |  |  |
| Connection typ  | e      | Connector                                |   |             |                              |  |  | Spring clamp terminal block            |  |  |  |
| Input type  |        | Sink                                     |   |             | Sink/source                  |  |  |  |  |  |  |
| Input signal volt   | tage   | 24 V DC +20%, -159                       | 6   |             |                              |  |  |  |  |  |  |
| Input signal cur  | rent   | 4.0 mA/24 V DC                           |   |             |                              |  |  |  |  |  |  |
| Input impedance   | e      | 5.6 kΩ                                   |   |             |                              |  |  |  |  |  |  |
| Input   | ON     | 3.0 mA or more                           |   |             |                              |  |  |  |  |  |  |
| sensitivity<br>current  | OFF    | 1.5 mA or less                           |   |             |                              |  |  |  |  |  |  |
| Input response  | time   | ON: 50 µs or less<br>OFF: 150 µs or less |   |             |                              |  |  |  |  |  |  |
| Input signal format No-voltage contact input<br>Sink: NPN open collector transistor Source: PNP open collector transistor |        |  |   |             |                              |  |  |  |  |  |  |
| Input circuit iso   | lation | lation Photo-coupler isolation           |   |             |                              |  |  |  |  |  |  |
| Input operation<br>display LED is lit when<br>input is on.  |        |  | LED is lit when<br>input is on. (F/L of<br>DISP switch is used<br>to change between<br>lower and higher<br>numbers.)  |             | LED is lit when input is on. | LED is lit when<br>input is on. (F/L of<br>DISP switch is used<br>to change between<br>lower and higher<br>numbers.)   | LED is lit when<br>input is on.<br>(DISP switch: IN) | LED is lit when input is on.           |  |  |  |
| Input circuit<br>configuration  |        |  | Sink input wiring<br>Sink input wiring<br>24 V DC<br>Photocoupler<br>COM<br>24 V DC<br>Source input wiring<br>24 V DC |             |                              | Source input wiring<br>Photocoupler<br>S'S<br>A<br>Source input wiring<br>24 V DC<br>A<br>Source input wiring<br>24 V DC<br>A<br>Source input wiring<br>24 V DC<br>A<br>A<br>A<br>A<br>A<br>A<br>A<br>A<br>A<br>A<br>A<br>A<br>A |  |  |  |  |  |

#### • Input specifications (Extension module (extension connector type), input, input/output module)

## • Input specifications (Extension module (extension cable type), input, input/output module)

| Item   |         |  |   |  | Specifications |     |  |      |  |  |  |  |  |
|--|---------|--|---|--|----------------|-----|--|------|--|--|--|--|--|
| itoini   |         |  |   |  | FX5-16ET/ES    |     | FX5-16ET/ES-H  |      |  |  |  |  |  |
| Connection typ   | е       | Screw terminal block                     |   |  |                |     |  |      |  |  |  |  |  |
| Input type   |         | Sink/source                              |   |  |                |     |  |      |  |  |  |  |  |
| Input signal volt  | tage    | 24 V DC +20%, -15%                       |   |  |                |     |  |      |  |  |  |  |  |
| Input signal cur   | rent    | 4.0 mA/24 V DC                           |   |  |                |     | 5.3 mA/24 V DC   |      |  |  |  |  |  |
| Input impedance  | e       | 5.6 kΩ                                   |   |  |                |     | 4.3 kΩ   |      |  |  |  |  |  |
| Input  | ON      | 3.0 mA or more                           |   | 3.5 mA or more   |                |     |  |      |  |  |  |  |  |
| sensitivity<br>current   | OFF     | 1.5 mA or less                           | 5 mA or less  |  |                |     |  |      |  |  |  |  |  |
| Input response   | time    | ON: 50 µs or less<br>OFF: 150 µs or less | : 150 µs or less<br>ON: 30 µs or less<br>OFF: 50 µs or less |  |                |     |  |      |  |  |  |  |  |
| Input signal format No-voltage contact input Sink: NPN open collector transistor Source: PNP open collector transistor |         |  |   |  |                |     |  |      |  |  |  |  |  |
| Input circuit isol   | lation  | Photo-coupler isolation                  |   |  |                |     |  |      |  |  |  |  |  |
| Input operation of   | display | LED is lit when input is or              |   |  |                |     |  |      |  |  |  |  |  |
| Input circuit  |         |  | When using serv<br>Sink input wiring<br>CPU module          | S(S)<br>S(S)<br>S(S)<br>S(S)<br>S(S)<br>S(S)<br>S(S)<br>S(S) |                | Sir | en using external power su<br>ki input wiring<br>hotocoupler S/S<br>+ AT | / DC |  |  |  |  |  |
| Input circuit<br>configuration   |         |  | CPU module  |  |                | _   | urce input wiring<br>hotocoupler<br>S/S<br>+<br>X<br>X                   |      |  |  |  |  |  |



#### Input specifications (Extension module powered input/output module)

# ○ Output specifications

# • Relay output (FX5U CPU module)

| Connection type<br>Output type<br>External power supply | 2 A/point<br>The total load current per cc<br>4 output points/common te   | or less" if not a CE, UL, cUL               | · · · ·          |  |  |  |  |
|---|---|---|------------------|--|--|--|--|
| Connection type<br>Output type<br>External power supply | Removable terminal block (M<br>Relay<br>30 V DC or less<br>240 V AC or less ("250 V AC<br>2 A/point<br>The total load current per cc<br>- 4 output points/common te | /3 screws)<br>or less" if not a CE, UL, cUL | compliant item)  |  |  |  |  |
| Output type<br>External power supply                    | Relay<br>30 V DC or less<br>240 V AC or less ("250 V AC<br>2 A/point<br>The total load current per co<br>· 4 output points/common te                                | or less" if not a CE, UL, cUL               | · · · ·          |  |  |  |  |
| External power supply                                   | 30 V DC or less<br>240 V AC or less ("250 V AC<br>2 A/point<br>The total load current per cc<br>· 4 output points/common te   | ommon terminal should be the                | · · · ·          |  |  |  |  |
| External power supply                                   | 240 V AC or less ("250 V AC<br>2 A/point<br>The total load current per co<br>· 4 output points/common te  | ommon terminal should be the                | · · · ·          |  |  |  |  |
| Max load  | The total load current per co<br>4 output points/common te  |   |                  |  |  |  |  |
|   | <ul> <li>8 output points/common te</li> </ul>   |   | following value. |  |  |  |  |
| Min. load   | 5 V DC, 2 mA (reference valu  | ues)  |                  |  |  |  |  |
| Open circuit leakage<br>current                         | -   |   |                  |  |  |  |  |
| Response OFF→ON   | Approx. 10 ms   |   |                  |  |  |  |  |
| time ON→OFF   | Approx. 10 ms   |   |                  |  |  |  |  |
| Isolation of circuit                                    | Mechanical isolation  |   |                  |  |  |  |  |
| Indication of output operation                          | LED is lit when output is on  |   |                  |  |  |  |  |
| Output circuit<br>configuration                         | A number is entered in the D  |   |                  |  |  |  |  |

# • Relay output (FX5UC CPU module)

| 14                              | ems         | Specifications   |  |  |  |
|---------------------------------|-------------|--|--|--|--|
| IL                              |             | FX5UC-32MR/DS-TS   |  |  |  |
| No. of outp                     | out points  | 16 points  |  |  |  |
| Connection                      | n type      | Spring clamp terminal block  |  |  |  |
| Output typ                      | e           | Relay  |  |  |  |
| External po                     | ower supply | 30 V DC or less<br>240 V AC or less ("250 V AC or less" if not a<br>CE, UL, cUL compliant item)  |  |  |  |
| Max. load                       |             | 2 A/point<br>The total load current per common terminal<br>should be the following value.<br>• 8 output points/common terminal: 4 A* or less   |  |  |  |
| Min. load                       |             | 5 V DC, 2 mA (reference values)  |  |  |  |
| Open circu<br>current           | it leakage  | -  |  |  |  |
| Response                        | OFF→ON      | Approx. 10 ms  |  |  |  |
| time                            | ON→OFF      | Approx. 10 ms  |  |  |  |
| Isolation of                    | circuit     | Mechanical isolation   |  |  |  |
| Indication operation            | of output   | LED is lit when output is on   |  |  |  |
| Output circuit<br>configuration |             | Load<br>C power supply<br>Fuse<br>Load<br>AC power supply<br>Fuse<br>COMI<br>Fuse<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COMI<br>COM |  |  |  |
|                                 |             | A number is entered in the $\Box$ of [COM $\Box$ ].  |  |  |  |

\*: 8 A or less when two common terminals are connected to the external part.

## • Transistor output (FX5U CPU module)

| ltem                         |                   | Specifications   |                 |                    |  |  |  |
|------------------------------|-------------------|--|-----------------|--------------------|--|--|--|
|                              |                   | FX5U-32MT/□  | FX5U-64MT/      | FX5U-80MT/         |  |  |  |
| No. of output                | points            | 16 points  | 32 points       | 40 points          |  |  |  |
| Connection t                 | ype               | Screw terminal block   |                 |                    |  |  |  |
| Output type                  |                   | Transistor/sink output (FX5U-DMT/ES, FX5U-DMT/DS)<br>Transistor/source output (FX5U-DMT/ESS, FX5U-DMT/DSS) |                 |                    |  |  |  |
| External pow                 | er supply         | 5 to 30 V DC   |                 |                    |  |  |  |
| Max. load                    |                   | 0.5 A/point<br>The total load current per co<br>· 4 output points/common to<br>· 8 output points/common to |                 | e following value. |  |  |  |
| Open circuit I               | eakage current    | 0.1 mA or less/30 V DC   |                 |                    |  |  |  |
| Voltage drop                 | Y0 to Y3          | 1.0 V or less  |                 |                    |  |  |  |
| when ON                      | Y4 and subsequent | 1.5 V or less  |                 |                    |  |  |  |
| Response                     | Y0 to Y3          | 2.5 µs or less/10 mA or more (5 to 24 V DC)  |                 |                    |  |  |  |
| time                         | Y4 and subsequent | 0.2 ms or less/200 mA or more (24 V DC)  |                 |                    |  |  |  |
| Isolation of ci              | rcuit             | Photo-coupler isolation  |                 |                    |  |  |  |
| Indication of o              | output operation  | LED is lit when output is on   |                 |                    |  |  |  |
| Output circuit configuration |                   | Sink output wiring   | Source output v | *                  |  |  |  |

## • Transistor output (FX5UC CPU module)

| Item            |                   | Specifications  |  |                    |  |  |  |
|-----------------|-------------------|---|--|--------------------|--|--|--|
|                 | ILEITI            | FX5UC-32MT/   | FX5UC-64MT/                              | FX5UC-96MT/        |  |  |  |
| No. of output   | points            | 16 points   | 32 points                                | 48 points          |  |  |  |
| Connection t    | уре               | Connector (FX5UC-DMT/D)<br>Spring clamp terminal block  |  |                    |  |  |  |
| Output type     |                   | Transistor/sink output (FX5L<br>Transistor/source output (FX  |  |                    |  |  |  |
| External pow    | er supply         | 5 to 30 V DC  |  |                    |  |  |  |
| Max. load       |                   | Y000 to Y003: 0.3 A/1 point<br>Y004 and subsequent: 0.1 A<br>The total load current per co<br>· 8 output points/common te | V1 point<br>ommon terminal should be the | e following value. |  |  |  |
| Open circuit I  | leakage current   | 0.1 mA or less/30 V DC  |  |                    |  |  |  |
| Voltage drop    | Y0 to Y3          | 1.0 V or less   |  |                    |  |  |  |
| when ON         | Y4 and subsequent | 1.5 V or less   |  |                    |  |  |  |
| Response        | Y0 to Y3          | 2.5 µs or less/10 mA or more (5 to 24 V DC)   |  |                    |  |  |  |
| time            | Y4 and subsequent | 0.2 ms or less/100 mA (24 V DC)   |  |                    |  |  |  |
| Isolation of ci | rcuit             | Photo-coupler isolation   |  |                    |  |  |  |
| Indication of   | output operation  | LED is lit when output is on (DISP switch: OUT) (FX5UC-□MT/D(SS))<br>LED is lit when output is on (FX5UC-32MT/DS(S)-TS)   |  |                    |  |  |  |
| Output circui   | t configuration   | Sink output wiring  | Source of                                | utput wiring       |  |  |  |

 $\star$ : 1.6 A or less when two common terminals are connected outside.

## • Transistor output (sink output, extension module)

|                              |                  |                                     |  |  |                     |                     | Specifications   |                  |                                 |                 |                 |  |
|------------------------------|------------------|-------------------------------------|--|--|---------------------|---------------------|--|------------------|---------------------------------|-----------------|-----------------|--|
|                              |                  | FX5-<br>C16EYT/D                    | FX5-<br>C32EYT/D   | FX5-C32ET/D  | FX5-C32EYT/<br>D-TS | FX5-C32ET/<br>DS-TS | FX5-8EYT/<br>ES  | FX5-16EYT/<br>ES | FX5-16ET/<br>ES                 | FX5-32ET/<br>ES | FX5-32ET/<br>DS | FX5-16ET/<br>ES-H  |
| Connection                   | type             | Connector                           |  |  | Spring clamp        | terminal block      | Screw termina  | al block         |                                 |                 |                 |  |
| Output type                  |                  | Transistor out                      | put/sink output  |  |                     |                     |  |                  |                                 |                 |                 |  |
| External pov                 | wer supply       | 5 to 30 V DC                        |  |  |                     |                     |  |                  |                                 |                 |                 |  |
| Max. load                    |                  |                                     |  | nmon terminal s<br>minal: 0.8 A or I   |                     | llowing value.      | 0.5 A/1 point<br>The total load current per common terminal should be the following value.<br>- 4 output points/common terminal: 0.8 A or less<br>- 8 output points/common terminal: 1.6 A or less |                  |                                 |                 |                 |  |
| Open circuit                 | leakage current  | 0.1 mA/30 V [                       | 00   |  |                     |                     |  |                  |                                 |                 |                 |  |
| Voltage drop                 | o when ON        | 1.5 V or less                       |  |  |                     |                     |  |                  |                                 |                 |                 |  |
| Response                     | OFF-ON           | 0.2 ms or less                      | :/100 mA (at 24  | V DC)  |                     |                     | 0.2 ms or less   | /200 mA (at 24   | V DC)                           |                 |                 | Y0, Y1, Y4, Y5:<br>2.5 µs or<br>less/10 mA<br>(at 5 to 24 V DC)<br>Y2, Y3, Y6,<br>Y7:<br>0.2 ms or less/<br>200 mA<br>(at 24 V DC) |
| time                         | ON→OFF           | 0.2 ms or less                      | ;/100 mA (at 24  | V DC)  |                     |                     | 0.2 ms or less   | /200 mA (at 24   | V DC)                           |                 |                 | Y0, Y1, Y4, Y5:<br>2.5 µs or<br>less/10 mA<br>(at 5 to 24 V DC)<br>Y2, Y3, Y6, Y7:<br>0.2 ms or less/<br>200 mA<br>(at 24 V DC)    |
| Isolation of o               | circuit          | Photo-couple                        | r isolation  |  |                     |                     |  |                  |                                 |                 |                 |  |
| Isolation of a               | output operation | LED is lit<br>when output<br>is on. | LED is lit<br>when output<br>is on.<br>(F/L of DISP<br>switch is<br>used to<br>change<br>between<br>lower and<br>higher<br>numbers.) | LED is lit<br>when output<br>is on.<br>(DISP switch:<br>OUT)   |                     | n output is on.     | LED is lit wher  | n output is on.  |                                 |                 |                 |  |
| Output circuit configuration |                  |                                     | Fuse   | d<br>wer supply<br>terms to come<br>wer supply<br>terms to come<br>terms | ->-<br>->-          |                     |  |                  | Load<br>DC power supply<br>Fuse |                 |                 |  |

#### • Transistor output (source output, extension module)

|   |                  |                                     |  |  |                       |                      | Specifications   |                   |   |   |                  |   |
|---|------------------|-------------------------------------|--|--|-----------------------|----------------------|--|-------------------|---|---|------------------|---|
|   |                  | FX5-C16EYT/<br>DSS                  | FX5-C32EYT/<br>DSS   | FX5-C32ET/<br>DSS  | FX5-C32EYT/<br>DSS-TS | FX5-C32ET/<br>DSS-TS | FX5-8EYT/<br>ESS   | FX5-16EYT/<br>FSS | FX5-16ET/<br>ESS  | FX5-32ET/<br>ESS  | FX5-32ET/<br>DSS | FX5-16ET/<br>ESS-H  |
| Connection                                  | type             | Connector                           | 000  | 000  |                       | terminal block       | Screw termina  |                   | LOO   | LOO   | DOO              | 200 11  |
| Output type                                 |                  |                                     | put/sink output  |  |                       |                      |  |                   |   |   |                  |   |
| External pov                                | wer supply       | 5 to 30 V DC                        |  |  |                       |                      |  |                   |   |   |                  |   |
| Max. load                                   |                  |                                     | current per cor<br>hts/common ter  |  |                       | llowing value.       | 0.5 A/1 point<br>The total load current per common terminal should be the following value.<br>• 4 output points/common terminal: 0.8 A or less<br>• 8 output points/common terminal: 1.6 A or less |                   |   |   |                  |   |
| Open circuit leakage current 0.1 mA/30 V DC |                  |                                     |  |  |                       |                      |  |                   |   |   |                  |   |
| Voltage drop                                | o when ON        | 1.5 V or less                       |  |  |                       |                      |  |                   |   |   |                  |   |
| Response                                    | OFF→ON           | 0.2 ms or less/100 mA (at 24 V DC)  |  |  |                       |                      | 0.2 ms or less/200 mA (at 24 V DC)   |                   |   | Y0, Y1, Y4, Y5:<br>2.5 µs or<br>less/10 mA<br>(at 5 to 24 V DC)<br>Y2, Y3, Y6, Y7:<br>0.2 ms or less/<br>200 mA<br>(at 24 V DC) |                  |   |
| time  | ON→OFF           | 0.2 ms or less                      | /100 mA (at 24   | V DC)  |                       |                      | 0.2 ms or less   | /200 mA (at 24 '  | V DC)   |   |                  | Y0, Y1, Y4, Y5:<br>2.5 µs or<br>less/10 mA<br>(at 5 to 24 V DC)<br>Y2, Y3, Y6, Y7:<br>0.2 ms or less/<br>200 mA<br>(at 24 V DC) |
| Isolation of d                              | circuit          | Photo-couple                        | r isolation  |  |                       |                      |  |                   |   |   |                  |   |
| Indication of                               | output operation | LED is lit<br>when output<br>is on. | LED is lit<br>when output<br>is on.<br>(F/L of DISP<br>switch is<br>used to<br>change<br>between<br>lower and<br>higher<br>numbers.) | LED is lit<br>when output<br>is on.<br>(DISP switch:<br>OUT) | LED is lit whe        | n output is on.      | LED is lit when  | n output is on.   |   |   |                  |   |
| Output circuit configuration                |                  |                                     | Loc por<br>Fuse  |  | }-<br>}-              |                      |  |                   | Load<br>DC power supply<br>Fuse<br>DC power supply<br>DC power supply<br>Fuse |   |                  |   |

#### Relay output (extension module)

|   | Item               |  |              | Specifications   |             |             |  |  |  |
|---|--------------------|--|--------------|--|-------------|-------------|--|--|--|
|   |                    | FX5-8EYR/ES  | FX5-16EYR/ES | FX5-16ER/ES  | FX5-32ER/ES | FX5-32ER/DS | FX5-C16EYR/D-TS  |  |  |
| Connection type Screw terminal block Spring clamp te  |                    |  |              |  |             |             | Spring clamp terminal block  |  |  |
| Output type   | type Relay         |  |              |  |             |             |  |  |  |
| External power supply<br>240 V AC or less<br>("250 V AC or less" if not a CE, UL, cUL compliant item) |                    |  |              |  |             |             |  |  |  |
| Max. load   |                    | 2 A/1 point<br>The total load current pe<br>• 4 output points/commo<br>• 8 output points/commo |              | be the following value.  |             |             | 2 A/1 point<br>The total load current per common<br>terminal should be the following<br>value.<br>- 8 output points/common terminal:<br>4 A or less*   |  |  |
| Min. load   |                    | 5 V DC, 2 mA (reference  | values)      |  |             |             |  |  |  |
| Response  | OFF→ON             | Approx. 10 ms  |              |  |             |             |  |  |  |
| time  | ON→OFF             | Approx. 10 ms  |              |  |             |             |  |  |  |
| Isolation of  | circuit            | Mechanical isolation   |              |  |             |             |  |  |  |
| Indication o  | f output operation | LED is lit when output is  | on.          |  |             |             |  |  |  |
| Output circuit configuration  |                    |  |              | Load<br>C power supply<br>C power supply<br>C cond<br>C power supply<br>C power supply |             |             | Load<br>DC pover supply<br>Fuse<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>Fuse<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>COMO<br>C |  |  |

#### Built-in analog input

|                                  | lkows                                       | Specifications   |  |  |  |  |
|----------------------------------|---|--|--|--|--|--|
|                                  |   | FX5U CPU module  |  |  |  |  |
| Analog input points              |   | 2 points (2 channels)  |  |  |  |  |
| Analog input                     | Voltage                                     | 0 to 10 V DC (input resistance 115.7 kΩ)   |  |  |  |  |
| Digital output                   |   | Unsigned 12-bit binary   |  |  |  |  |
| Device allocation                |   | SD6020 (Input data of ch1)<br>SD6060 (Input data of ch2)   |  |  |  |  |
| Input characteristics,           | Digital output value                        | 0 to 4000  |  |  |  |  |
| maximum resolution               | Maximum resolution                          | 2.5 mV   |  |  |  |  |
| Precision                        | Ambient temperature 25±5°C (77±41°F)        | Within ±0.5% (±20 digit*²)   |  |  |  |  |
| (Accuracy in respect to          | Ambient temperature 0 to 55°C (32±131°F)    | Within ±1.0% (±40 digit*2)   |  |  |  |  |
| full-scale digital output value) | Ambient temperature -20 to 0°C (32±131°F)*1 | Within ±1.5% (±60 digit*2)   |  |  |  |  |
| Conversion speed                 |   | 30 µs/channels (data refreshed every operation cycle)  |  |  |  |  |
| Absolute maximum input           |   | -0.5 V, +15 V  |  |  |  |  |
| Isolation                        |   | No isolation from the CPU module internal circuit, no isolation between the input terminals (channels) |  |  |  |  |
| Number of occupied input/ou      | Itput points                                | 0 points (No concern with the maximum no. of input/output points of the CPU module)                    |  |  |  |  |
| Terminal block used              |   | European-type terminal block   |  |  |  |  |

\*1: Products manufactured earlier than June 2016 do not support this specification.
 \*2: The term "digit" refers to "digital value".

#### Built-in analog output

|                                 | ltem  | Specifications  |  |  |  |  |
|---------------------------------|---|---|--|--|--|--|
|                                 |   | FX5U CPU module   |  |  |  |  |
| Analog output points            |   | 1 point (1 channel)   |  |  |  |  |
| Digital input                   |   | Unsigned 12-bit binary  |  |  |  |  |
| Analog output                   | Voltage                                     | 0 to 10 V DC (external load resistance 2 k $\Omega$ to 1 M $\Omega$ )               |  |  |  |  |
| Device allocation               |   | SD6180 (Output setting data of ch1)   |  |  |  |  |
| Output characteristics,         | Digital input value                         | 0 to 4000   |  |  |  |  |
| maximum resolution*1            | Maximum resolution                          | 2.5 mV  |  |  |  |  |
| Accuracy*2                      | Ambient temperature 25±5°C (77±41°F)        | Within ±0.5% (±20 digit*4)  |  |  |  |  |
| (Accuracy in respect to         | Ambient temperature 0 to 55°C (32±131°F)    | Within ±1.0% (±40 digit*4)  |  |  |  |  |
| full-scale analog output value) | Ambient temperature -20 to 0°C (32±131°F)*3 | Within ±1.5% (±60 digit*4)  |  |  |  |  |
| Conversion speed                |   | 30 µs (data refreshed every operation cycle)  |  |  |  |  |
| Isolation                       |   | No isolation from the CPU module internal circuit                                   |  |  |  |  |
| Number of occupied input/ou     | tput points                                 | 0 points (No concern with the maximum no. of input/output points of the CPU module) |  |  |  |  |
| Terminal block used             |   | European-type terminal block  |  |  |  |  |

\*1: There is a dead band near 0 V output, which is an area where some analog output values do not reflect digital input values.
\*2: External load resistance is set to 2 kΩ when shipped from the factory. Thus, output voltage will increase somewhat if the resistance is set higher than 2 kΩ. When the resistance is 1 MΩ, output voltage increases maximum 2%.
\*3: Products manufactured earlier than June 2016 do not support this specification.
\*4: The term "digit" refers to "digital value".

#### Built-in RS-485 communication

| ltem                          | Specifications   |  |  |  |  |  |
|-------------------------------|--|--|--|--|--|--|
| item                          | FX5U / FX5UC CPU module  |  |  |  |  |  |
| Transmission standards        | Conforms to RS-485/RS-422 specifications   |  |  |  |  |  |
| Data transmission speed       | Max. 115.2 kbps  |  |  |  |  |  |
| Communication method          | ull-duplex (FDX) / Half-duplex (HDX)   |  |  |  |  |  |
| Maximum transmission distance | 50 m   |  |  |  |  |  |
| Protocol type                 | MELSOFT connection, MC protocol (1C/3C/4C frames), non-protocol communication, MODBUS RTU<br>communication, inverter communication, N:N network, parallel link, communication protocol support |  |  |  |  |  |
| Isolation of circuit          | Not isolated   |  |  |  |  |  |
| Terminal resistors            | Built-in (OPEN/110 Ω/330 Ω)  |  |  |  |  |  |
| Terminal block used           | European-type terminal block   |  |  |  |  |  |

#### Built-in Ethernet communication

| Item                            |                                      | Specifications   |  |  |
|---------------------------------|--------------------------------------|--|--|--|
|                                 |                                      | FX5U / FX5UC CPU module  |  |  |
| Data transmiss                  | sion speed                           | 100/10 Mbps  |  |  |
| Communicatio                    | n method                             | Full-duplex (FDX) / Half-duplex (HDX)*1  |  |  |
| Interface                       | ·                                    | RJ45 connector   |  |  |
| Transmission r                  | nethod                               | Base band  |  |  |
| Maximum segr<br>(The distance I | nent length<br>between hub and node) | 100 m  |  |  |
| Cascade 100BASE-TX              |                                      | Cascade connection max. 2 stages*2   |  |  |
| connection                      | 10BASE-T                             | Cascade connection max. 4 stages*2   |  |  |
| Protocol type                   |                                      | CC-Link IE Field Network Basic, MELSOFT connection, SLMP (3E frame), socket communication,<br>communication protocol support, FTP server, MODBUS/TCP communication, SNTP client, Web<br>server (HTTP), simple CPU communication function |  |  |
| Number of connections           |                                      | Total 8 connections <sup>43,84</sup><br>(Up to 8 external devices can access one CPU module at the same time.)   |  |  |
| Hub*1                           |                                      | Hubs with 100BASE-TX or 10BASE-T ports are available.  |  |  |
| IP address*5                    |                                      | Initial value: 192.168.3.250   |  |  |
| Isolation of circuit            |                                      | Pulse transformer isolation  |  |  |
| Cable used*6                    | For 100BASE-TX connection            | Ethernet standard-compatible cable, category 5 or higher (STP cable)   |  |  |
| Cable used*6                    | For 10BASE-T connection              | Ethernet standard-compatible cable, category 3 or higher (STP cable)   |  |  |

\*1: IEEE802.3x flow control is not supported.
\*2: Number of stages that can be connected when a repeater hub is used. When a switching hub is used, check the specifications of the switching hub used.
\*3: One device connected to MELSOFT is not included in the number of connections. (The second and subsequent devices are included.)
\*4: The CC-Link IE Field Network Basic, FTP server, SNTP client, Web server and simple CPU communication function are not included in the number of connections.
\*5: If the 1st octet is 0 or 127, a parameter error (2222H) will result. (Example: 0.0.0, 127.0.0.0 etc.)
\*6: A straight cable can be used. If a personal computer or GOT and CPU module are directly connected a cross cable can be used.

#### Built-in positioning function

| Item                     | Specifications  |  |  |
|--------------------------|---|--|--|
| Item                     | FX5U / FX5UC CPU module   |  |  |
| Number of control axes   | 4 axes* (Simple linear interpolation by 2-axis simultaneous start)  |  |  |
| Maximum frequency        | 2147483647 (200 kpps in pulses)   |  |  |
| Positioning program      | Sequence program, Table operation   |  |  |
| Pulse output instruction | PLSY and DPLSY instructions   |  |  |
| Positioning instruction  | DSZR, DDSZR, DVIT, DDVIT, TBL, DRVTBL, DRVMUL, DABS, PLSV, DPLSV, DRVI, DDRVI, DRVA, and DDRVA instructions |  |  |

 $\star$ : The number of control axes is 2 when the pulse output mode is CW/CCW mode.

#### Built-in high speed counter function

| Item                           | Specifications   |                   |  |  |  |
|--------------------------------|--|-------------------|--|--|--|
| Item                           | FX5U / FX5UC CPU module  |                   |  |  |  |
|                                | Input specifications   | Maximum frequency |  |  |  |
|                                | 1 phase, 1 input counter (S/W)   | 200 kHz           |  |  |  |
|                                | 1 phase, 1 input counter (H/W)   | 200 kHz           |  |  |  |
| Types of high-speed counters   | 1 phase, 2 input counter   | 200 kHz           |  |  |  |
|                                | 2 phase, 2 input counter [1 edge count]  | 200 kHz           |  |  |  |
|                                | 2 phase, 2 input counter [2 edge count]  | 100 kHz           |  |  |  |
|                                | 2 phase, 2 input counter [4 edge count]  | 50 kHz            |  |  |  |
| Input allocation               | Parameter setup*   |                   |  |  |  |
| High-speed counter instruction | [High-speed processing instruction]<br>- Setting 32-bit data comparison (DHSCS)<br>- Resetting 32-bit data comparison (DHSCR)<br>- Comparison of 32-bit data band (DHSC)<br>- Start/stop of the 16-bit data high-speed I/O fur<br>- Start/stop of the 32-bit data high-speed I/O fur<br>(High-speed transfer instruction of current value]<br>- High-speed current value transfer of 16-bit data<br>- High-speed current value transfer of 32-bit data | a (HCMOV)         |  |  |  |

\*: For details, refer to manuals of each product.

# Extension Device Specifications I/O Modules

# • Powered input/output modules

| Model        | Total No. |                                | Connection                    |           |                     |                |
|--------------|-----------|--------------------------------|-------------------------------|-----------|---------------------|----------------|
| IVIOUEI      | of points |                                |                               |           |                     | type           |
| FX5-32ER/ES  |           |                                |                               |           | Relay               |                |
| FX5-32ET/ES  |           | 16 points 24 V DC (Sink/source |                               | 10        | Transistor (Sink)   | Screw terminal |
| FX5-32ET/ESS |           |                                | 24 V DC (Sink/source) 16 poin |           | Transistor (Source) |                |
| FX5-32ER/DS  | 32 points |                                |                               | 16 points | Relay               | block          |
| FX5-32ET/DS  |           |                                |                               |           | Transistor (Sink)   | ]              |
| FX5-32ET/DSS |           |                                |                               |           | Transistor (Source) | 1              |

#### Input module

| Model           | Total No. |           | No. of input/output points & Input/output type |    |  |                                |  |
|-----------------|-----------|-----------|--|----|--|--------------------------------|--|
| WOUEI           | of points |           |  |    |  | type                           |  |
| FX5-8EX/ES      | 8 points  | 8 points  | 24 V DC (Sink/source)                          |    |  | Screw terminal                 |  |
| FX5-16EX/ES     |           |           | - 24 V DC (SINK/SOURCE)                        |    |  | block                          |  |
| FX5-C16EX/D     | 16 points | 16 points | 24 V DC (Sink)                                 |    |  |                                |  |
| FX5-C16EX/DS    |           |           | 24 V DC (Sink/source)                          | ٦_ |  | Connector                      |  |
| FX5-C32EX/D     |           |           | 24 V DC (Sink)                                 |    |  | Connector                      |  |
| FX5-C32EX/DS    | 32 points | 32 points |  |    |  |                                |  |
| FX5-C32EX/DS-TS |           | 02 points | 24 V DC (Sink/source)                          |    |  | Spring clamp<br>terminal block |  |

#### • Output module

| Model             | Total No. |  | No. of input/output pc | ints & Input/     | output type         | Connection                     |
|-------------------|-----------|--|------------------------|-------------------|---------------------|--------------------------------|
| MOUEI             | of points |  |                        |                   | Output              | type                           |
| FX5-8EYR/ES       |           |  |                        |                   | Relay               |                                |
| FX5-8EYT/ES       | 8 points  |  |                        | 8 points          | Transistor (Sink)   |                                |
| FX5-8EYT/ESS      |           |  |                        |                   | Transistor (Source) | Screw terminal                 |
| FX5-16EYR/ES      |           |  |                        |                   | Relay               | block                          |
| FX5-16EYT/ES      |           |  |                        | Transistor (Sink) |                     |                                |
| FX5-16EYT/ESS     |           |  | _                      | 16 points         | Transistor (Source) |                                |
| FX5-C16EYT/D      | 16 points |  |                        |                   | Transistor (Sink)   | Connector                      |
| FX5-C16EYT/DSS    |           |  |                        |                   | Transistor (Source) | Connector                      |
| FX5-C16EYR/D-TS   |           |  |                        |                   | Relay               | Spring clamp<br>terminal block |
| FX5-C32EYT/D      |           |  |                        |                   | Connector           |                                |
| FX5-C32EYT/D-TS   | 32 points |  |                        |                   | Transistor (Sink)   | Spring clamp<br>terminal block |
| FX5-C32EYT/DSS    |           |  |                        | 32 points         |                     | Connector                      |
| FX5-C32EYT/DSS-TS |           |  |                        |                   | Transistor (Source) | Spring clamp<br>terminal block |

## • I/O module

| Model            | Total No. |           | No. of input/output points & Input/output type |           |                     |                                |  |
|------------------|-----------|-----------|--|-----------|---------------------|--------------------------------|--|
| IVIOUEI          | of points |           |  |           |                     | type                           |  |
| FX5-16ER/ES      |           |           |  |           | Relay               |                                |  |
| FX5-16ET/ES      | 16 points | 8 points  | 24 V DC (Sink/source)                          | 8 points  | Transistor (Sink)   | Screw terminal<br>block        |  |
| FX5-16ET/ESS     |           |           |  |           | Transistor (Source) |                                |  |
| FX5-C32ET/D      |           |           | 24 V DC (Sink)                                 | 16 points |                     | Connector                      |  |
| FX5-C32ET/DS-TS  | 00 painta |           |  |           | Transistor (Sink)   | Spring clamp<br>terminal block |  |
| FX5-C32ET/DSS    | 32 points | 16 points | 24 V DC (Sink/source)                          |           | Transistor (Source) | Connector                      |  |
| FX5-C32ET/DSS-TS |           |           |  |           |                     | Spring clamp<br>terminal block |  |

#### • High-speed pulse input/output module

| Model           |           |          | Connection            |          |                     |                |
|-----------------|-----------|----------|-----------------------|----------|---------------------|----------------|
| WOUEI           |           | Input    |                       | Output   |                     | type           |
| FX5-16ET/ES-H*  | 1C nointe | 0 pointo |                       | 0 pointo | Transistor (Sink)   | Screw terminal |
| FX5-16ET/ESS-H* | 16 points | 8 points | 24 V DC (Sink/source) | 8 points | Transistor (Source) | block          |

\*: Supported by FX5U/FX5UC CPU modules Ver. 1.030 or later.

# ○ Expansion adapter

| FX5-232ADP  | FX5-232ADP   |  |  |  |  |  |
|---|--|--|--|--|--|--|
| Item  | Specifications   |  |  |  |  |  |
| Transmission standard/<br>Maximum transmission distance/Isolation | Conforming to RS-232C/15 m/Photo-coupler isolation (Between communication line and CPU module)   |  |  |  |  |  |
| External device connection method                                 | 9-pin D-sub, male  |  |  |  |  |  |
| Communication method  | Half-duplex bidirectional/Full-duplex bidirectional  |  |  |  |  |  |
| Protocol type   | MELSOFT connection, MC protocol (1C/3C/4C frame), non-protocol communication, MODBUS RTU communication,<br>predefined protocol support |  |  |  |  |  |
| Baud rate   | 300/600/1200/2400/4800/9600/19200/38400/57600/115200 (bps)*  |  |  |  |  |  |
| Compatible CPU module   | FX5U, FX5UC  |  |  |  |  |  |
| Number of occupied input/output points                            | 0 points (no points occupied)  |  |  |  |  |  |
| Control power (supplied from CPU module)                          | 5 V DC, 30 mA /24 V DC, 30 mA  |  |  |  |  |  |

 $\star$ : The communication method and baud rate vary depending on the type of communication.

## • FX5-485ADP

| Item  | Specifications   |
|---|--|
| Transmission standard/<br>Maximum transmission distance/Isolation | Conforming to RS-485, RS-422/1200 m/Photo-coupler isolation (Between communication line and CPU module)  |
| External device connection method                                 | European-type terminal block   |
| Communication method  | Half-duplex bidirectional/Full-duplex bidirectional  |
| Protocol type   | MELSOFT connection, MC protocol (1C/3C/4C frame), non-protocol communication, MODBUS RTU communication,<br>inverter communication, N:N network, parallel link, predefined protocol support |
| Baud rate   | 300/600/1200/2400/4800/9600/19200/38400/57600/115200 (bps)*  |
| Terminal resistors  | Built-in (OPEN/110 Ω/330 Ω)  |
| Compatible CPU module   | FX5U, FX5UC  |
| Number of occupied input/output points                            | 0 points (no points occupied)  |
| Control power (supplied from CPU module)                          | 5 V DC, 20 mA /24 V DC, 30 mA  |

 $\star$ : The communication method and baud rate vary depending on the type of communication.

#### • FX5-4AD-ADP

| Item   | Specifications  |                                 |                         |            |  |  |
|--|---|---------------------------------|-------------------------|------------|--|--|
| Analog input points  | 4 points (4 channels)   |                                 |                         |            |  |  |
| External device connection method                                    | European-t  | ype terminal block              |                         |            |  |  |
| Analog input voltage   | -10 to +10  | V DC (input resistance 1 MΩ)    |                         |            |  |  |
| Analog input current   | -20 to +20  | mA DC (input resistance 250 Ω)  |                         |            |  |  |
| Digital output value   | 14-bit binar  | y value                         |                         |            |  |  |
|  |   | Analog input range              | Digital output value    | Resolution |  |  |
|  |   | 0 to 10 V                       | 0 to 16000              | 625 μV     |  |  |
|  |   | 0 to 5 V                        | 0 to 16000              | 312.5 μV   |  |  |
| hannak ala sana da da Kana yana da Kana Wi                           | Voltage   | 1 to 5 V                        | 0 to 12800              | 312.5 μV   |  |  |
| Input characteristics, resolution*1                                  |   | -10 to +10 V                    | -8000 to +8000          | 1250 µV    |  |  |
|  |   | 0 to 20 mA                      | 0 to 16000              | 1.25 µA    |  |  |
|  | Current   | 4 to 20 mA                      | 0 to 12800              | 1.25 µA    |  |  |
|  |   | -20 to +20 mA                   | -8000 to +8000          | 2.5 µA     |  |  |
| Accuracy<br>(Accuracy in respect to full-scale digital output value) | Ambient temperature 25±5°C: within ±0.1% (±16 digit)<br>Ambient temperature 0 to 55°C: within ±0.2% (±32 digit)<br>Ambient temperature -20 to 0°C*2: within ±0.3% (±48 digit) |                                 |                         |            |  |  |
| Absolute maximum input   | Voltage: ±1   | 5 V, Current: ±30 mA            |                         |            |  |  |
| Isolation  | Between input terminal and PLC: Photocoupler isolation<br>Between input terminal channels: Non-isolation  |                                 |                         |            |  |  |
| Power supply   | 24 V DC, 20 mA (internal power supply)<br>5 V DC, 10 mA (internal power supply)   |                                 |                         |            |  |  |
| Compatible CPU module  | Compatible  | with FX5U and FX5UC, from their | first released products |            |  |  |
| Number of occupied input/output points                               | 0 points (no  | points occupied)                |                         |            |  |  |

\*1: For the input conversion characteristic, refer to manuals of each product.
\*2: Products manufactured earlier than June 2016 do not support this specification.

#### • FX5-4AD-PT-ADP

|                     | ltem                      |          | Specifications  |  |  |  |  |
|---------------------|---------------------------|----------|---|--|--|--|--|
| Analog input points |                           |          | 4 points (4 channels)   |  |  |  |  |
| Extern<br>metho     | al device connec<br>d     | ction    | European-type terminal block  |  |  |  |  |
| Usable<br>detect    | e resistance temp<br>or*1 | perature | Pt100<br>Ni100 (DIN 43760 1987)   |  |  |  |  |
| Tempe               | rature                    | Pt100    | -200 to 850°C (-328 to 1562°F)  |  |  |  |  |
| measu               | ring range                | Ni100    | -60 to 250°C (-76 to 482°F)   |  |  |  |  |
|                     |                           |          | 16-bit signed binary value  |  |  |  |  |
| Digital             | output value              | Pt100    | -2000 to 8500 (-3280 to 1562)   |  |  |  |  |
|                     |                           | Ni100    | -600 to 2500 (760 to 4820)  |  |  |  |  |
|                     | Ambient                   | Pt100    | ±0.8°C  |  |  |  |  |
| acy                 | temperature<br>25±5°C     | Ni100    | ±0.4°C  |  |  |  |  |
| Accuracy            | Ambient<br>temperature    | Pt100    | ±2.4°C  |  |  |  |  |
|                     | -20 to 55°C               | Ni100    | ±1.2°C  |  |  |  |  |
| Resolu              | ition                     |          | 0.1°C (0.1 to 0.2°F)  |  |  |  |  |
| Conve               | rsion speed*2             |          | About 85 ms/channel   |  |  |  |  |
| Isolation           |                           |          | Between input terminal and CPU module: Photocoupler isolation<br>Between input terminal channels: Non-isolation |  |  |  |  |
| Power supply        |                           |          | 24 V DC, 20 mA (internal power supply)<br>5 V DC, 10 mA (internal power supply)                                 |  |  |  |  |
| Compa               | atible CPU modu           | ıle      | FX5U, FX5UC: Ver. 1.040 or later  |  |  |  |  |
| Numbe               | er of occupied I/         | O points | 0 point (no occupied points)  |  |  |  |  |

\*1: Only 3-wire type resistance temperature detectors can be used.
\*2: For details of conversion speeds, refer to the manual.

#### • FX5-4AD-TC-ADP

| Item                                 |                               |          | Specifications  |                           |  |  |
|--------------------------------------|-------------------------------|----------|---|---------------------------|--|--|
| Analog input points                  |                               |          | 4 points (4 channels)   |                           |  |  |
| External device connection<br>method |                               | tion     | European-type terminal block  |                           |  |  |
| Usable                               | thermocouple                  |          | K, J , T, B, R, S   |                           |  |  |
|                                      |                               | K        | -200 to 1200°C (-328 to 2192°F)   |                           |  |  |
|                                      |                               |          | -40 to 750°C (-40 to 1382°F)  |                           |  |  |
| Temper                               | rature                        | Т        | -200 to 350°C (-328 to 662°F)   |                           |  |  |
| measu                                | measuring range               |          | 600 to 1700°C (1112 to 3092°F)  |                           |  |  |
|                                      |                               |          | 0 to 1600°C (32 to 2912°F)  |                           |  |  |
|                                      |                               | S        | 0 to 1600°C (32 to 2912°F)  |                           |  |  |
|                                      |                               |          | 16-bit signed binary value  |                           |  |  |
|                                      |                               | K        | -2000 to 12000 (-3280 to 21920)   |                           |  |  |
|                                      |                               | J        | -400 to 7500 (-400 to 13820)  |                           |  |  |
| Digital                              | output value                  | Т        | -2000 to 3500 (-3280 to 6620)   |                           |  |  |
|                                      |                               | В        | 6000 to 17000 (11120 to 30920)  |                           |  |  |
|                                      |                               | R        | 0 to 16000 (320 to 29120)   |                           |  |  |
|                                      |                               | S        | 0 to 16000 (320 to 29120)   |                           |  |  |
|                                      |                               | к        | ±3.7°C (-100 to 1200°C)*2   | ±4.9°C (-150 to -100°C)*2 |  |  |
|                                      |                               | r.       | ±7.2°C (-200 to -150°C)*2   |                           |  |  |
|                                      |                               | J        | ±2.8°C  |                           |  |  |
|                                      | Ambient<br>temperature        | T        | ±3.1°C (0 to 350°C)*2   | ±4.1°C (-100 to 0°C)*2    |  |  |
|                                      | 25±5°C                        |          | ±5.0°C (-150 to -100°C)*2   | ±6.7°C (-200 to -150°C)*2 |  |  |
|                                      |                               | В        | ±3.5°C  |                           |  |  |
| *1                                   |                               | R        | ±3.7°C  |                           |  |  |
| Accuracy*1                           |                               | S        | ±3.7°C  |                           |  |  |
| noc                                  |                               | к        | ±6.5°C (-100 to 1200°C)*2   | ±7.5°C (-150 to -100°C)*2 |  |  |
| Ā                                    |                               |          | ±8.5°C (-200 to -150°C)*2   |                           |  |  |
|                                      | Ambient                       | J        | ±4.5°C  |                           |  |  |
|                                      | temperature                   | Т        | ±4.1°C (0 to 350°C)*2   | ±5.1°C (-100 to 0°C)*2    |  |  |
|                                      | -20 to 55°C                   | <u> </u> | ±6.0°C (-150 to -100°C)*2   | ±7.7°C (-200 to -150°C)*2 |  |  |
|                                      |                               | В        | ±6.5°C  |                           |  |  |
|                                      |                               | R        | ±6.5°C  |                           |  |  |
|                                      |                               | S        | ±6.5°C  |                           |  |  |
| Resolut                              | Resolution K, J, T<br>B, R, S |          | 0.1°C (0.1 to 0.2°F)  |                           |  |  |
|                                      |                               |          | 0.1 to 0.3°C (0.1 to 0.6°F)   |                           |  |  |
| Conversion speed*3                   |                               |          | About 85 ms/channel   |                           |  |  |
| Isolation                            |                               |          | Between input terminal and CPU module: Photocoupler isolation<br>Between input terminal channels: Non-isolation |                           |  |  |
| Power supply                         |                               |          | 24 V DC, 20 mA (internal power supply<br>5 V DC, 10 mA (internal power supply)                                  | )                         |  |  |
| Compatible CPU module                |                               |          | FX5U, FX5UC: Ver. 1.040 or later  |                           |  |  |
| Numbe                                | er of occupied I/             | ) points | 0 point (no occupied points)  |                           |  |  |

\*1: Obtaining sufficient accuracy requires a warm-up of 45 minutes (energization).
\*2: Accuracy varies depending on the measured temperature range in ().
\*3: For details of conversion speeds, refer to the manual.

#### FX5-4DA-ADP

|  | Specifications  |  |                     |            |  |
|--|---|--|---------------------|------------|--|
| Analog output points   | 4 points (4   | 4 points (4 channels)  |                     |            |  |
| External device connection method                                      | European-   | type terminal block  |                     |            |  |
| Analog output voltage  | -10 to +10  | V DC (external load resistance value 1 kΩ                          | to 1 MΩ)            |            |  |
| Analog output current  | 0 to 20 m/  | A DC (external load resistance value 0 to 5                        | Ω 00                |            |  |
| Digital input  | 14-bit bina   | ry value   |                     |            |  |
|  |   | Analog output range  | Digital input value | Resolution |  |
|  |   | 0 to 10 V  | 0 to 16000          | 625 μV     |  |
|  | Valtage   | 0 to 5 V   | 0 to 16000          | 312.5 µV   |  |
| Output characteristics, resolution*1                                   | Voltage   | 1 to 5 V   | 0 to 16000          | 250 μV     |  |
|  |   | -10 to +10 V   | -8000 to +8000      | 1250 µV    |  |
|  | Current   | 0 to 20 mA   | 0 to 16000          | 1.25 µA    |  |
|  |   | 4 to 20 mA   | 0 to 16000          | 1 µA       |  |
| Accuracy<br>(Accuracy in respect to full-scale analog output<br>value) | Ambient temperature 25±5°C: within ±0.1% (Voltage ±20 mV, Current ±20 μA)<br>Ambient temperature -20 to 55°C*2: within ±0.2% (Voltage ±40 mV, Current ±40 μA) |  |                     |            |  |
| Isolation  | Between output terminal and PLC: Photocoupler isolation<br>Between output terminal channels: Non-isolation  |  |                     |            |  |
| Power supply   | 24 V DC +20%, -15% 160 mA (external power supply)<br>5 V DC, 10 mA (internal power supply)  |  |                     |            |  |
| Compatible CPU module  | Compatib  | Compatible with FX5U and FX5UC, from their first released products |                     |            |  |
| Number of occupied input/output points 0 points (no points occupied)   |   |  |                     |            |  |

\*1: For details on the output conversion characteristic, refer to manuals of each product.
 \*2: The ambient temperature specification is 0 to 55°C for products manufactured earlier than June 2016.

# $\diamond$ Expansion board

| Item                                   | Specifications  |  |   |  |  |
|--|---|--|---|--|--|
| item                                   | FX5-232-BD  | FX5-485-BD   | FX5-422-BD-GOT                                      |  |  |
| Transmission standards                 | Conforming to RS-232C   | Conforming to RS-485, RS-422   | Conforming to RS-422                                |  |  |
| Maximum transmission distance          | 15 m  | 50 m   | According to the specification of the GOT           |  |  |
| External device connection method      | 9-pin D-sub, male   | European-type terminal block   | 8-pin MINI-DIN, female                              |  |  |
| Isolation                              | Non-insulation (between communication line and CPU)   | Non-insulation (between communication line and CPU)  | Non-insulation (between communication line and CPU) |  |  |
| Communication method                   | Half-duplex bidirectional/full duplex bidirectional*1   | Half-duplex bidirectional/full duplex bidirectional*1  | Half-duplex bidirectional                           |  |  |
| Protocol type                          | MELSOFT connection, MC protocol (1C/3C/4C<br>frame), non-protocol communication, MODBUS<br>RTU communication, predefined protocol support | MELSOFT connection, MC protocol (1C/3C/4C<br>frame), non-protocol communication, MODBUS<br>RTU communication, inverter communication, N:N<br>network, parallel link, predefined protocol support | -   |  |  |
| Baud rate                              | 300/600/1200/2400/4800/9600/19200/<br>38400/57600/115200 (bps)*1  | 300/600/1200/2400/4800/9600/19200/<br>38400/57600/115200 (bps)*1   | 9600/19200/38400/57600/115200 (bps)                 |  |  |
| Terminal resistors                     | -   | Built-in (OPEN/110 Ω/330 Ω)  | -   |  |  |
| Power supply                           | 5 V DC, 20 mA (internal power supply)   | 5 V DC, 20 mA (internal power supply)  | 5 V DC, 20 mA (internal power supply)*2             |  |  |
| Compatible CPU module                  | FX5U  | FX5U   | FX5U  |  |  |
| Number of occupied input/output points | 0 points (no points occupied)   | 0 points (no points occupied)  | 0 points (no points occupied)                       |  |  |

\*1: The communication method and baud rate vary depending on the type of communication.
 \*2: When the GOT 5 V type is connected with this product, the power consumption increases. For the current consumption, refer to the manual of the model to be connected.

# ♦ Extension power supply module

## • FX5-1PSU-5V

| Item                                    |             | Specifications   |  |  |
|---|-------------|--|--|--|
| Rated supply voltage                    |             | 100 to 240 V AC  |  |  |
| Allowable range of supply voltage       | Э           | 85 to 264 V AC   |  |  |
| Frequency rating                        |             | 50/60 Hz   |  |  |
| Allowable instantaneous power f         | ailure time | Operation can be continued upon occurrence of instantaneous power failure for 10 ms or less. |  |  |
| Power fuse                              |             | 250 V, 3.15 A time-lag fuse  |  |  |
| In-rush current                         |             | 25 A Max. 5 ms or less/100 V AC<br>50 A Max. 5 ms or less/200 V AC                           |  |  |
| Power consumption                       |             | 20 W Max.  |  |  |
| Output current*                         | 24 V DC     | 300 mA (Maximum output current depends on the ambient temperature.)                          |  |  |
| (For power supply to rear stage) 5 V DC |             | 1200 mA (Maximum output current depends on the ambient temperature.)                         |  |  |
| Compatible CPU module                   |             | FX5U (AC power supply type)  |  |  |
| Number of occupied input/outpu          | t points    | 0 points (no points occupied)  |  |  |

\*: For details on the current conversion characteristic, refer to manuals of each product.

#### • FX5-C1PS-5V

| Item                                   |             | Specifications  |  |
|--|-------------|---|--|
| Supply voltage                         |             | 24 V DC   |  |
| Voltage fluctuation range              |             | +20%, -15%  |  |
| Allowable time of momentary pow        | ver failure | Operation can be continued upon occurrence of instantaneous power failure for 5 ms or less. |  |
| Power fuse                             |             | 125 V, 3.15 A time-lag fuse   |  |
| In-rush current                        |             | 35 A Max. 0.5 ms or less/24 V DC  |  |
| Power consumption                      |             | 30 W Max.   |  |
| Output current*                        | 24 V DC     | 625 mA (Maximum output current depends on the ambient temperature.)                         |  |
| (For power supply to rear stage)       | 5 V DC      | 1200 mA (Maximum output current depends on the ambient temperature.)                        |  |
| Compatible CPU module                  |             | FX5U (DC power supply type) FX5UC   |  |
| Number of occupied input/output points |             | 0 points (no points occupied)   |  |

 $\star$ : For details on the current conversion characteristic, refer to manuals of each product.

# ◇ Bus conversion module

● FX5-CNV-BUS (FX5 (extension cable type)→FX3 extension)

| Item                                   | Specifications   |  |
|--|--|--|
| Compatible CPU module                  | FX5U, FX5UC  |  |
| Number of occupied input/output points | 8 points (Either input or output is available for counting.) |  |
| Control power (supplied from PLC)      | 5 V DC 150 mA  |  |

# ♦ Connector conversion module

● FX5-CNV-IF (FX5 (extension cable type) → FX5 (extension connector type) extension)

|  | <i>J</i> [,,                  |  |
|--|-------------------------------|--|
| Item                                   | Specifications                |  |
| Compatible CPU module                  | FX5U                          |  |
| Number of occupied input/output points | 0 points (no points occupied) |  |
| Control power (supplied from PLC)      | 0 mA (no power consumed)      |  |

#### ● FX5-CNV-BUSC (FX5 (extension connector type)→FX3 extension)

| FX5U, FX5UC  |  |
|--|--|
| 8 points (Either input or output is available for counting.) |  |
| 5 V DC 150 mA  |  |
|  |  |

# ● FX5-CNV-IFC (FX5 (extension connector type)→ FX5 (extension cable type) extension)

| Item                                   |                               |  |
|--|-------------------------------|--|
| Compatible CPU module                  | FX5UC                         |  |
| Number of occupied input/output points | 0 points (no points occupied) |  |
| Control power (supplied from PLC)      | 0 mA (no power consumed)      |  |

# ◇ Intelligent function module

# • FX5-4AD

|                        |                     | Specifications   |                       |            |  |
|------------------------|---------------------|--|-----------------------|------------|--|
| Analog input points    |                     | 4 points (4 channels)  |                       |            |  |
| External device conne  | ection method       | Spring clamp terminal block                                  | (                     |            |  |
| Analog input voltage   |                     | -10 to +10 V DC (Input resis                                 | tance 400 kΩ or more) |            |  |
| Analog input current   |                     | -20 to +20 mA DC (Input re                                   | sistance 250 Ω)       |            |  |
| Absolute maximum input |                     | Voltage: ±15 V, Current: ±30                                 | ) mA                  |            |  |
|                        |                     | Analog input range   | Digital output value  | Resolution |  |
|                        |                     | 0 to 10 V  | 0 to 32000            | 312.5 μV   |  |
|                        | Voltage             | 0 to 5 V   | 0 to 32000            | 156.25 µV  |  |
|                        | voltage             | 1 to 5 V   | 0 to 32000            | 125 µV     |  |
| Input characteristics, |                     | -10 to +10 V   | -32000 to +32000      | 312.5 μV   |  |
| resolution*1           |                     | User range setting   | -32000 to +32000      | 125 µV*2   |  |
|                        |                     | 0 to 20 mA   | 0 to 32000            | 625 nA     |  |
|                        | Current             | 4 to 20 mA   | 0 to 32000            | 500 nA     |  |
|                        | Current             | -20 to +20 mA  | -32000 to +32000      | 625 nA     |  |
|                        |                     | User range setting   | -32000 to +32000      | 500 nA*2   |  |
| Digital output value   | Voltage/<br>Current | 16-bit signed binary (-32768                                 | 3 to +32767)          |            |  |
|                        |                     | Ambient temperature 25±5°C: within ±0.1% (±64 digits)        |                       |            |  |
| Accuracy               | Voltage/<br>Current | Ambient temperature 0 to 55°C: within ±0.2% (±128 digits)    |                       |            |  |
|                        | Guneni              | Ambient temperature -20 to 0°C: within ±0.3% (±192 digits)   |                       |            |  |
| Conversion speed       |                     | 80 µs/ch   |                       |            |  |
| Isolation              |                     | Between input terminal and PLC: Photocoupler isolation       |                       |            |  |
| Isolation              |                     | Between input terminal channels: Non-isolation               |                       |            |  |
| Power supply           |                     | 24 V DC, 40 mA (internal power supply)                       |                       |            |  |
|                        |                     | 5 V DC, 100 mA (internal power supply)                       |                       |            |  |
| Compatible CPU module  |                     | FX5U, FX5UC: Ver. 1.050 or                                   |                       |            |  |
|                        |                     | Connection with FX5UC requires FX5-CNV-IFC or FX5-C1PS-5V.   |                       |            |  |
| Number of occupied I.  | /O points           | 8 points (Either input or output is available for counting.) |                       |            |  |

\*1: For details on the input characteristics, refer to the manual.

\*2: Maximum resolution in the user range setting.

#### • FX5-4DA

| Items                             |                     | Specifications  |                             |            |  |
|-----------------------------------|---------------------|---|-----------------------------|------------|--|
| Analog output points              |                     | 4 points (4 channels)   |                             |            |  |
| External device connection method |                     | Spring clamp terminal block   |                             |            |  |
| Analog output voltag              | le                  | -10 to +10 V DC (External loa   | ad resistance 1 kΩ to 1 MΩ) |            |  |
| Analog output currer              | nt                  | 0 to 20 mA DC (External load  | d resistance 0 to 500 Ω)    |            |  |
|                                   |                     | Analog output range   | Digital value               | Resolution |  |
|                                   |                     | 0 to 10 V   | 0 to 32000                  | 312.5 µV   |  |
|                                   | Voltage             | 0 to 5 V  | 0 to 32000                  | 156.3 µV   |  |
| Output                            | voliage             | 1 to 5 V  | 0 to 32000                  | 125 µV     |  |
| characteristics,                  |                     | -10 to +10 V  | -32000 to +32000            | 312.5 µV   |  |
| resolution*1                      |                     | User range setting  | -32000 to +32000            | 312.5 µV*2 |  |
|                                   |                     | 0 to 20 mA  | 0 to 32000                  | 625 nA     |  |
|                                   | Current             | 4 to 20 mA  | 0 to 32000                  | 500 nA     |  |
|                                   |                     | User range setting  | -32000 to +32000            | 500 nA*2   |  |
| Digital input                     | Voltage/<br>Current | 16-bit signed binary (-32768 to +32767)                                       |                             |            |  |
|                                   | Voltage/<br>Current | Ambient temperature 25±5°C: within ±0.1% (Voltage ±20 mV, Current ±20 µA)     |                             |            |  |
| Accuracy                          |                     | Ambient temperature 0 to 55°C: within ±0.2% (Voltage ±40 mV, Current ±40 µA)  |                             |            |  |
|                                   | Gurrent             | Ambient temperature -20 to 0°C: within ±0.3% (Voltage ±60 mV, Current ±60 µA) |                             |            |  |
| Conversion speed                  |                     | 80 µs/ch  |                             |            |  |
| loolation                         |                     | Between output terminal and PLC: Photocoupler isolation                       |                             |            |  |
| Isolation                         |                     | Between output channels: Non-isolation  |                             |            |  |
| Power supply                      |                     | 5 V DC, 100 mA (internal power supply)  |                             |            |  |
| Power supply                      |                     | 24 V DC, +20%, -15% 150 mA (external power supply)                            |                             |            |  |
| Compatible CPU module             |                     | FX5U, FX5UC: Ver. 1.050 or later  |                             |            |  |
|                                   |                     | Connection with FX5UC requires FX5-CNV-IFC or FX5-C1PS-5V.                    |                             |            |  |
| Number of occupied I/O points     |                     | 8 points (Either input or output is available for counting.)                  |                             |            |  |

\*1: For details on the output characteristics, refer to the manual.\*2: Maximum resolution in the user range setting.

## • FX5-8AD

| FX5-8AD     Item                  |  | Specifications  |   |            |  |  |  |
|-----------------------------------|--|---|---|------------|--|--|--|
| Analog input points               |  | 8 points (8 channels)   |   |            |  |  |  |
| External device connection method |  | Spring clamp terminal block   |   |            |  |  |  |
| Analog input voltage              |  | -10 to 10 V DC (input resistance 1 MΩ)  |   |            |  |  |  |
| Analog input curr                 |  | -20 to +20 mA DC (input resistance $1002$ )   |   |            |  |  |  |
| Absolute maximu                   |  | Voltage: ±15 V, Current: ±30 mA   |   |            |  |  |  |
| ADSOIULE MAXIMU                   | Inniput  | K. J. T: 0.1°C (0.1 to 0.2°F)   |   |            |  |  |  |
|                                   | Thermocouple   | K, J, I: 0.1°C (0.1 to 0.2°F)<br>B, R, S: 0.1 to 0.3°C (0.1 to 0.6°F)   |   |            |  |  |  |
|                                   | Resistance<br>temperature<br>detector                  | 0.1°C (0.2°F)   |   |            |  |  |  |
| Input                             |  | Analog input range  | Digital output value  | Resolution |  |  |  |
| characteristics,                  |  | 0 to 10 V   | 0 to 32000  | 312.5 µV   |  |  |  |
| resolution                        | Voltage  | 0 to 5 V  | 0 to 32000  | 156.25 μV  |  |  |  |
|                                   |  | 1 to 5 V  | 0 to 32000  | 125 µV     |  |  |  |
|                                   |  | -10 to +10 V  | -32000 to +32000  | 312.5 µV   |  |  |  |
|                                   |  | 0 to 20 mA  | 0 to 32000  | 625 nA     |  |  |  |
|                                   | Current  | 4 to 20 mA  | 0 to 32000  | 500 nA     |  |  |  |
|                                   |  | -20 to +20 mA   | -32000 to +32000  | 625 nA     |  |  |  |
| Digital output<br>value           | Thermocouple   | K:         -2000 to +12000 (-3280 to +21920)           J:         -400 to +7500 (-400 to +13820)           T:         -2000 to +3500 (-3280 to +6620)           B:         6000 to 17000 (11120 to 30920)           R:         0 to 16000 (320 to 29120)           S:         0 to 16000 (320 to 29120) | : -2000 to +12000 (-3280 to +21920)<br>-400 to +7500 (-400 to +13820)<br>-2000 to -3500 (-3280 to +6620)<br>: 6000 to 17700 (11120 to 30920)<br>: 0 to 16000 (320 to 29120)   |            |  |  |  |
| (16-bit signed<br>binary value)   | Resistance<br>temperature<br>detector                  | Pt100: -2000 to +8500 (-3280 to +15620)<br>Ni100: -600 to +2500 (-760 to +4820)   |   |            |  |  |  |
|                                   | Voltage/<br>Current                                    | 16-bit signed binary (-32000 to +32000)   |   |            |  |  |  |
|                                   | Resistance<br>temperature<br>detector                  | Ambient temperature 25±5°C  | Pt100: ±0.8°C<br>Ni100: ±0.4°C  |            |  |  |  |
|                                   | Thermocouple   | Ambient temperature -20 to 55°C   | Pt100: ±2.4°C<br>Ni100: ±1.2°C  |            |  |  |  |
| Accuracy*                         |  | Ambient temperature 25±5°C  | $ \begin{array}{lll} {\rm K:} \pm 3.5^{\circ}{\rm C} \ (-200 \ {\rm to} \ -150^{\circ}{\rm C}) & {\rm K:} \ \pm 2.5^{\circ}{\rm C} \ (-150 \ {\rm to} \\ {\rm K:} \ \pm 1.5^{\circ}{\rm C} \ (-100 \ {\rm to} \ 1200^{\circ}{\rm C}) & {\rm J:} \ \pm 1.2^{\circ}{\rm C} \\ {\rm T:} \ \pm 3.5^{\circ}{\rm C} \ (-200 \ {\rm to} \ -150^{\circ}{\rm C}) & {\rm T:} \ \pm 2.5^{\circ}{\rm C} \ (-150 \ {\rm to} \\ {\rm T:} \ \pm 1.5^{\circ}{\rm C} \ (-100 \ {\rm to} \ 350^{\circ}{\rm C}) & {\rm B:} \ \pm 2.3^{\circ}{\rm C} \\ {\rm R:} \ \pm 2.5^{\circ}{\rm C} & {\rm S:} \ \pm 2.5^{\circ}{\rm C} \end{array} $ | ,          |  |  |  |
|                                   |  | Ambient temperature -20 to 55°C   | K:         ±8.5°C (-200 to -150°C)         K:         ±7.5°C (-150 to           K:         ±6.5°C (-100 to 1200°C)         J:         ±3.5°C           T:         ±5.2°C (-200 to -150°C)         T:         ±4.2°C (-150 to           T:         ±3.1°C (-100 to 350°C)         B:         ±6.5°C           R:         ±6.5°C         S:         ±6.5°C  |            |  |  |  |
|                                   | Voltage/   | Ambient temperature 25±5°C  | Within ±0.3% (±192 digits)  |            |  |  |  |
|                                   | Current  | Ambient temperature -20 to 55°C   | Within ±0.5% (±320 digits)  |            |  |  |  |
|                                   | Voltage/<br>Current                                    | 1 ms/ch   |   |            |  |  |  |
| Conversion<br>speed               | Thermocouple/<br>Resistance<br>temperature<br>detector | 40 ms/ch  |   |            |  |  |  |
| Isolation                         |  | Between input terminal and PLC: Photocoupler isolation<br>Between input terminal channels: Non-isolation  |   |            |  |  |  |
| Power supply                      |  | 24 V DC, 40 mA (internal power supply)<br>24 V DC +20%, -15% 100 mA (external power supply)   |   |            |  |  |  |
| Compatible CPU module             |  | FX5U, FX5UC: Ver. 1.050 or later<br>Connection with FX5UC requires FX5-CNV-IFC or FX5-C1PS-5V.  |   |            |  |  |  |
| Number of occupied I/O points     |  | 8 points (Either input or output is available for counting.)  |   |            |  |  |  |

\*: To stabilize the accuracy, warm-up (supply power) the system for 30 minutes or more after power-on.

## • FX5-4LC

| • FA5-                                | Item  |  | Specifications   |  |  |  |
|---------------------------------------|---|--|--|--|--|--|
| Control sy                            |   | Two position cont  | rol, standard PID control, heating/cooling PID control, cascade control  |  |  |  |
|                                       | evice connection method   | Spring clamp term  |  |  |  |  |
|                                       | peration cycle  | 250 ms/4 ch  | in de Dioork   |  |  |  |
|                                       | ure measuring range   | Thermocouple   | K:       -200 to +1300°C (-100 to +2400°F)         J:       -200 to +1200°C (-300 to +700°F)         S:       0 to 1700°C (0 to 3200°F)         R:       0 to 1700°C (0 to 3200°F)         E:       -200 to +1000°C (0 to 1800°F)         B:       0 to 1800°C (0 to 3300°F)         N:       0 to 1800°C (0 to 3300°F)         B:       0 to 1800°C (0 to 3300°F)         V:       0 to 1300°C (0 to 3300°F)         V:       0 to 1300°C (0 to 3300°F)         V:       0 to 1300°C (0 to 3300°F)         U:       -200 to +600°C (-300 to +700°F)         U:       -200 to +600°C (0 to 1600°F)         L:       0 to 900°C (0 to 1600°F) |  |  |  |
|                                       |   | Resistance<br>temperature<br>detector<br>Micro voltage<br>input  | Pt100 (3-wire type): -200 to +600°C (-300 to +1100°F)<br>JPt100 (3-wire type): -200 to +500°C (-300 to +900°F)<br>Pt1000 (2-wire/3-wire type): -200.0 to +650.0°C (-328 to +1184°F)<br>0 to 10 mV DC, 0 to 100 mV DC   |  |  |  |
| Heater dis                            | connection detection  | Alarm detection  |  |  |  |  |
| . locitor die                         | Number of input points  | 4 points   |  |  |  |  |
|                                       |   | Thermocouple   | K, J, R, S, E, T, B, N, PLII, W5Re/W26Re, U, L   |  |  |  |
|                                       | Input type  | Resistance<br>temperature<br>detector  | 3-wire type P1100<br>3-wire type JP1100<br>2-wire/3-wire type P11000   |  |  |  |
|                                       |   | Micro voltage inpu   | t  |  |  |  |
|                                       | Measurement accuracy  | Refer to the MELS  | EC iQ-F FX5 User's Manual (Temperature Control).   |  |  |  |
| Ø                                     | Cold junction temperature<br>compensation error                 | Ambient<br>temperature 0 to<br>55°C  | Within ±1.0°C. When the input value is -150 to -100°C: Within ±2.0°C<br>When the input value is -200 to -150°C: Within ±3.0°C  |  |  |  |
| nput specifications                   |   | Ambient<br>temperature -20<br>to 0°C   | Within ±1.8°C. When the input value is -150 to -100°C: Within ±3.6°C<br>When the input value is -200 to -150°C: Within ±5.4°C  |  |  |  |
| spec                                  | Resolution  | 0.1°C (0.1°F), 1.0°C   | C (1.0°F), 0.5 μV, or 5.0 μV (depends on the input range of the sensor used)   |  |  |  |
| out                                   | Sampling cycle  | 250 ms/4ch   |  |  |  |  |
| du l                                  | Influence of input<br>conductor resistance                      | 3-wire type  | About 0.03%/ $\Omega$ for full scale, and 10 $\Omega$ or less per line   |  |  |  |
|                                       | (for resistance temperature detector input)                     | 2-wire type  | About 0.04%/ $\Omega$ for full scale, and 7.5 $\Omega$ or less per line  |  |  |  |
|                                       | Influence of external<br>resistance<br>(for thermocouple input) | About 0.125 μV/Ω   |  |  |  |  |
|                                       | Input impedance   | $1 \text{ M}\Omega$ or more  |  |  |  |  |
|                                       | Sensor current  | About 0.2 mA (for  | resistance temperature detector input)   |  |  |  |
|                                       | Operation at input<br>disconnection/short circuit               |  | le (for resistance temperature detector input)   |  |  |  |
| Output sp                             | ecifications  | Number of points: 4<br>Type: NPN open collector transistor output, Rated load voltage: 5 to 24 V DC<br>Maximum load current: 100 mA, Control output cycle: 0.5 to 100.0 seconds  |  |  |  |  |
| Power sup                             | ylad  |  | nternal power supply)<br>5% 25 mA (external power supply)  |  |  |  |
| Isolation                             |   | <ul> <li>The analog input part and between the transistor output part and PLC are insulated by the photocoupler.</li> <li>The analog input part and between the transistor output part and power supply are insulated by the DC-DC converter.</li> <li>Insulated between channels</li> </ul> |  |  |  |  |
| · · · · · · · · · · · · · · · · · · · | le CPU module   |  | X5UC requires FX5-CNV-IFC or FX5-C1PS-5V.  |  |  |  |
| Number o                              | f occupied I/O points   | 8 points (Either inp   | 8 points (Either input or output is available for counting.)   |  |  |  |

#### • FX5-20PG-P, FX5-20PG-D

| Item                          | Spec   |  |  |
|-------------------------------|--|--|--|
| liem                          | FX5-20PG-P   | FX5-20PG-D   |  |
| Number of control axes        | 2 axes   |  |  |
| Command Speed                 | 200 kpps   | 5 Mpps   |  |
| Pulse Output                  | Output signal: PULSE/SIGN mode, CW/CCW mode,<br>phase A/B (4 multiplication), phase A/B (1 multiplication)<br>Output terminal: Transistor<br>5 to 24 V DC 50 mA or less  | Output signal: PULSE/SIGN mode, CW/CCW mode,<br>phase A/B (4 multiplication), phase A/B (1 multiplication)<br>Output terminal: Differential driver equivalent to AM26C31 |  |
| External I/O specifications   | Input: READY/STOP/FLS/RLS/PG024/DOG/CHG terminals: 24 V DC 5 mA,<br>PULSER A/PULSER B terminals: 5 V DC 14 mA<br>Zero point signal PG05 terminal: 5 V DC 5 mA<br>Output: CLEAR (deviation counter): 5 to 24 V DC 100 mA or less<br>Circuit insulation: Photocoupler insulation |  |  |
| Power supply                  | 24 V DC +20%, -15% 120 mA (external power supply)  | 24 V DC +20%, -15% 165 mA (external power supply)  |  |
| Compatible CPU module         | FX5U, FX5UC: Ver. 1.050 or later<br>Connection with FX5UC requires FX5-CNV-IFC or FX5-C1PS-5V.   |  |  |
| Number of occupied I/O points | 8 points (Either input or output is available for counting.)   |  |  |

#### • FX5-ENET

|   |   | Items  |   |  | Specifications   |  |  |
|---|---|--|---|--|--|--|--|
|   | Station typ   | e  |   |  | Master station   |  |  |
|   | <u> </u>  | number of connectabl   |   |  | 32   |  |  |
|   | Number of   | stations occupied by   | a slave station   |  | 1 to 4   |  |  |
|   | RX  |  |   |  | 2048 points  |  |  |
|   | Maximum number of link points per network   |  |   | RY   | 2048 points  |  |  |
|   | TVICOAITTICITTI   |  | of notwork  | RWr  | 1024 points  |  |  |
|   |   |  |   | RWw  | 1024 points  |  |  |
|   |   |  |   | RX   | 2048 points  |  |  |
|   |   |  | Master  | RY   | 2048 points  |  |  |
|   |   |  | station   | RWr  | 1024 points  |  |  |
|   |   | number of link points  |   | RWw  | 1024 points  |  |  |
| CC-Link IE  | per station   |  |   | RX   | 64/128/192/256 points  |  |  |
| Field   |   |  | Slave   | RY   | 64/128/192/256 points  |  |  |
| Network Basic   |   |  | station*2   | RWr  | 32/64/96/128 points  |  |  |
|   |   |  |   | RWw  | 32/64/96/128 points  |  |  |
|   |   | number used in the cy  |   |  | 61450  |  |  |
|   |   | number used in autom   | atic detection of   | f  | Master station: An unused port number is assigned automatically.   |  |  |
|   | connected   | 1  |   |  | Slave station: 61451   |  |  |
|   |   | Data transfer speed  |   |  | 100 Mbps   |  |  |
|   | Transmission  | Maximum station-to   |   | 9  | 100 m  |  |  |
|   | specifications  | Overall cable distance           Number of cascade           100BASE-TX  |   |  | Depends on the system configuration  |  |  |
|   | 1   |  |   |  | When using a switching hub, check the number of cascaded stages with the manufacturer of the hub   |  |  |
|   | connections         rocbride rike           Network topology         rike                           |  |   |  | to be used.  |  |  |
|   | Hub*3   | ppology  |   |  | Consult the manufacturer.<br>Hubs with 100BASE-TX ports*4 can be used.   |  |  |
|   |   |  |   |  | Ethernet standard-compatible cable Category 5 or higher (STP cable)  |  |  |
|   | COINECTION  | Data transfer speed  | 100BASE-TX  |  | 100/10 Mbps  |  |  |
|   |   | Communication mode   |   |  | Full-duplex or half-duplex*3   |  |  |
|   |   | Transmission method  |   |  | Base band  |  |  |
|   | Transmission  | Interface  |   |  | RJ45 connector   |  |  |
|   | specifications  | Maximum segment length   |   |  |  |  |  |
|   |   | (Maximum distance between hub and<br>Number of cascade 100BASE-TX  |   | id node)   | 100 m*6  |  |  |
| General-purpose   |   |  |   |  | Max. 2 stages*7  |  |  |
| Ethernet communication  |   | connections  |   |  | Max. 4 stages*7  |  |  |
|   | Supported   | protocol   |   |  | Socket communication   |  |  |
|   | <u> </u>  | connections  |   |  | Total of 32 connections (Up to 32 external devices can access one FX5-ENET module at the same time.)   |  |  |
|   | Hub*3   |  |   |  | Hubs with 100BASE-TX or 10BASE-T ports*8 can be used.  |  |  |
|   |   |  | 100BASE-TX  |  | Ethernet standard-compatible cable Category 5 or higher (STP cable)  |  |  |
|   | Connection  | n cable*5  | 10BASE-T  |  | Ethernet standard-compatible cable Category 3 or higher (STP/UTP cable) Ethernet standard-compatible cable Category 3 or higher (STP/UTP cable)                            |  |  |
| Number of ports   |   |  | TODAGLET  |  | 2*9  |  |  |
| Power supply  |   |  |   |  | 5 V DC, 110 mA (internal power supply)   |  |  |
|   |   |  |   |  | EX5U. EX5UC: Ver. 1.110 or later   |  |  |
| Compatible CPU module   |   |  |   |  | Connection with FX5UC requires FX5-CNV-IFC or FX5-C1PS-5V.   |  |  |
| Number of occupied I/O points   |   |  |   |  | 8 points (Either input or output is available for counting.)   |  |  |
| <ul> <li>S: IEEE802.3x flow co</li> <li>The ports must cor</li> <li>A straight/cross cal</li> <li>For maximum segn</li> </ul> | occupation<br>notrol is not<br>noty with thole can be<br>nent length<br>s when a re<br>noty with th | , 2-station occupation<br>supported.<br>le IEEE802.3 100BA<br>used.<br>(length between hul<br>epeater hub is used.<br>le IEEE802.3 100BA | on, 3-station of<br>SE-TX standa<br>os), consult the<br>When using a<br>SE-TX or IEEE | ccupatio<br>Irds.<br>e manufa<br>I switchin<br>5802.3 10 | n, or 4-station occupation.<br>Incturer of the hub used.<br>Ig hub, check the number of cascaded stages with the manufacturer of the hub to be used.<br>JBASE-T standards. |  |  |

#### FX5-ENET/IP

|                  |                             |                                     | Specifications   |
|------------------|-----------------------------|-------------------------------------|--|
|                  |                             | Communication format                | Standard EtherNet/IP   |
|                  |                             | Number of connections               | 32   |
|                  | Class 1                     | Communication data size             | 1444 bytes (per connection)                                  |
|                  | Class 1                     | Connection type                     | Point-to-point, multicast                                    |
|                  | communications              | RPI (communication cycle)           | 2 to 60000 ms  |
|                  |                             | PPS (communication processing       | 3000 pps (case of 128 bytes)                                 |
|                  |                             | performance)                        |  |
|                  |                             | Communication format                | Standard EtherNet/IP   |
|                  | Class 3                     | Number of connections               | 32*1   |
|                  | communications              | (number of simultaneous executions) |  |
|                  |                             | Communication data size             | 1414 bytes (per connection)*2                                |
|                  |                             | Connection type                     | Point-to-point   |
| EtherNet/IP      |                             | Communication format                | Standard EtherNet/IP   |
| communications   | UCMM                        | Number of connections               | 32*1   |
|                  | communications              | (number of simultaneous executions) |  |
|                  |                             | Communication data size             | 1414 bytes*2   |
|                  |                             | Connection type                     | Point-to-point   |
|                  | Transmission specifications | Data transmission speed             | 100 Mbps   |
|                  |                             | Communication mode                  | Full-duplex  |
|                  |                             | Transmission method                 | Base band  |
|                  |                             | IP version                          | IPv4 is supported.   |
|                  |                             | Maximum segment length              | 100 m*3  |
|                  |                             | Number of cascade connections       | 100BASE-TX: 2 levels maximum*4                               |
|                  | Network topology            |                                     | Star topology, line pology                                   |
|                  | Hub*5                       |                                     | *6   |
|                  | Connection cable*7          |                                     | 100BASE-TX   |
|                  |                             | Data transfer speed                 | 100/10 Mbps  |
|                  |                             | Communication mode                  | Full-duplex or half-duplex*5                                 |
|                  | Transmission                | Transmission method                 | Base band  |
| General-purpose  | specifications              | Maximum segment length              | 100 m*3  |
| Ethernet         |                             | Number of cascade connections       | 100BASE-TX:2 levels maximum*4<br>10BASE-T:4 levels maximum*4 |
| communication    | Protocol type               | 1                                   | Socket communication   |
|                  | Number of connecti          | ons                                 | Total of 32 connections*8                                    |
|                  | Hub*5                       | 010                                 | *9   |
|                  | Connection cable*7          |                                     | 100BASE-TX, 10BASE-T   |
| Number of ports  |                             |                                     | 2*10   |
| Power supply     |                             |                                     | 24 V DC, 110 mA (internal power supply)                      |
|                  |                             |                                     | FX5U, FX5UC: Ver. 1.110 or later                             |
| Compatible CPU r | nodule                      |                                     | Connection with FX5UC requires FX5-CNV-IFC or FX5-C1PS-5V.   |
| Number of occupi | ed I/O points               |                                     | 8 points (Either input or output is available for counting.) |
|                  | ou , o pointo               |                                     |  |

\*1 : The total number of connections for Class 3 communications and UCMM communications is 32.
\*2 : This size is the maximum size which can be specified to 'Data length' of Class1 communication input data area of the request command during the client operation. During the sever operation, since the FX5-ENET/IP automatically responds according to the request command received from the client, the maximum size is not prescribed.
\*3 : For maximum segment length (length between hubs), consult the manufacturer of the hub used.

\*43 : For maximum segment length (length between hubs), consult the manufacturer of the hub used.
\*44 : This number applies when a repeater hub is used. When using a switching hub, check the number of cascaded stages with the manufacturer of the hub to be used.
\*5 : IEEE802.3x flow control is not supported.
\*6 : Hubs with 100BASE-TX ports can be used. The ports must comply with the IEEE802.3 100BASE-TX standards.
\*7 : A straight/cross cable can be used.

\*8 : Up to 32 external devices can access one FX5-ENET/IP module at the same time.
 \*9 : Hubs with 100BASE-TX or 10BASE-T ports can be used. The ports must comply with the IEEE802.3 100BASE-TX or IEEE802.3 10BASE-T standards.

 $\star$  10: Since the IP address is shared by two ports, only one address can be set.

#### FX5-CCL-MS

|                              |                                | Specifications   |                                  |                                    |                                  |                                      |                                  |                                      |                                  |                    |                 |
|------------------------------|--------------------------------|--|----------------------------------|------------------------------------|----------------------------------|--------------------------------------|----------------------------------|--------------------------------------|----------------------------------|--------------------|-----------------|
| Compatible functi            | ons                            | Master station   | or intelligent dev               | rice station                       |                                  |                                      |                                  |                                      |                                  |                    |                 |
| CC-Link supporte             | d version                      | Ver. 2.00 and Ver. 1.10  |                                  |                                    |                                  |                                      |                                  |                                      |                                  |                    |                 |
| Transmission Spe             | od                             |  |                                  |                                    | /5 Mbps/10 Mbp                   |                                      |                                  |                                      |                                  |                    |                 |
| Transmission ope             | eu                             |  |                                  |                                    | 2.5 Mbps/5 Mbp                   | os/10 Mbps/auto                      | o-tracking                       |                                      |                                  |                    |                 |
| Station number               |                                |  | n: 0 • Intelligen                |                                    |                                  |                                      |                                  |                                      |                                  |                    |                 |
| Connectable stati            |                                |  |                                  |                                    | intelligent device               |                                      |                                  |                                      |                                  |                    |                 |
| (at the time of ma           | ,                              | (  | ,                                |                                    | ot be connected)                 |                                      |                                  |                                      |                                  |                    |                 |
| Maximum overall              | cable length                   |  | depending on tr                  | ·                                  | ea)<br>number of I/O pa          | into remoto I/O                      | atationa io 440 a                | × (000)                              |                                  |                    |                 |
| Maximum numbe                |                                |  |                                  |                                    | evice stations: U                |                                      |                                  | '                                    | f intolligant dovi               | on stations and r  | omoto dovico    |
| stations (at the tin         | ne of master station)          | stations is 44   |                                  | is + intelligent de                | evice stations. Of               | 5 10 14 Stations                     |                                  | inputs/outputs c                     | n in teiligent devic             | Je stations and i  | entote device   |
| Number of occup              | ied stations (at the           |  |                                  | P 1 11 11                          |                                  |                                      |                                  |                                      |                                  |                    |                 |
| time of intelligent          |                                | 1 to 4 stations  | (changed accord                  | ding to the settir                 | ng of engineering                | tool)                                |                                  |                                      |                                  |                    |                 |
|                              |                                |  |                                  |                                    | ation: 448 points                | * <sup>3</sup> , remote devi         | ce stations and i                | ntelligent device                    | stations: 448 po                 | ints)              |                 |
| Maximum                      | CC-Link Ver. 1                 |  | ter (RWw): 56 p                  |                                    |                                  |                                      |                                  |                                      |                                  |                    |                 |
| number of<br>link points per |                                |  | ter (RWr): 56 po                 |                                    | 440                              |                                      | Alexandra and taken the          | and dealer and the                   | (40                              |                    |                 |
| system*5                     | CC-Link Ver. 2                 | Remote I/O (RX, RY): 896 (remote I/O station: 448 points* <sup>3</sup> , remote device stations and intelligent device stations: 448 points)     Remote register (RWw): 112 points |                                  |                                    |                                  |                                      |                                  |                                      |                                  |                    |                 |
| o you on the                 | CO-LINK Vel. 2                 | Remote register (RWr): 112 points     Remote register (RWr): 112 points  |                                  |                                    |                                  |                                      |                                  |                                      |                                  |                    |                 |
|                              |                                |  | <u></u>                          |                                    |                                  |                                      | CC-Lir                           | nk Ver. 2                            |                                  |                    |                 |
|                              | Extended cyclic                | CC-Lin   | k Ver. 1                         |                                    |                                  |                                      |                                  |                                      |                                  |                    |                 |
|                              | setting                        |  |                                  | Single                             |                                  | Double                               |                                  | Quadruple                            |                                  | Octuple            |                 |
|                              | Number of<br>occupied stations | Remote I/O   | Remote register                  | Remote I/O                         | Remote register                  | Remote I/O                           | Remote register                  | Remote I/O                           | Remote register                  | Remote I/O         | Remote register |
|                              | 1 station occupied             | RX, RY: 32 points  | RWw: 4 points                    | RX, RY: 32 points                  | RWw: 4 points                    | RX, RY: 32 points                    | RWw: 8 points                    | RX, RY: 64 points                    | RWw: 16 points                   | RX, RY: 128 points | RWw: 32 points  |
| Number of link               | 1 station occupied             | (16 points)*4  | RWr: 4 points                    | (16 points)*4                      | RWr: 4 points                    | (16 points)*4                        | RWr: 8 points                    | (48 points)*4                        | RWr: 16 points                   | (112 points)*4     | RWr: 32 points  |
| points*5                     | 2 station occupied             | RX, RY: 64 points  | RWw: 8 points                    | RX, RY: 64 points                  | RWw: 8 points                    | RX, RY: 96 points                    | RWw: 16 points                   | RX, RY: 192 points                   | RWw: 32 points                   | RX, RY: 384 points | RWw: 64 points  |
|                              |                                | (48 points)*4  | RWr: 8 points<br>RWw: 12 points  | (48 points)*4<br>RX, RY: 96 points | RWr: 8 points                    | (80 points)*4                        | RWr: 16 points                   | (176 points)*4<br>RX, RY: 320 points | RWr: 32 points<br>RWw: 48 points | (368 points)*4     | RWr: 64 points  |
|                              | 3 station occupied             | RX, RY: 96 points<br>(80 points)*4   | RWW: 12 points<br>RWr: 12 points | (80 points)*4                      | RWw: 12 points<br>RWr: 12 points | RX, RY: 160 points<br>(144 points)*4 | RWw: 24 points<br>RWr: 24 points | (304 points)*4                       | RWr: 48 points                   |                    |                 |
|                              |                                | RX, RY: 128 points   | RWw: 16 points                   | RX, RY: 128 points                 |                                  | RX, RY: 224 points                   | RWw: 32 points                   |                                      | RWw, RWr: 64 points              | $\sim$             |                 |
|                              | 4 station occupied             | (112 points)*4   | RWr: 16 points                   | (112 points)*4                     | RWr: 16 points                   | (208 points)*4                       | RWr: 32 points                   | (-)*4                                | (-)*4                            |                    |                 |
| Transmission cab             | le                             | CC-Link Ver. 1.10 compatible CC-Link dedicated cable   |                                  |                                    |                                  |                                      |                                  |                                      |                                  |                    |                 |
| Compatible CPU               | module                         |  | Ver. 1.050 or late               |                                    |                                  |                                      |                                  |                                      |                                  |                    |                 |
|                              |                                | Connection with FX5UC requires FX5-CNV-IFC or FX5-C1PS-5V.   |                                  |                                    |                                  |                                      |                                  |                                      |                                  |                    |                 |
| Communication r              |                                | Broadcast polli  | 0                                |                                    |                                  |                                      |                                  |                                      |                                  |                    |                 |
| Transmission form            |                                | HDLC complian  |                                  |                                    |                                  |                                      |                                  |                                      |                                  |                    |                 |
| Error control syst           | em                             | CRC (X <sup>16</sup> + X <sup>12</sup>   | ,                                |                                    | a sector a                       |                                      |                                  |                                      |                                  |                    |                 |
| Power supply                 | ind I/O painta                 |  | , -15% 100 mA (                  | · · ·                              |                                  |                                      |                                  |                                      |                                  |                    |                 |
| Number of occup              |                                | 1  | e counted on eit                 | · · ·                              | put)<br>thor with the F          |                                      | -                                |                                      |                                  |                    |                 |

\*1: When using the FX5-CCL-MS as the master station, it cannot be used together with the FX3U-16CCL-M.
\*2: When using the FX5-CCL-MS as the intelligent device station, it cannot be used together with the FX3U-16CCL-M.
\*3: The number of remote I/O points that can be used per system varies depending on the number of input/output points of the extension device. For the limit of the number of I/O points, refer to the following manual.
→ MELSEC IQ-F FX5U User's Manual (Hardware)
\*4: The numbers in parentheses are the points that can be used when the module is an intelligent device station.
\*5: Numbers of Infxs with FX5U/FX5UC CPU module Ver. 1.100 or later. GX Works3 Ver. 1.047Z or later required. For details on the number of links with FX5U/FX5UC CPU module earlier than Ver. 1.100, refer to the following manual.
→ MELSEC IQ-F FX5 User's Manual (CC-Link)

#### FX5-CCLIEF

| Item Specifications        |          | Specifications   |  |  |  |
|----------------------------|----------|--|--|--|--|
| Station type               |          | Intelligent device station   |  |  |  |
| Station number             |          | 1 to 120 (sets by parameter or program)  |  |  |  |
| Communication speed        |          | 1 Gbps   |  |  |  |
| Network topology           |          | Line topology, star topology (coexistence of line topology and star topology is also possible),<br>and ring topology |  |  |  |
| Maximum station-to-station | distance | Max. 100 m (Conforming to ANSI/TIA/EIA-568-B (Category 5e))  |  |  |  |
| Cascade connection         |          | Max. 20 stages   |  |  |  |
| Communication method       |          | Token passing  |  |  |  |
|                            | RX       | 384 points, 48 bytes   |  |  |  |
| Maximum number of link     | RY       | 384 points, 48 bytes   |  |  |  |
| points*1                   | RWr      | 024 points, 2048 bytes*2   |  |  |  |
|                            | RWw      | 1024 points, 2048 bytes*2  |  |  |  |
| Compatible CPU module      |          | FX5U, FX5UC Ver. 1.030 or later.<br>Connection with FX5UC requires FX5-CNV-IFC or FX5-C1PS-5V.                       |  |  |  |
| Power supply               |          | 5 V DC, 10 mA (internal power supply)<br>24 V DC, 230 mA (external power supply)                                     |  |  |  |
| Number of occupied I/O poi | nts      | 8 points (Either input or output is available for counting.)   |  |  |  |

\*1: The maximum number of link points that a master station can assign to one FX5-CCLIEF module. \*2: 256 points (512 bytes) when the mode of the master station is online (High-Speed Mode).

#### FX5-ASL-M

| Item   | Specifications  |  |  |  |
|--|---|--|--|--|
| Transmission clock                                       | 27.0 kHz  |  |  |  |
| Maximum transmission distance (total extension distance) | 200 m*1   |  |  |  |
| Transmission system                                      | DC power supply superimposed total frame/cyclic system  |  |  |  |
| Connection type  | Bus type (multi-drop method, T-branch method, tree branch method)   |  |  |  |
| Transmission protocol                                    | Dedicated protocol (AnyWireASLINK)  |  |  |  |
| Error control  | Checksum, double check method   |  |  |  |
| Number of connected I/O points                           | Up to 448 points*2*3 (256 input points maximum/256 output points maximum)   |  |  |  |
| Number of connected slave modules                        | Up to 128 modules (the number varies depending on the current consumption of each slave module)   |  |  |  |
| External interface                                       | 7-piece spring clamp terminal block push-in type  |  |  |  |
| RAS function   | Transmission line disconnection position detection function     Transmission line short-circuit detection function     Transmission power drop detection function |  |  |  |
| Transmission line (DP, DN)                               | UL-compliant general-purpose 2-wire cable   |  |  |  |
| Power cable (24 V, 0 V)                                  | UL-compliant general-purpose cable     For dedicated flat cables  |  |  |  |
| Memory   | Built-in memory EEPROM (rewrite endurance: 100 thousand times)  |  |  |  |
| Compatible CPU module                                    | FX5U, FX5UC: Ver. 1.050 or later<br>Connection with FX5UC requires FX5-CNV-IFC or FX5-C1PS-5V.  |  |  |  |
| Power supply   | 5 V DC, 200 mA (internal power supply)<br>24 V DC +15%, -10% 100 mA (external power supply)   |  |  |  |
| Number of occupied I/O points                            | 8 (can be counted on either input or output)  |  |  |  |

\*1: For the slave module in which the transmission line (DP, DN) and module of entre input of output)
 \*1: For the slave module in which the transmission line (DP, DN) and module opwer line noise filter between the power supply and the line . When laying a 4-wire (DP, DN, 24 V, 0 V) line for fifty meters or more, insert a power line noise filter between the power supply and the line . For details, refer to the manual of ASLINK filter (ANF-01) made by Anywire Corporation.
 \*2: The number of remote I/O points that can be used per system varies depending on the number of input/output points of the extension device. For the limit of the number of I/O points, refer to the following manual. → MELSEC IO-F FX5U User's Manual (Hardware)
 \*3: Supported by FX5U/FX5UC CPU modules Ver. 1.100 or later and by GX Works3 Ver. 1.047Z or later.

#### • FX5-DP-M

|                       |   |             | Specifications   |  |
|-----------------------|---|-------------|--|--|
| PROFIBUS-DF           | P station type  |             | Class 1 master station   |  |
|                       | Electrical standard and characteristics                         |             | Compliant with EIA-RS485   |  |
|                       | Medium  |             | Shielded twisted pair cable  |  |
|                       | Network configuration   |             | Bus topology (or tree topology when repeaters are used)  |  |
|                       | Data link method  |             | Between DP-Masters: Token passing<br>Between DP-Master and DP-Slave: Polling                   |  |
|                       | Encoding method   |             | NRZ  |  |
|                       | Transmission speed*1  |             | 9.6 kbps, 19.2 kbps, 93.75 kbps, 187.5 kbps, 500 kbps, 1.5 Mbps, 3 Mbps, 6 Mbps, 12 Mbps       |  |
| Transmission          | Transmission distance   |             | Differs depending on transmission speed <sup>*2</sup>  |  |
| specifications        | Maximum number of repeaters<br>(Between DP-Master and DP-Slave) |             | 3 repeaters  |  |
|                       | Number of connectable modules (per segment)                     |             | 32 per segment (including repeaters)   |  |
|                       | Maximum number of DP-Slave                                      | es          | 64 modules*3   |  |
|                       | Number of connectable nodes<br>(number of repeaters)            |             | 32, 62 (1), 92 (2), 122 (3), 126 (4)   |  |
|                       | Transmittable data  | Input data  | Max. of 2048 bytes (Max. of 244 bytes per DP-Slave)  |  |
|                       | Transmittable data  | Output data | Max. of 2048 bytes (Max. of 244 bytes per DP-Slave)  |  |
| Number of occ         | cupied I/O points   |             | 8 points (Either input or output is available for counting.)                                   |  |
| Power supply          |   |             | 5 V DC, 150 mA (internal power supply)   |  |
| Compatible CPU module |   |             | FX5U, FX5UC: Ver. 1.110 or later<br>Connection with FX5UC requires FX5-CNV-IFC or FX5-C1PS-5V. |  |
| Number of occ         | cupied I/O points   |             | 8 points (Either input or output is available for counting.)                                   |  |

\*1: Transmission speed accuracy is within ±0.2% (compliant with IEC61158-2).
\*2: For details on the transmission distance, refer to the manual.
\*3: For details on the PROFIBUS-DP network configuration, refer to the manual.

# Simple motion module FX5-40SSC-S FX5-80SSC-S

Control specification

|                              | lte                               | em                             |  | cations                      |  |
|------------------------------|-----------------------------------|--------------------------------|--|------------------------------|--|
| Number of c                  | ontrol a                          |                                | FX5-40SSC-S  | FX5-80SSC-S                  |  |
| (Virtual serve               | o amplifi                         | ier axis included)             | Max. 4 axes  | Max. 8 axes                  |  |
| Operation cy<br>(Operation c |                                   | ttings)                        | 0.888 ms / 1.777 ms  |                              |  |
| Interpolation                | n functio                         | on                             | Linear interpolation (up to 4-<br>interpolation)   |                              |  |
| Control syste                | em                                |                                | PTP (Point To Point) control<br>linear and arc), Speed contro<br>control, Position-speed swit<br>torque control          | ol, Speed-position switching |  |
| Acceleration                 | /decele                           | eration process                | Trapezoidal acceleration/de<br>S-curve acceleration/ decel   |                              |  |
| Compensati                   | on func                           | tion                           | Backlash compensation, Ele<br>function   | ectronic gear, Near pass     |  |
| Synchronou                   | s                                 | Input axis                     | Servo input axis, synchrono generation axis  | us encoder axis, command     |  |
|                              |                                   | Output axis                    | Cam shaft  |                              |  |
|                              |                                   | Number of<br>registered cams*1 | Up to 64 cams  | Up to 128 cams               |  |
| Cam control                  |                                   | Cam data format                | Stroke ratio data format, co   | ordinate data format         |  |
|                              |                                   | Automatic<br>generation of cam | Automatic generation of cam for rotary cutter  |                              |  |
| Control unit                 |                                   | 0                              | mm, inch, degree, pulse  |                              |  |
| Number of p                  | ositioni                          | ng data                        | 600 data (positioning data No. 1 to 600)/axis<br>(Can be set with MELSOFT GX Works3 or a sequence<br>program.)           |                              |  |
| Backup                       |                                   |                                | Parameters, positioning data, and block start data can<br>be saved on flash ROM (battery-less backup)                    |                              |  |
| Home                         | Home position return method       |                                | Proximity dog method, Count method 1, Count method 2,<br>Data set method, Scale home position signal detection<br>method |                              |  |
| position<br>return           | Fast home position return control |                                | Provided   |                              |  |
|                              | Auxiliary functions               |                                | Home position return retry, Home position shift  |                              |  |
|                              | Linear control                    |                                | Linear interpolation control (Up to 4 axes)*2<br>(Vector speed, Reference axis speed)                                    |                              |  |
|                              | Fixed-                            | pitch feed control             | Fixed-pitch feed control (Up to 4 axes)  |                              |  |
|                              | 2-axis                            | circular interpolation         | Auxiliary point-specified circular interpolation,<br>Central point-specified circular interpolation                      |                              |  |
|                              | Speed control                     |                                | Speed control (Up to 4 axes)   |                              |  |
|                              | Speed-position switching control  |                                | INC mode, ABS mode   |                              |  |
| Positioning<br>control       | Positic<br>contro                 | on-speed switching             | INC mode   |                              |  |
|                              | Currer                            | nt value change                | Positioning data, Start No. for a current value changing   |                              |  |
|                              | NOP in                            | nstruction                     | Provided   |                              |  |
|                              | JUMP                              | instruction                    | Unconditional JUMP, Condit   | ional JUMP                   |  |
|                              | LOOP,                             | LEND                           | Provided   |                              |  |
|                              | High-level positioning control    |                                | Block start, Condition start, Wait start, Simultaneous<br>start,<br>Repeated start                                       |                              |  |
|                              | JOG                               | operation                      | Provided   |                              |  |
| Manual                       |                                   | g operation                    | Provided   |                              |  |
| control                      |                                   |                                |  | le (Incremental)             |  |
|                              | Manua                             | al pulse generator             | Possible to connect 1 module (Incremental),<br>Unit magnification (1 to 10000 times)                                     |                              |  |

|                                     |  | Specifications   |  |  |
|-------------------------------------|--|--|--|--|
|                                     |  | FX5-40SSC-S FX5-80SSC-S  |  |  |
| Expansion<br>control                | Speed-torque control                           | Speed control without positioning loops, Torque control,<br>Tightening & press-fit control               |  |  |
| Absolute po                         | sition system                                  | Made compatible by setting a battery to servo amplifier  |  |  |
| Synchronou                          | s encoder interface                            | Up to 4 channels (Total of the internal interface, via PLC CPU interface, and servo amplifier interface) |  |  |
|                                     | Internal interface                             | 1 ch (Incremental)   |  |  |
|                                     | Speed limit function                           | Speed limit value, JOG speed limit value   |  |  |
|                                     | Torque limit function                          | Torque limit value same setting, torque limit value<br>individual setting                                |  |  |
| Functions that limit                | Forced stop                                    | Valid/Invalid setting  |  |  |
| control                             | Software stroke limit<br>function              | Movable range check with current feed value,<br>movable range check with machine feed value              |  |  |
|                                     | Hardware stroke limit<br>function              | Provided   |  |  |
|                                     | Speed change function                          | Provided   |  |  |
|                                     | Override function                              | 1 to 300 [%]   |  |  |
| Functions<br>that change<br>control | Acceleration/deceleration time change function | Provided   |  |  |
| details                             | Torque change function                         | Provided   |  |  |
|                                     | Target position change function                | Target position address and speed are changeable   |  |  |
|                                     | M-code output function                         | Provided   |  |  |
| Other                               | Step function                                  | Deceleration unit step, Data No. unit step   |  |  |
| functions                           | Skip function                                  | Via PLC CPU, Via external command signal   |  |  |
|                                     | Teaching function                              | Provided   |  |  |
| Parameter ir                        | nitialization function                         | Provided   |  |  |
| External inpu                       | ut signal setting function                     | Via CPU  |  |  |
| Amplifier-les                       | s operation function                           | Provided   |  |  |
| Mark<br>detection                   |  | Continuous Detection mode,<br>Specified Number of Detections mode, Ring Buffer<br>mode                   |  |  |
| function                            | Mark detection signal                          | Up to 4 points   |  |  |
|                                     | Mark detection setting                         | 16 settings  |  |  |
| Optional dat                        | a monitor function                             | 4 points/axis  |  |  |
| Driver comm                         | nunication function                            | Provided   |  |  |
| SSCNET co                           | nnect/disconnect function                      | Provided   |  |  |
| Digital                             | Bit data                                       | 16 ch  |  |  |
| oscilloscope<br>function*3          | Word data                                      | 16 ch  |  |  |

\*1: The number of registered cams varies depending on the memory capacity, cam resolution, and the number of coordinates.
\*2: 4-axis linear interpolation control is enabled only at the reference axis speed.
\*3: 8 ch word data and 8 ch bit data can be displayed in real time.

#### Module specification

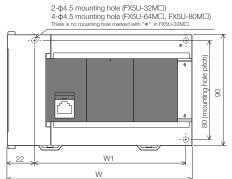
| Item                        |                                 | Specifi  | cations       |  |
|-----------------------------|---------------------------------|--|---------------|--|
|                             |                                 | FX5-40SSC-S  | FX5-80SSC-S   |  |
| Number of co                | ontrol axes                     | Max. 4 axes  | Max. 8 axes   |  |
| Servo amplifie              | er connection method            | SSCNET III/H   |               |  |
| Maximum ove                 | erall cable distance [m]        | 400  | 800           |  |
| Maximum dist                | tance between stations [m]      | 100  |               |  |
| Peripheral I/F              |                                 | Via CPU module (Ethernet)  |               |  |
| Manual pulse<br>function    | generator operation             | Possible to connect 1 modu   | ıle           |  |
| Synchronous function        | encoder operation               | Possible to connect 4 modu<br>interface, via PLC CPU inter<br>interface) |               |  |
|                             | No. of input points             | 4 points   |               |  |
|                             | Input method                    | Positive common/Negative<br>(Photocoupler isolation)                     | common shared |  |
|                             | Rated input voltage/<br>current | 24 V DC/Approx. 5 mA   |               |  |
| Input signals               | Operating voltage range         | 19.2 to 26.4 V DC (24 V DC +10%/-20%, ripple ratio 5% or less)           |               |  |
| (DI)                        | ON voltage/current              | 17.5 V DC or more/3.5 mA   | or more       |  |
|                             | OFF voltage/current             | 7 V DC or less/1.0 mA or les   | SS            |  |
|                             | Input resistance                | Approx. 6.8 kΩ   |               |  |
|                             | Response time                   | 1 ms or less (OFF→ON, ON→OFF)  |               |  |
|                             | Recommended wire size           | AWG24 (0.2 mm²)  |               |  |
|                             | No. of input points             | 1 point  |               |  |
|                             | Input method                    | Positive common/Negative common shared<br>(Photocoupler isolation)       |               |  |
|                             | Rated input voltage/<br>current | 24 V DC/Approx. 5 mA   |               |  |
| Forced stop<br>input signal | Operating voltage range         | 19.2 to 26.4 V DC (24 V DC +10%/-20%, ripple ratio 5% or less)           |               |  |
| (EMI)                       | ON voltage/current              | 17.5 V DC or more/3.5 mA   | or more       |  |
|                             | OFF voltage/current             | 7 V DC or less/1.0 mA or less  |               |  |
|                             | Input resistance                | Approx. 6.8 kΩ   |               |  |
|                             | Response time                   | 4 ms or less (OFF→ON, ON   | →OFF)         |  |
|                             | Recommended wire size           | AWG24 (0.2 mm <sup>2</sup> )   |               |  |

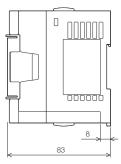
|  |                                |                                     | Specifi  | cations           |  |
|--|--------------------------------|-------------------------------------|--|-------------------|--|
|  |                                |                                     | FX5-40SSC-S  | FX5-80SSC-S       |  |
| oder signal  | Signal input fo                | orm                                 | Phase A/Phase B (magnifica<br>2/magnification by 1), PULS          |                   |  |
|  |                                | Input pulse frequency               | Max. 1 Mpulse/s<br>(After magnification by 4, up                   | to 4 Mpulse/s)    |  |
| Dou  |                                | Pulse width                         | 1 µs or more   |                   |  |
| a snou   | Differential<br>output type    | Leading edge/<br>trailing edge time | 0.25 µs or less  |                   |  |
| Lor  | (26LS31 or                     | Phase difference                    | 0.25 µs or more  |                   |  |
| nct  | equivalent)                    | Rated input voltage                 | 5.5 V DC or less   |                   |  |
| al sy  |                                | High/Low-voltage                    | 2.0 to 5.25 V DC/0 to 0.8 V DC                                     |                   |  |
| ente   |                                | Differential voltage                | ±0.2 V   |                   |  |
| Bme  |                                | Cable length                        | Up to 30 m   |                   |  |
| / Incn   |                                | Input pulse frequency               | Max. 200 kpulse/s<br>(After magnification by 4, up                 | to 800 kpulse/s)  |  |
| tor  |                                | Pulse width                         | 5 µs or more   |                   |  |
| Manual pulse generator / Incremental synchronous encoder | Voltageoutput/                 | Leading edge/<br>trailing edge time | 1.2 µs or less   |                   |  |
| se   | Opencollector<br>type (5 V DC) | Phase difference                    | 1.2 µs or more   |                   |  |
| D  | (3 V DO)                       | Rated input voltage                 | 5.5 V DC or less   |                   |  |
| lanual   |                                | High/Low-voltage                    | 3.0 to 5.25 V DC/2 mA or less, 0 to 1.0 V DC/5 mA or more          |                   |  |
| 2  |                                | Cable length                        | Up to 10 m   |                   |  |
| С  | Compatible CPU module          |                                     | Compatible with FX5U and FX5UC, from their first released products |                   |  |
| Number of occupied input/<br>output points               |                                |                                     | 8 points (Either input or output is available for counting.)       |                   |  |
| Po   | ower supply                    |                                     | 24 V DC +20%/-15% (exterr  | nal power supply) |  |

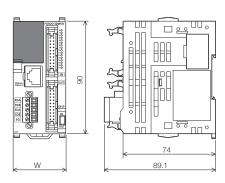
Unit: mm

# **External Dimensions**

#### **CPU** module





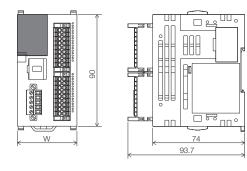


- External color: Main body, Munsell 0.6B7.6/0.2

| Model  | W: mm | W1: mm<br>Mounting hole pitches | MASS (Weight): kg |
|--|-------|---------------------------------|-------------------|
| FX5U-32MR/ES, FX5U-32MT/ES, FX5U-32MT/ESS<br>FX5U-32MR/DS, FX5U-32MT/DS, FX5U-32MT/DSS | 150   | 123                             | Approx. 0.7       |
| FX5U-64MR/ES, FX5U-64MT/ES, FX5U-64MT/ESS<br>FX5U-64MR/DS, FX5U-64MT/DS, FX5U-64MT/DSS | 220   | 193                             | Approx. 1.0       |
| FX5U-80MR/ES, FX5U-80MT/ES, FX5U-80MT/ESS<br>FX5U-80MR/DS, FX5U-80MT/DS, FX5U-80MT/DSS | 285   | 258                             | Approx. 1.2       |



| Model                        | W: mm | MASS (Weight): kg |
|------------------------------|-------|-------------------|
| FX5UC-32MT/D, FX5UC-32MT/DSS | 42.1  | Approx. 0.2       |
| FX5UC-64MT/D, FX5UC-64MT/DSS | 62.2  | Approx. 0.3       |
| FX5UC-96MT/D, FX5UC-96MT/DSS | 82.3  | Approx. 0.35      |

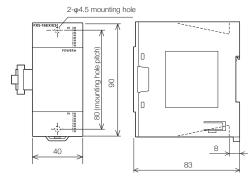


External color: Main body, Munsell 0.6B7.6/0.2
 Accessories: FX2NC-100MPCB type power cable

| Model                               | W: mm | MASS (Weight): kg |
|-------------------------------------|-------|-------------------|
| FX5UC-32MT/DS-TS, FX5UC-32MT/DSS-TS | 48.1  | Approx. 0.25      |
| FX5UC-32MR/DS-TS                    | 68.2  | Approx. 0.35      |

### I/O module

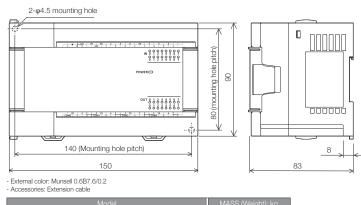
Input module/output module (extension cable type), high-speed pulse input/output module



- External color: Munsell 0.6B7.6/0.2

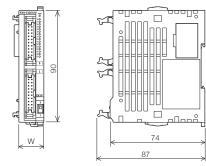
| Model   | MASS (Weight): k |
|---|------------------|
| FX5-8EX/ES, FX5-8EYR/ES, FX5-8EYT/ES,<br>FX5-8EYT/ESS   | Approx. 0.2      |
| FX5-16EX/ES, FX5-16EYR/ES, FX5-16EYT/ES,<br>FX5-16EYT/ESS, FX5-16ER/ES, FX5-16ET/ES,<br>FX5-16ET/ESS, FX5-16ET/ES-H, FX5-16ET/ESS-H | Approx. 0.25     |

#### Powered input/output modules



| Middel   | WADD (WCIght). Kg |
|--|-------------------|
| FX5-32ER/ES, FX5-32ET/ES, FX5-32ET/ESS<br>FX5-32ER/DS, FX5-32ET/DS, FX5-32ET/DSS | Approx. 0.65      |
|  |                   |

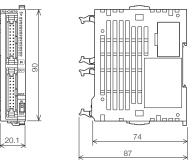
#### Input module/output module (extension connector type)



- External color: Munsell 0.6B7.6/0.2

| Model   | W: mm |              |
|---|-------|--------------|
| FX5-C16EX/D, FX5-C16EX/DS<br>FX5-C16EYT/D, FX5-C16EYT/DSS | 14.6  | Approx. 0.1  |
| FX5-C32EX/D, FX5-C32EX/DS<br>FX5-C32EYT/D, FX5-C32EYT/DSS | 20.1  | Approx. 0.15 |

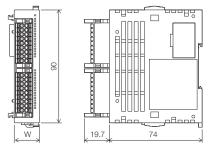
#### I/O module (extension connector type)



- External color: Munsell 0.6B7.6/0.2

| Model                      | MASS (Weight): kg |
|----------------------------|-------------------|
| FX5-C32ET/D, FX5-C32ET/DSS | Approx. 0.15      |

## Input module/output module/I/O module (Spring clamp terminal block type)



- External color: Main body, Munsell 0.6B7.6/0.2

| Model                               | W: mm | MASS (Weight): kg |
|-------------------------------------|-------|-------------------|
| FX5-C16EYR/D-TS                     | 30.7  | Approx. 0.2       |
| FX5-C32EX/DS-TS, FX5-C32EYT/D-TS,   |       |                   |
| FX5-C32EYT/DSS-TS, FX5-C32ET/DS-TS, | 20.1  | Approx. 0.15      |
| FX5-C32ET/DSS-TS                    |       |                   |

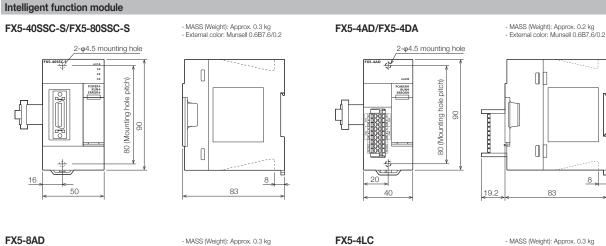
166

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Specifications

# **External Dimensions**

Unit: mm





FX5-4LC

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FX5-4LC ÷ţř

OUT OUT. COM NC

CT CT H A BITG-W B biTG-W C CT CT H A BITG-W 4 B biTG-W

19

FX5-CCL-MS

4

60

2-φ4.5 mounting hole

POWER® RUN® ERROR®

2-φ4.5 mounting hole

80 (Mounting hole pitch)

60

RDO

POWER® RUN® ERROR®

MST0 156K0 625K0 2.5M0 5M0 10M0

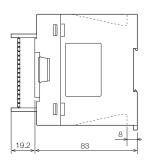
<del>:</del>

50

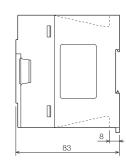
80 (Mounting hole pitch)

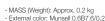
6

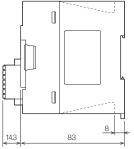
- MASS (Weight): Approx. 0.3 kg - External color: Munsell 0.6B7.6/0.2



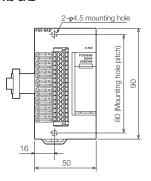
MASS (Weight): Approx. 0.3 kg
 External color: Munsell 0.6B7.6/0.2







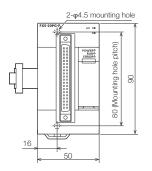
- MASS (Weight): Approx. 0.3 kg - External color: Munsell 0.6B7.6/0.2

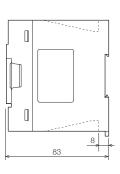


8 19.2 83

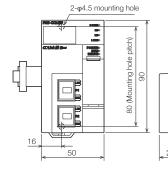
- MASS (Weight): Approx. 0.2 kg - External color: Munsell 0.6B7.6/0.2

## FX5-20PG-P/FX5-20PG-D

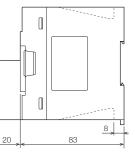




#### FX5-CCLIEF

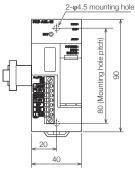




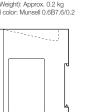


FX5-ASL-M

16

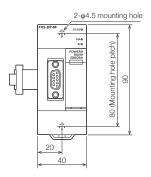






# **External Dimensions**

#### FX5-DP-M



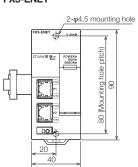


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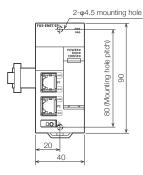
#### FX5-ENET



Unit: mm - MASS (Weight): Approx. 0.2 kg - External color: Munsell 0.6B7.6/0.2



FX5-ENET/IP



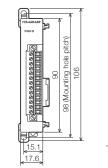
- MASS (Weight): Approx. 0.2 kg - External color: Munsell 0.6B7.6/0.2

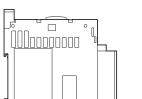
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#### Expansion adapter

FX5-4AD-ADP/FX5-4DA-ADP FX5-4AD-PT-ADP/FX5-4AD-TC-ADP





10000

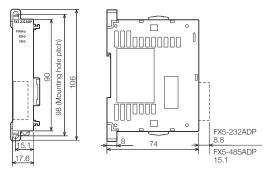
8

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- MASS (Weight): Approx. 0.1 kg - External color: Munsell 0.6B7.6/0.2

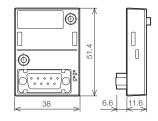
FX5-232ADP/FX5-485ADP

- MASS (Weight): Approx. 0.08 kg - External color: Munsell 0.6B7.6/0.2



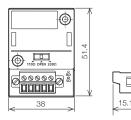
Expansion board

## FX5-232-BD



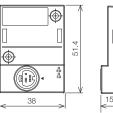
FX5-485-BD

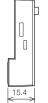
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FX5-422-BD-GOT

- MASS (Weight): Approx. 0.02 kg - External color: Munsell N1.5





10



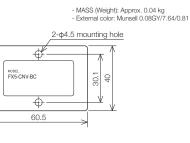
15.4

# **External Dimensions**



#### Connector conversion adapter



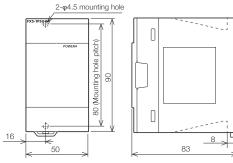


16.4 

#### FX5 extension power supply module

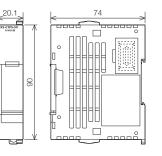


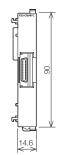
- MASS (Weight): Approx. 0.3 kg - External color: Munsell 0.6B7.6/0.2 - Accessories: Extension cable - M3 terminal screw for terminal block - DIN rail of 35 mm in width can be installed

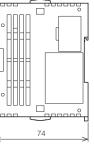


#### FX5-C1PS-5V

MASS (Weight): Approx. 0.1 kg
 External color: Munsell 0.6B7.6/0.2

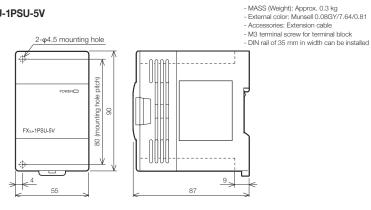






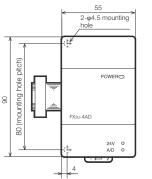
#### FX3 extension power supply module

#### FX3U-1PSU-5V



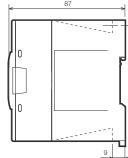
#### FX3 intelligent function module

FX3U-4AD/FX3U-4DA FX3U-64CCL/FX3U-16CCL-M



External color: Munsell 0.08GY/7.64/0.81 - Accessories: Special block No. label, dust sheet, and terminating resistor\*
 - M3 terminal screw for terminal block
 - DIN rail of 35 mm in width can be installed
 \*: Attached only to FX3U-16CCL-M

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| Model                    | MASS (Weight): kg |
|--------------------------|-------------------|
| FX3U-4AD, FX3U-4DA       | Approx. 0.2       |
| FX3U-64CCL, FX3U-16CCL-M | Approx. 0.3       |

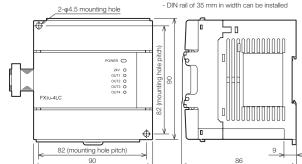
#### FX3U-4LC

10

Specifications

170

Mass (Weight): Approx. 0.4 kg
External color: Munsell 0.08GY/7.64/0.81
M3 terminal screw for terminal block

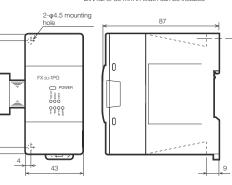


FX3U-1PG

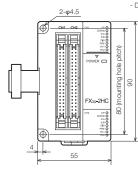
80 (mounting hole pitch)

8

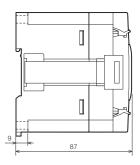
- Mass (Weight): Approx. 0.2 kg - External color: Munsell 0.08GY/7.64/0.81 M3 terminal screw for terminal block
DIN rail of 35 mm in width can be installed



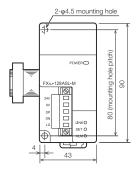
FX3U-2HC



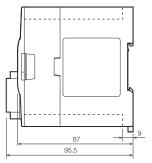
- Mass (Weight): Approx. 0.2 kg External color: Munsell 0.08GY/7.64/0.81
DIN rail of 35 mm in width can be installed



FX3U-128ASL-M



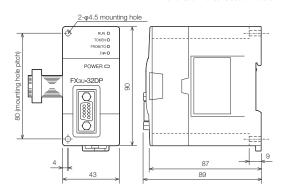
- Mass (Weight): Approx. 0.2 kg - External color: Munsell 0.08GY/7.64/0.81 - DIN rail of 35 mm in width can be installed



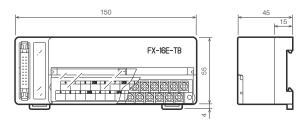
Unit: mm

## FX3U-32DP

Mass (Weight): Approx. 0.2 kg
 External color: Munsell 0.08GY/7.64/0.81



## Terminal module (common to all models)



External color: Munsell 0.08GY/7.64/0.81
 Accessory: Terminal block arrangement card
 M3.5 terminal screw for terminal block
 DIN rail of 35 mm in width can only be installed

# **Terminal arrangement**

# FX5U CPU module

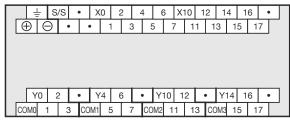
## FX5U-32MR/ES, FX5U-32MT/ES

|   | Ŧ  | S/ | ′S | 0V  | X0  | 2 | 4   | 6  | 5 X  | 10 | 12 | 14  | 1  | 6  | • |
|---|----|----|----|-----|-----|---|-----|----|------|----|----|-----|----|----|---|
| L | Ν  | 1  | ٠  | 24\ | / 1 | 1 | 3   | 5  | 7    | 11 | 1  | 3   | 15 | 17 | · |
|   |    |    |    |     |     |   |     |    |      |    |    |     |    | _  | _ |
|   |    |    |    |     |     |   |     |    |      |    |    |     |    |    |   |
|   |    |    |    |     |     |   |     |    |      |    |    |     |    |    |   |
|   |    |    |    |     |     |   |     |    |      |    |    |     |    |    |   |
|   |    |    |    |     |     |   |     |    |      |    |    |     |    |    |   |
| Г | YO | 2  | 2  | • 1 | Y4  | 6 | •   | Y1 | 10 1 | 2  | •  | Y14 | 1  | 6  | • |
|   | Y0 | 2  | 3  | •   | Y4  |   | 7 0 | Y1 | 10 1 | 2  |    | Y14 | 1  | 6  | • |

# FX5U-32MT/ESS

| _   |     |   |   |   |     |            | _ |   |   |    |    |    |   |   |   |    |    |    |   |   |   |  |
|-----|-----|---|---|---|-----|------------|---|---|---|----|----|----|---|---|---|----|----|----|---|---|---|--|
|     | Y0  | 2 | 2 | • |     | <b>/</b> 4 | 6 | 6 | • | •  | Y٠ | 10 | 1 | 2 | • | •  | Y  | 14 | 1 | 6 | • |  |
| +V0 | ) - | 1 | 3 | 3 | +V1 | 5          | 5 | 7 | 7 | +\ | /2 | 1  | 1 | 1 | 3 | +\ | /3 | 1  | 5 | 1 | 7 |  |

## FX5U-32MR/DS, FX5U-32MT/DS



FX5U-32MT/DSS

| Y0 2 • Y4 6 • Y10 12 • Y      | 14 16 • |  |
|-------------------------------|---------|--|
|                               |         |  |
| +V0 1 3 +V1 5 7 +V2 11 13 +V3 | 15 17   |  |

# FX5U-64MR/ES, FX5U-64MT/ES

| 3 | L ( | S/S | 0V | 0V   | X0 | 2  | 2 | 4   | 6  | X10 | 0 12 | 2 1 | 4 1 | 6 X | 20 2 | 22 2 | 24 2 | 26 X | 30 3 | 32 3 | 34 ; | 36 •   |    |
|---|-----|-----|----|------|----|----|---|-----|----|-----|------|-----|-----|-----|------|------|------|------|------|------|------|--------|----|
| L | N   | •   | 24 | V 24 | 1V | 1  | 3 | 5   | 1  | 7   | 11   | 13  | 15  | 17  | 21   | 23   | 25   | 27   | 31   | 33   | 35   | 37     |    |
|   |     |     |    |      |    |    |   |     |    |     |      |     |     |     |      |      |      |      |      |      |      |        |    |
|   |     |     |    |      |    |    |   |     |    |     |      |     |     |     |      |      |      |      |      |      |      |        |    |
|   |     |     |    |      |    |    |   |     |    |     |      |     |     |     |      |      |      |      |      |      |      |        |    |
|   |     |     |    |      |    |    |   |     |    |     |      |     |     |     |      |      |      |      |      |      |      |        |    |
| Γ | ′0  | 2   | •  | Y4   | 6  | 1. |   | /10 | 12 | •   | Y1   | 4 1 | 6   | • Y | 20 2 | 2 2  | 24 2 | 26 Y | 30 3 | 32 3 | 34 ; | 36 COI | M5 |

# FX5U-64MT/ESS

|      |   |   | _ |   |   |   | _   |    | <br> |     |    | _ | _  |    |    |    |    |   |      |      |    |    |     |
|------|---|---|---|---|---|---|-----|----|------|-----|----|---|----|----|----|----|----|---|------|------|----|----|-----|
| Y0   | 2 | • | Y | 4 | 6 | ٠ | Y10 | 12 | • \  | Y14 | 16 | • | Y2 | 20 | 22 | 24 | 26 | Y | 30 3 | 32 3 | 34 | 36 | +V5 |
| V0 1 |   |   |   |   |   |   |     |    |      |     |    |   |    |    |    |    |    |   |      |      |    |    |     |

# FX5U-64MR/DS, FX5U-64MT/DS

| Γ | Ļ        | - | S/S | • |     | •    | X0 | 2   | 4   | 1  | 6  | X10  | 12  | 2 - | 14 | 16   | X20  | 22  | 24   | 26   | X30  | 32  | 34  | 36   | ٠    |     |
|---|----------|---|-----|---|-----|------|----|-----|-----|----|----|------|-----|-----|----|------|------|-----|------|------|------|-----|-----|------|------|-----|
|   | $\oplus$ | e | )   | • | ٠   | •    |    | 1   | 3   | 5  | 7  | 7    | 11  | 13  | 15 | 5 1  | 7 2  | 1 2 | 23 2 | 25 2 | 27 3 | 1 3 | 3 3 | 35 3 | 7    |     |
|   |          |   |     |   |     |      |    |     |     |    |    |      |     |     |    |      |      |     |      |      |      |     |     |      |      |     |
|   |          |   |     |   |     |      |    |     |     |    |    |      |     |     |    |      |      |     |      |      |      |     |     |      |      |     |
|   |          |   |     |   |     |      |    |     |     |    |    |      |     |     |    |      |      |     |      |      |      |     |     |      |      |     |
|   | Y        | 0 | 2   | • | ·   | Y4   | 6  | •   | Y   | 10 | 12 | ٠    | Y1  | 4   | 16 | ٠    | Y20  | 22  | 24   | 26   | Y30  | 32  | 34  | 36   | COM5 |     |
|   | COM0     | 1 | Т   | 3 | CON | 11 5 | 5  | 7 0 | OM2 | 11 | 1  | 3 C( | ОМЗ | 15  | 17 | 7 CO | M4 2 | 1 2 | 23 2 | 25 2 | 7 3  | 1 3 | 3 3 | 35 3 | 7    | · . |

# FX5U-64MT/DSS

| Y   | 0 | 2 | ٠   | Y4  |   | 6 | •  | Y10  | 12  | • | Y   | 14 | 16  | •   | Y2  | 20 | 22 | 24 | 2  | 6  | ′30 | 32  | 3  | 4 3 | 6 · | +V5 |
|-----|---|---|-----|-----|---|---|----|------|-----|---|-----|----|-----|-----|-----|----|----|----|----|----|-----|-----|----|-----|-----|-----|
| +V0 | 1 | 3 | 3 + | ·V1 | 5 | 7 | +\ | /2 1 | 1 1 | 3 | +V3 | 15 | 5 1 | 7 - | -V4 | 21 | 2  | 3  | 25 | 27 | 3   | 1 : | 33 | 35  | 37  |     |

# FX5U CPU module

# FX5U-80MR/ES, FX5U-80MT/ES

|          |     |     | -, |      |    |        |    |     |    |    |     |      |     |    |     |                    |     |     |    |    |     |    |     |     |     |      |        |     |     |      |     |     |     |   |   |
|----------|-----|-----|----|------|----|--------|----|-----|----|----|-----|------|-----|----|-----|--------------------|-----|-----|----|----|-----|----|-----|-----|-----|------|--------|-----|-----|------|-----|-----|-----|---|---|
| 늰        | - 5 | S/S | 0V | 0V   | X  | 0      | 2  | 4   | 6  | X  | 10  | 12   | 14  | 1  | 6   |                    | ٠   | X20 | 22 | 2  | 4 2 | 6  | •   | X30 | ) 3 | 2 34 | 4 36   | 3   | •   | X40  | 42  | 44  | 46  | • |   |
| Ĺ        | Ν   | •   | 24 | 1V 2 | 4V | 1      | 3  | 3   | 5  | 7  | 11  | 1    | 3 . | 15 | _   | 1                  | 7   | •   | 21 | 23 | 25  | 27 | , , | •   | 31  | 33   | 35     | 37  | •   | 4    | 1 4 | 3 4 | 5 4 | 7 |   |
|          |     |     |    |      |    |        |    |     |    |    |     |      |     |    |     |                    |     |     |    |    |     |    |     |     |     |      |        |     |     |      |     |     |     |   |   |
|          |     |     |    |      |    |        |    |     |    |    |     |      |     |    |     |                    |     |     |    |    |     |    |     |     |     |      |        |     |     |      |     |     |     |   |   |
|          |     |     |    |      |    |        |    |     |    |    |     |      |     |    |     |                    |     |     |    |    |     |    |     |     |     |      |        |     |     |      |     |     |     |   |   |
| <b>—</b> |     |     |    |      | 1  | -      |    |     | 1  | _  |     |      |     | -  |     |                    |     |     | 1  | -  |     |    |     |     |     |      |        |     |     |      |     |     |     | _ | 1 |
| Y        | 0   | 2   | •  | Y4   | 6  | ;<br>_ | •  | Y10 | 12 | 2  | •   | Y14  | 16  | -  |     | Y20                | 22  | 24  | 26 |    |     |    | •   | Y30 | ) 3 | 2 34 | 4   36 | 5 L | •   | Y40  | 42  | 44  | 46  | • |   |
| COM0     | 1   | 3   | CC | M1   | 5  | 7      | CO | M2  | 11 | 13 | CON | 13 1 | 5   | 17 | CON | /14 2 <sup>.</sup> | 1 2 | 3   | 25 |    | 27  | •  | CC  | DM5 | 31  | 33   | 35     | 37  | COI | M6 4 | 1 4 | 3 4 | 5 4 | 7 |   |
|          |     |     |    |      |    |        |    |     |    |    |     |      |     |    |     |                    |     |     |    |    |     |    |     |     |     |      |        |     |     |      |     |     |     |   |   |

## FX5U-80MT/ESS

| Y   | /0 | 2 | •    | Y4   | 6   | •    | Y10  | 12   | •    | Y14  | 16  | •   | Y20  | 22  | 24 | 26 | ]    | •  | Т | •  | Y30  | 32   | 34   | 36 | •   | Y40  | 42  | 44   | 46   | •  | ٦ |
|-----|----|---|------|------|-----|------|------|------|------|------|-----|-----|------|-----|----|----|------|----|---|----|------|------|------|----|-----|------|-----|------|------|----|---|
| +V0 | 1  | 3 | 3 +\ | /1 5 | 5 7 | 7 +\ | /2 1 | 11 1 | 3 +\ | /3 1 | 5 1 | 7 + | V4 2 | 1 2 | 3  | 25 | í [: | 27 | • | +V | 5 31 | 1 33 | 3 35 | 3  | 7 + | V6 4 | 1 4 | 13 4 | 15 4 | 17 | _ |

## FX5U-80MR/DS, FX5U-80MT/DS

| Ļ        | - S | /S | ٠  | ٠    | X0 | 2   | 4    | 4  | 6  | X10  | 12    | 14  | 16   |       | •   | X2( | 0 22 | 2   | 4 26 | 3  | • ) | (30  | 32 3 | 34 ; | 36 | •   | X40  | 42  | 44  | 46  | ٠ |     |
|----------|-----|----|----|------|----|-----|------|----|----|------|-------|-----|------|-------|-----|-----|------|-----|------|----|-----|------|------|------|----|-----|------|-----|-----|-----|---|-----|
| $\oplus$ | Θ   | ŀ  | •  |      | •  | 1   | 3    | 5  | 7  | 7 1  | 1 1   | 3 1 | 5    | 1     | 7   | •   | 21   | 23  | 25   | 27 | ٠   | 31   | 33   | 35   | 37 | •   | 4    | 1 4 | 3 4 | 5 4 | 7 |     |
|          |     |    |    |      |    |     |      |    |    |      |       |     |      |       |     |     |      |     |      |    |     |      |      |      |    |     |      |     |     |     |   |     |
|          |     |    |    |      |    |     |      |    |    |      |       |     |      |       |     |     |      |     |      |    |     |      |      |      |    |     |      |     |     |     |   |     |
|          |     |    |    |      |    |     |      |    |    |      |       |     |      |       |     |     |      |     |      |    |     |      |      |      |    |     |      |     |     |     |   |     |
| Y        | D I | 2  | •  | Y4   | 6  | •   | Y    | 10 | 12 | ٠    | Y14   | 16  | •    | Y20   | 22  | 24  | 1 26 |     | ·    | -  | •   | /30  | 32 3 | 34 ; | 36 | •   | Y40  | 42  | 44  | 46  | • | ]   |
| сомо     | 1   | 3  | CO | M1 5 | 5  | 7 ( | COM2 | 11 | 1  | 3 00 | DM3 1 | 5 1 | 7 00 | DM4 2 | 1 2 | 3   | 25   | - 1 | 27   | •  | СОМ | 5 31 | 33   | 35   | 37 | CON | 16 4 | 1 4 | 3 4 | 5 4 | 7 | r ! |

## FX5U-80MT/DSS

| Y   | /0 | 2 | ٠    | Y4   | 6   | •                | Y10  | 12   | ٠    | Y14  | 16  | •    | Y20  | 22   | 24  | 26 |   | ŀ  | •   | Y30  | 32  | 34  | 36  | •   | Y40  | 42  | 44   | 46 | ŀ  | ۱۱ |
|-----|----|---|------|------|-----|------------------|------|------|------|------|-----|------|------|------|-----|----|---|----|-----|------|-----|-----|-----|-----|------|-----|------|----|----|----|
| +V0 | 1  |   | 3 +1 | /1 5 | 5 7 | 7 + <sup>1</sup> | /2 1 | 1 13 | 3 +1 | /3 1 | 5 1 | 7 +' | V4 2 | 21 2 | 3 2 | 5  | 1 | 27 | • + | V5 3 | 1 3 | 3 3 | 5 3 | 7 + | V6 4 | 1 4 | 13 4 | 45 | 47 |    |

# FX5UC CPU module

| FX5UC-32MT/D   | FX5UC-32MT/DSS  | FX5UC-32MT/DS-TS   | FX5UC-32MT/DSS-TS  | FX5UC-32MR/DS-TS   |
|--|---|--|--|--|
| Input           X0         X10           X1         X11           X2         X12           X3         X13           X4         X14           X5         X15           X6         X16           X7         X17           COM         COM    | Input           X0         X10           X1         X11           X2         X12           X3         X13           X4         X14           X5         X15           X6         X16           X7         X17           COM0         COM0           •         • | X5 X15<br>X6 X16<br>X7 X17<br>S/S S/S  | Input           X0         X10           X1         X11           X2         X12           X3         X13           X4         X14           X5         X15           X6         X16           X7         X17           S/S         S/S  | Input*         Input*           X0         X0         X10         X10           X1         X1         X11         X11           X2         X2         X12         X12           X3         X3         X13         X13           X4         X4         X14         X14           X5         X5         X15         X15           X6         X6         X16         X16           X7         X7         X17         X17           S/S0         S/S0         S/S1         S/S1                                  |
| Output           Y0         Y10           Y1         Y11           Y2         Y12           Y3         Y13           Y4         Y14           Y5         Y15           Y6         Y16           Y7         Y17           COM0         COM0 | Output           Y0         Y10           Y1         Y11           Y2         Y12           Y3         Y13           Y4         Y14           Y5         Y15           Y6         Y16           Y7         Y17           +V0         +V0           •         •  | Output           Y0         Y10           Y1         Y11           Y2         Y12           Y3         Y13           Y4         Y14           Y5         Y15           Y6         Y16           Y7         Y17           COM0         COM0 | Output           Y0         Y10           Y1         Y11           Y2         Y12           Y3         Y13           Y4         Y14           Y5         Y15           Y6         Y16           Y7         Y17           +V0         +V0 | Output*         Output*           Y0         Y0           Y1         Y1           Y1         Y11           Y12         Y12           Y3         Y3           Y4         Y4           Y15         Y15           Y6         Y6           Y16         Y16           Y17         Y17           COM0         COM0 |

## FX5UC-64MT/D

# FX5UC-64MT/DSS

| F)   | (5UC- | 6 | 54MT/ | D    |   |       |
|------|-------|---|-------|------|---|-------|
| Inp  | out   |   | Inp   | out  |   |       |
| X0   | X10   |   | X20   | X30  |   |       |
| X1   | X11   |   | X21   | X31  |   |       |
| X2   | X12   |   | X22   | X32  |   |       |
| X3   | X13   |   | X23   | X33  |   |       |
| X4   | X14   |   | X24   | X34  |   | Notch |
| X5   | X15   |   | X25   | X35  | J |       |
| X6   | X16   |   | X26   | X36  |   |       |
| X7   | X17   |   | X27   | X37  |   |       |
| COM  | COM   |   | COM   | COM  |   |       |
| •    | •     |   | •     | •    |   |       |
| Out  | put   |   | Out   | put  |   |       |
| Y0   | Y10   |   | Y20   | Y30  |   |       |
| Y1   | Y11   |   | Y21   | Y31  |   |       |
| Y2   | Y12   |   | Y22   | Y32  |   |       |
| Y3   | Y13   |   | Y23   | Y33  |   |       |
| Y4   | Y14   | l | Y24   | Y34  | 4 | Notch |
| Y5   | Y15   |   | Y25   | Y35  | J |       |
| Y6   | Y16   |   | Y26   | Y36  |   |       |
| Y7   | Y17   |   | Y27   | Y37  |   |       |
| COM0 | COM0  |   | COM1  | COM1 |   |       |
| •    | •     |   | •     | •    |   |       |
|      |       |   |       |      | _ | 1     |

| Inp  | out  | Inp  | out  | ]     |
|------|------|------|------|-------|
| X0   | X10  | X20  | X30  | ]     |
| X1   | X11  | X21  | X31  |       |
| X2   | X12  | X22  | X32  |       |
| X3   | X13  | X23  | X33  | ]     |
| X4   | X14  | X24  | X34  | Notch |
| X5   | X15  | X25  | X35  |       |
| X6   | X16  | X26  | X36  |       |
| X7   | X17  | X27  | X37  | ]     |
| COM0 | COM0 | COM1 | COM1 |       |
| •    | •    | •    | •    | ]     |
| Out  | put  | Outp | out  |       |
| Y0   | Y10  | Y20  | Y30  | ]     |
| Y1   | Y11  | Y21  | Y31  | 11    |
| Y2   | Y12  | Y22  | Y32  | 11    |
| Y3   | Y13  | Y23  | Y33  | ]     |
| Y4   | Y14  | Y24  | Y34  | Notch |
| Y5   | Y15  | Y25  | Y35  |       |
| Y6   | Y16  | Y26  | Y36  |       |
| Y7   | Y17  | Y27  | Y37  | 11    |
| +V0  | +V0  | +V1  | +V1  | ]     |
| •    | •    | •    | •    | ]     |
|      |      |      |      |       |

# **Terminal arrangement**

## FX5UC-96MT/D

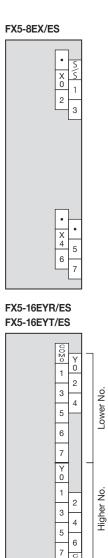
| In  | put   | In   | out  |   | Inp  | out  |       |
|-----|-------|------|------|---|------|------|-------|
| X0  | X10   | X20  | X30  |   | X40  | X50  |       |
| X1  | X11   | X21  | X31  |   | X41  | X51  |       |
| X2  | X12   | X22  | X32  |   | X42  | X52  |       |
| X3  | X13   | X23  | X33  |   | X43  | X53  |       |
| X4  | X14   | X24  | X34  | 1 | X44  | X54  | Notch |
| X5  | X15   | X25  | X35  |   | X45  | X55  |       |
| X6  | X16   | X26  | X36  |   | X46  | X56  |       |
| X7  | X17   | X27  | X37  |   | X47  | X57  |       |
| CON | 1 COM | COM  | COM  |   | COM  | COM  |       |
| •   | •     | •    | •    |   | •    | •    |       |
| Ou  | tput  | Out  | put  |   | Out  | put  |       |
| Y0  | Y10   | Y20  | Y30  |   | Y40  | Y50  |       |
| Y1  | Y11   | Y21  | Y31  |   | Y41  | Y51  |       |
| Y2  | Y12   | Y22  | Y32  |   | Y42  | Y52  |       |
| Y3  | Y13   | Y23  | Y33  |   | Y43  | Y53  |       |
| Y4  | Y14   | Y24  | Y34  | 1 | Y44  | Y54  | Notch |
| Y5  | Y15   | Y25  | Y35  |   | Y45  | Y55  |       |
| Y6  | Y16   | Y26  | Y36  | Ι | Y46  | Y56  |       |
| Y7  | Y17   | Y27  | Y37  |   | Y47  | Y57  |       |
| COM | COM0  | COM1 | COM1 |   | COM2 | COM2 |       |
| Ŀ   | •     | •    | •    |   | •    | •    |       |

## FX5UC-96MT/DSS

|   | Ing  | out  | Ing  | out  | In   | out  |   |          |
|---|--|--|--|--|--|--|---|----------|
| Γ | X0   | X10  | X20  | X30  | X40  | X50  | 1 |          |
|   | X1   | X11  | X21  | X31  | X41  | X51  |   |          |
|   | X2   | X12  | X22  | X32  | X42  | X52  |   |          |
|   | X3   | X13  | X23  | X33  | X43  | X53  |   |          |
|   | X4   | X14  | X24  | X34  | X44  | X54  |   | Notch    |
|   | X5   | X15  | X25  | X35  | X45  | X55  |   |          |
|   | X6   | X16  | X26  | X36  | X46  | X56  |   |          |
|   | X7   | X17  | X27  | X37  | X47  | X57  |   |          |
|   | COM0   | COM0   | COM1   | COM1   | COM2   | COM2   |   |          |
|   | •  | •  | •  | •  | •  | •  |   |          |
|   | Out  | nut  | Out  | nut.   | 0.1  |  |   |          |
|   |  | pui  | Out  | pui  | Out  | put  |   |          |
|   | Y0   | Y10  | Y20  | Y30  | Y40  | Y50  | 1 |          |
|   |  | <u> </u>   |  | <u> </u>   |  |  |   |          |
|   | Y0   | Y10  | Y20  | Y30  | Y40  | Y50  |   |          |
|   | Y0<br>Y1                                     | Y10<br>Y11   | Y20<br>Y21   | Y30<br>Y31   | Y40<br>Y41   | Y50<br>Y51   |   |          |
|   | Y0<br>Y1<br>Y2                               | Y10<br>Y11<br>Y12                                    | Y20<br>Y21<br>Y22                                    | Y30<br>Y31<br>Y32                                    | Y40<br>Y41<br>Y42                                    | Y50<br>Y51<br>Y52                                    |   | _∕ Notch |
|   | Y0<br>Y1<br>Y2<br>Y3                         | Y10<br>Y11<br>Y12<br>Y13                             | Y20<br>Y21<br>Y22<br>Y23                             | Y30<br>Y31<br>Y32<br>Y33                             | Y40<br>Y41<br>Y42<br>Y43                             | Y50<br>Y51<br>Y52<br>Y53                             |   | _∕ Notch |
|   | Y0<br>Y1<br>Y2<br>Y3<br>Y4                   | Y10<br>Y11<br>Y12<br>Y13<br>Y14                      | Y20<br>Y21<br>Y22<br>Y23<br>Y24                      | Y30<br>Y31<br>Y32<br>Y33<br>Y34                      | Y40<br>Y41<br>Y42<br>Y43<br>Y44                      | Y50<br>Y51<br>Y52<br>Y53<br>Y54                      |   | - Notch  |
|   | Y0<br>Y1<br>Y2<br>Y3<br>Y4<br>Y5             | Y10<br>Y11<br>Y12<br>Y13<br>Y14<br>Y15               | Y20<br>Y21<br>Y22<br>Y23<br>Y24<br>Y25               | Y30<br>Y31<br>Y32<br>Y33<br>Y34<br>Y35               | Y40<br>Y41<br>Y42<br>Y43<br>Y44<br>Y45               | Y50<br>Y51<br>Y52<br>Y53<br>Y54<br>Y55               |   | _∕ Notch |
|   | Y0<br>Y1<br>Y2<br>Y3<br>Y4<br>Y5<br>Y6       | Y10<br>Y11<br>Y12<br>Y13<br>Y14<br>Y15<br>Y16        | Y20<br>Y21<br>Y22<br>Y23<br>Y24<br>Y25<br>Y26        | Y30<br>Y31<br>Y32<br>Y33<br>Y34<br>Y35<br>Y36        | Y40<br>Y41<br>Y42<br>Y43<br>Y44<br>Y45<br>Y46        | Y50<br>Y51<br>Y52<br>Y53<br>Y54<br>Y55<br>Y56        |   | - Notch  |
|   | Y0<br>Y1<br>Y2<br>Y3<br>Y4<br>Y5<br>Y6<br>Y7 | Y10<br>Y11<br>Y12<br>Y13<br>Y14<br>Y15<br>Y16<br>Y17 | Y20<br>Y21<br>Y22<br>Y23<br>Y24<br>Y25<br>Y26<br>Y27 | Y30<br>Y31<br>Y32<br>Y33<br>Y34<br>Y35<br>Y36<br>Y37 | Y40<br>Y41<br>Y42<br>Y43<br>Y44<br>Y45<br>Y46<br>Y47 | Y50<br>Y51<br>Y52<br>Y53<br>Y54<br>Y55<br>Y56<br>Y57 |   | - Notch  |

# I/O module

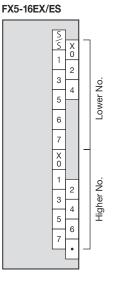
# ◇ Input module/output module (extension cable type)



4

6

COM 1



FX5-16EYT/ESS

+ V 0

1 3 5

6 7

Y 0

1 3 5

7

2

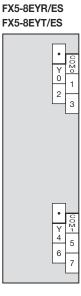
4

6

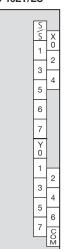
+ V 1

Lower No.

Higher No.



FX5-16ER/ES FX5-16ET/ES





FX5-16ET/ESS

| S           |             |
|-------------|-------------|
| S/S         | X<br>0      |
|             | X<br>0<br>2 |
| 3           | 4           |
| 5           |             |
| 6           |             |
| 7           |             |
| 7<br>Y<br>0 |             |
| 1           |             |
| 3           | 2           |
|             | 4           |
| 5           | 6           |
| 7           | +           |
|             | +<br>V      |
|             |             |

# $\diamond$ High-speed pulse input/output module

| FX5-16ET/ES-H  | FX5-16ET/ESS-H   |
|--|--|
| S X<br>1 0<br>3 4<br>5<br>6<br>7<br>Y<br>0<br>1<br>2<br>3 4<br>5<br>6<br>7<br>C<br>M | S<br>S<br>X<br>1<br>2<br>3<br>4<br>5<br>6<br>7<br>7<br>9<br>0<br>1<br>2<br>3<br>4<br>5<br>6<br>7<br>7<br>4<br>5<br>6<br>7<br>7<br>4<br>5<br>7<br>9<br>0<br>1<br>2<br>3<br>4<br>5<br>7<br>7<br>9<br>0<br>7<br>9<br>7<br>0<br>7<br>9<br>7<br>9<br>7<br>9<br>7<br>9<br>7<br>9<br>7<br>9 |

# ◇ Powered input/output modules

## FX5-32ER/ES, FX5-32ET/ES

| Ŧ  | S | /S | 0V | X  | 0  | 2 | 4 | 4 | 6  | X | 0 | 2 | 4  | ļ. | 6 | ;   | ٠ |   |
|----|---|----|----|----|----|---|---|---|----|---|---|---|----|----|---|-----|---|---|
|    | V | ٠  | 2  | 4V | 1  |   | 3 | 5 | 5  | 7 | 1 |   | 3  | 5  | ; | 7   |   | _ |
|    |   |    |    |    |    |   |   |   |    |   |   |   |    |    |   |     |   |   |
|    |   |    |    |    |    |   |   |   |    |   |   |   |    |    |   |     |   |   |
|    |   |    |    |    |    |   |   |   |    |   |   |   |    |    |   |     |   |   |
|    |   |    |    |    |    |   |   |   |    |   |   |   |    |    |   |     |   |   |
| Y0 | 2 | 2  | •  | Υ  | '4 | 6 | Ŀ | • | Y0 | 2 | - | • | Y. | 4  | 6 | ; [ | • |   |

#### FX5-32ET/ESS

| _  |   |   |   |   |   |    |   |     |   |   |    |    |   |   |   |   |    |   |   |   |   |   |   |  |
|----|---|---|---|---|---|----|---|-----|---|---|----|----|---|---|---|---|----|---|---|---|---|---|---|--|
|    | Y | D | 2 | 2 | • |    | Y | 4 ( | 6 | - | •  | Y  | 0 | 2 | 2 | • | ,  | Υ | 4 | 6 | 3 | • | , |  |
| +V | 0 | 1 |   | 3 | ; | +V | 1 | 5   | 7 | 7 | +\ | /2 | 1 |   | 3 | 3 | +V | 3 | 5 | ; | 7 |   |   |  |

## FX5-32ER/DS, FX5-32ET/DS

| Ŧ  | S/3 | s | •   | X0 | 2   | 4 | ł    | 6  | XC | ) 2 | 2 | 4  |   | 6 | • |   |
|----|-----|---|-----|----|-----|---|------|----|----|-----|---|----|---|---|---|---|
| ÐE | Ð   | • | •   | -  | 1 : | 3 | 5    | 7  | 7  | 1   | 3 | 3  | 5 |   | 7 | _ |
|    |     |   |     |    |     |   |      |    |    |     |   |    |   |   |   |   |
|    |     |   |     |    |     |   |      |    |    |     |   |    |   |   |   |   |
|    |     |   |     |    |     |   |      |    |    |     |   |    |   |   |   |   |
|    |     |   |     |    |     |   |      |    |    |     |   |    |   |   |   |   |
| YO | 2   | - | • 1 | Y4 | 6   |   | . T. | YO | 2  | 1.  | • | Y4 | 1 | 6 | • | 7 |

## FX5-32ET/DSS

| _  |   |   |   |   |   |    |    |   |   | _ |    | _  |   |   |   |   |    |    |   |   |   |   |   |   |
|----|---|---|---|---|---|----|----|---|---|---|----|----|---|---|---|---|----|----|---|---|---|---|---|---|
| [  | Y | 0 | 2 | 2 | • | •  | Y  | 4 | 6 |   | •  | Y  | 0 | 2 | 2 | • | ,  | Υ  | 4 | 6 | 3 | • | • |   |
| +V | 0 | 1 |   | 3 | } | +V | '1 | 5 |   | 7 | +\ | /2 | 1 | 1 | 3 | 3 | +V | /3 | 5 | 5 | 7 | ' |   | · |

# I/O module

# ◇ Input module/output module (extension connector type)

| F | X5-C | 16EX | /D    |
|---|------|------|-------|
|   | In   | out  | 7     |
|   | X0   | X0   |       |
|   | X1   | X1   |       |
|   | X2   | X2   |       |
|   | X3   | X3   |       |
|   | X4   | X4   | Notch |
|   | X5   | X5   |       |
|   | X6   | X6   |       |
|   | X7   | X7   |       |
|   | COM  | COM  |       |
|   | •    | •    |       |
|   |      |      |       |

| F | (5-C1 | 6EX/ | D  | S      |
|---|-------|------|----|--------|
|   | In    | out  | ٦  |        |
|   | X0    | X0   |    |        |
|   | X1    | X1   |    |        |
|   | X2    | X2   |    |        |
|   | X3    | X3   |    |        |
|   | X4    | X4   | ]] | /Notch |
|   | X5    | X5   |    |        |
|   | X6    | X6   |    |        |
|   | X7    | X7   |    |        |
|   | COM0  | COM0 |    |        |
|   | •     | •    |    |        |

| F          | X5-C32EX/D  | FX5-C32EX/DS   | FX5-C32EX/DS-TS   |
|------------|---|--|---|
| Lower No.  | Input           X0         X0           X1         X1           X2         X2           X3         X3           X4         X4           X5         X5           X6         X6           X7         X7           COM COM         • | or Nu hotch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Notch<br>Not | Input           X0         X10           X1         X11           X2         X12           X3         X13           X4         X14           X5         X15           X6         X16           X7         X17           S/S         S/S           Input |
| Higher No. | Input           X0         X0           X1         X1           X2         X2           X3         X3           X4         X4           X5         X5           X6         X6           X7         X7           COM COM         • | Input           X0         X0           X1         X1           X2         X2           X3         X3           X4         X4           X5         X5           X6         X6           X7         X7           COM1 COM1         .  | X0         X10           X1         X11           X2         X12           X3         X13           X4         X14           X5         X15           X6         X16           X7         X17           S/S         S/S                                 |

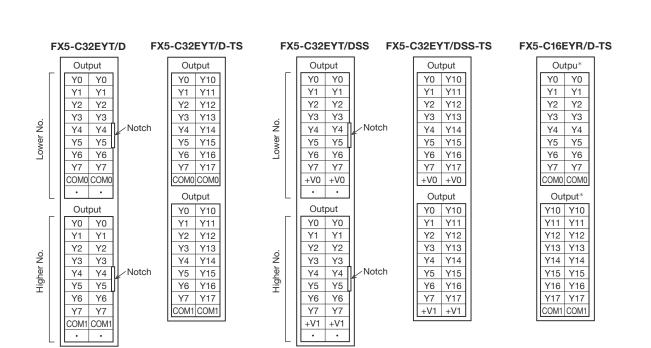
# **Terminal arrangement**

#### FX5-C16EYT/D

| Out | put                              |   |   |
|-----|----------------------------------|---|---|
| 1/0 |                                  |   |   |
| ΥÜ  | Y0                               |   |   |
| Y1  | Y1                               |   |   |
| Y2  | Y2                               |   |   |
| Y3  | Y3                               |   |   |
| Y4  | Y4                               | ]   | Notch   |
| Y5  | Y5                               |   |   |
| Y6  | Y6                               |   |   |
| Y7  | Y7                               |   |   |
| OM0 | COM0                             |   |   |
| •   | •                                |   |   |
|     | Y2<br>Y3<br>Y4<br>Y5<br>Y6<br>Y7 | Y1         Y1           Y2         Y2           Y3         Y3           Y4         Y4           Y5         Y5           Y6         Y6 | Y1         Y1           Y2         Y2           Y3         Y3           Y4         Y4           Y5         Y5           Y6         Y6           Y7         Y7 |

### FX5-C16EYT/DSS

| Γ | Out | put | 7     |
|---|-----|-----|-------|
|   | Y0  | Y0  | ]     |
| Ш | Y1  | Y1  | ]     |
| Ш | Y2  | Y2  | ]     |
| Ш | Y3  | Y3  | 11    |
| Ш | Y4  | Y4  | Notch |
| Ш | Y5  | Y5  |       |
| Ш | Y6  | Y6  | ]     |
| Ш | Y7  | Y7  | ]     |
| Ш | +V0 | +V0 | ]     |
|   | •   | •   | ]     |



# ◇ I/O module (extension connector type)

# FX5-C32ET/D

#### FX5-C32ET/DSS Г ٦

| FX5-C32ET/DSS-TS |
|------------------|
|------------------|

| ~         | 0.00  |      | 1 |       |  |
|-----------|-------|------|---|-------|--|
|           | Input |      |   |       |  |
| $ \Gamma$ | X0    | X0   |   |       |  |
|           | X1    | X1   |   |       |  |
| [[        | X2    | X2   |   |       |  |
| [[        | ХЗ    | X3   |   |       |  |
|           | X4    | X4   | ] | Notch |  |
|           | X5    | X5   |   |       |  |
|           | X6    | X6   |   |       |  |
|           | X7    | X7   |   |       |  |
|           | COM   | COM  |   |       |  |
| IL        | •     | •    |   |       |  |
|           | Out   | put  |   |       |  |
|           | Y0    | Y0   |   |       |  |
|           | Y1    | Y1   |   |       |  |
|           | Y2    | Y2   |   |       |  |
|           | Y3    | Y3   |   |       |  |
|           | Y4    | Y4   |   | Notch |  |
|           | Y5    | Y5   |   |       |  |
|           | Y6    | Y6   |   |       |  |
|           | Y7    | Y7   |   |       |  |
| C         | OMO   | COM0 |   |       |  |
| IL        | •     | •    |   |       |  |
|           |       |      | - |       |  |

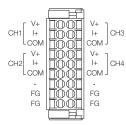
| 5-C32ET/DS |      |  |  |  |  |
|------------|------|--|--|--|--|
| Input      |      |  |  |  |  |
| X0         | X10  |  |  |  |  |
| X1         | X11  |  |  |  |  |
| X2         | X12  |  |  |  |  |
| X3         | X13  |  |  |  |  |
| X4         | X14  |  |  |  |  |
| X5         | X15  |  |  |  |  |
| X6         | X16  |  |  |  |  |
| X7         | X17  |  |  |  |  |
| S/S        | S/S  |  |  |  |  |
| Output     |      |  |  |  |  |
| Y0         | Y10  |  |  |  |  |
| Y1         | Y11  |  |  |  |  |
| Y2         | Y12  |  |  |  |  |
| Y3         | Y13  |  |  |  |  |
| Y4         | Y14  |  |  |  |  |
| Y5         | Y15  |  |  |  |  |
| Y6         | Y16  |  |  |  |  |
| Y7         | Y17  |  |  |  |  |
| COM0       | COM0 |  |  |  |  |

|   | Inp  |      |       |
|---|------|------|-------|
|   | X0   | X0   | ]     |
|   | X1   | X1   |       |
|   | X2   | X2   |       |
|   | X3   | X3   |       |
|   | X4   | X4   | Notch |
|   | X5   | X5   | ļΙ    |
|   | X6   | X6   |       |
|   | X7   | X7   |       |
|   | COM0 | COMO |       |
|   | •    | •    |       |
|   | Out  | put  |       |
|   | Y0   | Y0   | ]     |
|   | Y1   | Y1   | ]     |
|   | Y2   | Y2   | ]     |
|   | Y3   | Y3   |       |
|   | Y4   | Y4   | Notch |
|   | Y5   | Y5   | JI    |
|   | Y6   | Y6   |       |
|   | Y7   | Y7   |       |
|   | +V0  | +V0  |       |
|   | •    | •    | ]     |
| 1 |      |      |       |

| (5-C32E1/DSS |        |     |  |  |
|--------------|--------|-----|--|--|
| Γ            | Input  |     |  |  |
|              | X0     | X10 |  |  |
|              | X1     | X11 |  |  |
|              | X2     | X12 |  |  |
|              | X3     | X13 |  |  |
|              | X4     | X14 |  |  |
|              | X5     | X15 |  |  |
|              | X6     | X16 |  |  |
|              | X7     | X17 |  |  |
|              | S/S    | S/S |  |  |
|              | Output |     |  |  |
|              | Y0     | Y10 |  |  |
|              | Y1     | Y11 |  |  |
|              | Y2     | Y12 |  |  |
|              | Y3     | Y13 |  |  |
|              | Y4     | Y14 |  |  |
|              | Y5     | Y15 |  |  |
|              | Y6     | Y16 |  |  |
|              | Y7     | Y17 |  |  |
|              | +V0    | +V0 |  |  |
| L            |        |     |  |  |

#### FX5 intelligent function module

#### FX5-4AD



| CHI V+<br>CMI V+<br>CMI V+<br>CH2 V+<br>CH2 V+<br>CH2 V+<br>CH2 V+<br>CH2 V+<br>CH2 V+<br>CH2 CM<br>CH2 CM<br>CM<br>CH2 CM<br>CH2 CM<br>CM<br>CM<br>CM<br>CM<br>CM<br>CM<br>CM<br>CM<br>CM<br>CM<br>CM<br>CM<br>C | ]сн4 |
|---|------|

| FX5-8AD   |  | FX5-4LC   |
|---|--|---|
| CH1 [A/TC+<br>B/TC-<br>CH2 [A/TC+<br>B/TC-<br>CH3 [A/TC+<br>B/TC-<br>CH4 [B/TC-<br>CH4 [B/TC-<br>CH6 [A/TC+<br>B/TC-<br>CH6 [A/TC+<br>CH6 [A/TC+<br>CH7 [A/TC+<br>CH7 [A/TC+<br>B/TC-<br>CH8 [A/TC+ | b/VI+<br>COM<br>b/VI+<br>COM<br>b/VI+<br>COM<br>b/VI+<br>COM<br>b/VI+<br>COM<br>b/VI+<br>COM<br>b/VI+<br>COM<br>b/VI+<br>COM | OUT<br>OUT<br>COM<br>COM<br>CH1<br>CH1<br>CH2<br>A<br>B<br>CH2<br>A<br>B<br>CH3<br>A<br>CH3<br>A<br>B<br>CH3<br>A<br>B<br>CH4<br>A<br>B<br>CH4<br>B<br>CT<br>A<br>B<br>CT<br>A<br>B<br>CT<br>A<br>B<br>CT<br>A<br>B<br>CT<br>CT<br>A<br>CT<br>A |

#### OUT1 OUT3 OUT2 OUT4 COM1 COM2 NC CT MIN MD B/TC+/VL+ MD b/TC-/VL-CT B/TC+/VL+ ٦Ŋ DALDI b/TC-/VL-CT B/TC+/VL+ DAUD b/TC-/VL-CT B/TC+/VL+ b/TC-/VL-MN

#### FX5-20PG-P

|     | $\frown$ | - |     |
|-----|----------|---|-----|
| B20 | 0        | 0 | A20 |
| B19 | 0        | 0 | A19 |
| B18 | ۵        | 0 | A18 |
| B17 | 0        | 0 | A17 |
| B16 | 0        | 0 | A16 |
| B15 | 0        | 0 | A15 |
| B14 | 0        | 0 | A14 |
| B13 | ۵        | 0 | A13 |
| B12 | 0        | 0 | A12 |
| B11 | ۵        | 0 | A11 |
| B10 | ۵        | 0 | A10 |
| B9  | ۵        | 0 | A9  |
| B8  | ۵        | 0 | A8  |
| B7  | ۵        | 0 | A7  |
| B6  | ۵        | 0 | A6  |
| B5  | ۵        | 0 | A5  |
| B4  | ۵        | 0 | A4  |
| B3  | ۵        | 0 | A3  |
| B2  | ۵        | 0 | A2  |
| B1  | ۵        | 0 | A1  |

|         |             | Ax      |             |
|---------|-------------|---------|-------------|
| Pin No. | Signal name | Pin No. | Signal name |
| B20     | PULSER B-   | A20     | PULSER B+   |
| B19     | PULSER A-   | A19     | PULSER A+   |
| B18     | PULSE COM   | A18     | PULSE COM   |
| B17     | PULSE R     | A17     | PULSE R     |
| B16     | PULSE COM   | A16     | PULSE COM   |
| B15     | PULSE F     | A15     | PULSE F     |
| B14     | CLRCOM      | A14     | CLRCOM      |
| B13     | CLEAR       | A13     | CLEAR       |
| B12     | RDYCOM      | A12     | RDYCOM      |
| B11     | READY       | A11     | READY       |
| B10     | PG0COM      | A10     | PG0COM      |
| B9      | PG05        | A9      | PG05        |
| B8      | PG024       | A8      | PG024       |
| B7      | COM         | A7      | COM         |
| B6      | COM         | A6      | COM         |
| B5      | CHG         | A5      | CHG         |
| B4      | STOP        | A4      | STOP        |
| B3      | DOG         | A3      | DOG         |
| B2      | RLS         | A2      | RLS         |
| B1      | FLS         | A1      | FLS         |

#### FX5-20PG-D

B20

B19

B18

B17 0 0 A17

B16 0 0 A16

B15

B14

B13 0 0

B12 0 0 A12

B11 B10 0 0 A10

В9

B8

B7

B6

B5 B4

B3

B2

B1

0 0

0 0 A19

0 0

0 0 A15

۵

0 A11

0 0

0 0 A8

0 0 A7

A6

0 0 A4

0 0 AЗ

0 0 A2

0 0

A20

A18

A14

A13

A9

A5

A1

| Axi     | is 2 (AX2)  | Axi     | s 1 (AX1)   |
|---------|-------------|---------|-------------|
| Pin No. | Signal name | Pin No. | Signal name |
| B20     | PULSER B-   | A20     | PULSER B+   |
| B19     | PULSER A-   | A19     | PULSER A+   |
| B18     | PULSE R-    | A18     | PULSE R-    |
| B17     | PULSE R+    | A17     | PULSE R+    |
| B16     | PULSE F-    | A16     | PULSE F-    |
| B15     | PULSE F+    | A15     | PULSE F+    |
| B14     | CLRCOM      | A14     | CLRCOM      |
| B13     | CLEAR       | A13     | CLEAR       |
| B12     | RDYCOM      | A12     | RDYCOM      |
| B11     | READY       | A11     | READY       |
| B10     | PG0COM      | A10     | PG0COM      |
| B9      | PG05        | A9      | PG05        |
| B8      | PG024       | A8      | PG024       |
| B7      | COM         | A7      | COM         |
| B6      | COM         | A6      | COM         |
| B5      | CHG         | A5      | CHG         |
| B4      | STOP        | A4      | STOP        |
| B3      | DOG         | A3      | DOG         |
| B2      | RLS         | A2      | RLS         |
| B1      | FLS         | A1      | FLS         |

#### FX5-40SSC-S FX5-80SSC-S

|    |          |   | 1                     |
|----|----------|---|-----------------------|
| 26 |          | 1 | 13                    |
| 25 |          | þ | 12                    |
| 24 | þ        | þ | 11                    |
| 23 | þ        | þ | 10                    |
| 22 |          | þ | 9                     |
| 21 |          | þ | 9<br>8<br>7           |
| 20 | þ        | þ |                       |
| 19 | þ        | þ | 6<br>5<br>4<br>3<br>2 |
| 18 |          | þ | 5                     |
| 17 |          | þ | 4                     |
| 16 | þ        | þ | 3                     |
| 15 |          | þ |                       |
| 14 | <u>L</u> | 0 | ) 1                   |
|    | ~        |   | /                     |

| D: N    | 0: 1        | D' 11    | 0: 1        |
|---------|-------------|----------|-------------|
| Pin No. | Signal name | Pin No.  | Signal name |
| 1       | Idle        | 14       | Idle        |
| 2       | SG          | 15       | SG          |
| 3       | HA          | 16       | HB          |
| 4       | HAH         | 17       | HBH         |
| 5       | HAL         | 18       | HBL         |
| 6 to 9  | Idle        | 19 to 22 | Idle        |
| 10      | EMI         | 23       | EMI.COM     |
| 11      | DI1         | 24       | DI2         |
| 12      | DI3         | 25       | DI4         |
| 13      | COM         | 26       | COM         |

#### FX5-ENET FX5-ENET/IP

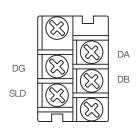
| 5 | 1 |
|---|---|
|   | 8 |

| Pin<br>No. | Signal<br>name | Description                                   |
|------------|----------------|---|
| INU.       |                |   |
| 1          | TP0+           | Data 0 transmission/reception (positive side) |
| 2          | TP0-           | Data 0 transmission/reception (negative side) |
| 3          | TP1+           | Data 1 transmission/reception (positive side) |
| 4          | TP2+           | Data 2 transmission/reception (positive side) |
| 5          | TP2-           | Data 2 transmission/reception (negative side) |
| 6          | TP1-           | Data 1 transmission/reception (negative side) |
| 7          | TP3+           | Data 3 transmission/reception (positive side) |
| 8          | TP3-           | Data 3 transmission/reception (negative side) |

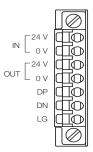
## **Terminal arrangement**

#### FX5-CCL-MS

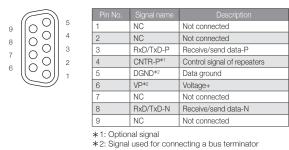
#### FX5-CCLIEF



|   | $\equiv$ | 1 | Pin<br>No. | Signal<br>name |   |   |
|---|----------|---|------------|----------------|---|---|
|   |          |   | 1          | TP0            | + | Data 0 transmission/reception (positive side) |
|   |          |   | 2          | TP0            | - | Data 0 transmission/reception (negative side) |
| 1 |          |   | 3          | TP1            | + | Data 1 transmission/reception (positive side) |
|   |          | 8 | 4          | TP2            | + | Data 2 transmission/reception (positive side) |
|   |          | 1 | 5          | TP2            | - | Data 2 transmission/reception (negative side) |
|   |          |   | 6          | TP1            | - | Data 1 transmission/reception (negative side) |
|   |          |   | 7          | TP3            | + | Data 3 transmission/reception (positive side) |
|   |          |   | 8          | TP3            | - | Data 3 transmission/reception (negative side) |



#### FX5-DP-M

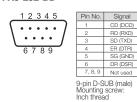


### **Expansion adapter**

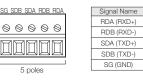
| X5-4AD-ADP | FX5-4DA-ADP | FX5-4AD-PT-ADP | FX5-4AD-TC-ADP | FX5-232ADP  |
|------------|-------------|----------------|----------------|---|
| V1+        | V1+         | L1+            | •              | 5 • Pin No. Signal<br>1 CD (DCD)  |
| 11+        | 11+         | L1-            | L1+            | 4 • 9 2 RD (RXD)  |
| COM1       | COM1        | 11-            | L1-            | 3<br>2<br>4<br>6<br>5<br>5<br>8<br>6<br>5<br>8<br>6<br>7<br>6<br>5<br>8<br>6<br>8<br>7<br>8<br>8<br>9<br>7<br>8<br>8<br>8<br>9<br>7<br>8<br>9<br>8<br>8<br>8<br>9<br>7<br>8<br>8<br>9<br>8<br>9 |
| V2+        | V2+         | L2+            | •              | 6 DR (DSR)<br>7, 8, 9 Not used  |
| 12+        | 12+         | L2-            | L2+            | 9-pin D-SUB (male)  |
| COM2       | COM2        | 12-            | L2-            | Mounting screw:<br>Inch thread  |
| V3+        | V3+         | L3+            | •              |   |
| 13+        | 13+         | L3-            | L3+            |   |
| COM3       | COM3        | 13-            | L3-            | FX5-485ADP  |
| V4+        | V4+         | L4+            | •              |   |
| 14+        | 14+         | L4-            | L4+            | RDA (RXD+)  |
| COM4       | COM4        | 14-            | L4-            | 5 poles   |
| ÷          | •           | •              | •              | © SDB (TXD-)<br>© SG (GND)  |

### **Expansion board**

#### FX5-232-BD



#### FX5-485-BD



#### FX5-422-BD-GOT



8-pin MINI-DIN (female)

#### FX5-ASL-M

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### FX5 extension power supply module

#### FX5-1PSU-5V



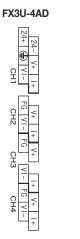
## FX3 extension power supply module

FX3U-1PSU-5V



## FX3 intelligent function module

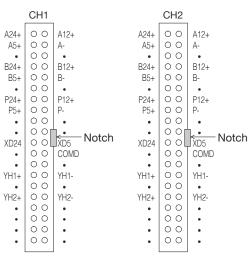
FX3U-4DA



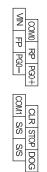
| CHI | 24- V+ I+<br>24+ 🕒 VI- |
|-----|------------------------|
| CH2 | • V+<br>V+<br>+        |
| СНЗ |                        |
| CH4 | +<br>V+<br>V-<br>+     |

| FX3U-4LC |  |
|----------|--|
| Ð        | CT FG PTB/TC-/COM CT FG PTB/TC-/COM OUT1 OUT2        |
| 24+ 24-  | CH1 PTA/•/• PTB/TC+//L+ CH2 PTA/•/• PTB/TC+//L+ COM1 |
|          |  |
| •        | CT FG PTB/TC-/COM CT FG PTB/TC-/COM OUT3 OUT4        |
| • •      | CT PTA/*/* PTB/TC+/VL+ CT PTA/*/* PTB/TC+/VL+ COM2   |

#### FX3U-2HC







#### FX3U-64CCL FX3U-16CCL-M





FX3U-128ASL-M



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#### FX3U-32DP

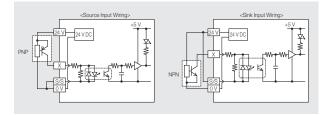
|     | $\sim$     |                              |            |             |                     |
|-----|------------|------------------------------|------------|-------------|---------------------|
| /   | (• u       | <ul> <li>Assigned</li> </ul> | Pin No.    | Signal name | Description         |
| 600 | 4.         | O Not assigne                | 3          | RXD/TXD-P   | Receive/send data-P |
| ∞•  |            |                              | 4          | RTS         | Ready to send       |
| ~0  | <b>⇔</b> ● |                              | 5          | DGND        | Data ground         |
|     | N 0        |                              | 6          | VP          | Voltage+            |
| (   | -0         |                              | 8          | RXD/TXD-N   | Receive/send data-N |
|     | $\bigcirc$ |                              | 1, 2, 7, 9 | NC          | Not assigned        |

| (1) | CPU category                           | FX5U, FX5    | UC etc  |                      |   |           | Mor          | del system  |            |         |     |
|-----|--|--------------|---|----------------------|---|-----------|--------------|-------------|------------|---------|-----|
| (2) | Type category                          | C (Extensio  | on connector type)<br>Insion cable type)                  |                      |   |           | moc          |             |            |         |     |
| (3) | Total number of<br>input/output points | 8, 16, 32, 4 | 10, 64, 80, 96, etc.                                      |                      |   |           |              |             |            |         |     |
|     |  | М            | CPU module  | FX5                  |   | C         | 20           | RЛ          | D          | /ES     |     |
| (4) | Module category                        | E            | Extension devices including both input and output devices | ГЛЭ                  | - | C         | 32           | IVI         | Π          | /E3     |     |
|     |  | EX           | Input extension module                                    | (4)                  |   | (0)       | ( <b>0</b> ) | (4)         | (5)        | (6)     | (7) |
|     |  | EY           | Output extension module                                   | (1)                  |   | (2)       | (3)          | (4)         | (5)        | (6)     | (7) |
| (5) | Output type                            | R            | Relay output  |                      |   |           |              |             |            |         |     |
| (5) | Output type                            | Т            | Transistor output   | 7                    |   |           |              |             |            |         |     |
|     |  |              |   |                      |   |           |              |             |            |         |     |
|     |  | Symbol       | Power supply  | Input type           |   | Transisto |              |             | Input type | Transis |     |
|     |  | /ES          | AC  | 24 V DC, sink/source |   | sink      |              | sink/source | Э          | -       |     |
| 6)  | Power supply, input/<br>output system  | /ESS         | AC  | 24 V DC, sink/source |   | source    |              | -           |            | source  |     |
|     | ouiput system                          | /DS          | DC  | 24 V DC, sink/source |   | sink      |              | sink/source | Э          | -       |     |
|     |  | /DSS         | DC  | 24 V DC, sink/source |   | source    |              | -           |            | source  |     |
|     |  | /D           | DC  | 24 V DC, sink        |   | sink      |              | sink        |            | sink    |     |
| (7) | Other suffix symbols                   | -H           | High-speed input/output<br>function expansion             |                      |   |           |              |             |            |         |     |
|     |  | -TS          | Spring clamp terminal block                               | 1                    |   |           |              |             |            |         |     |

## $\diamond$ Type system (CPU module, input/output extension device)

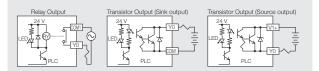
#### ◇ Input signal format

- When a contactless sensor output is connected to PLC, PNP open collector transistor output can be handled via source input wiring, and NPN open collector transistor output via sink input wiring.
- S/S terminal and 0 V terminal are short-circuited by source input wiring. (Left side of the drawing below)
   S/S terminal and 24 V terminal are short-circuited by sink input wiring. (Right side of the drawing below)



## ○ Output signal format

- Relay output type is mechanically isolated by a relay, while transistor output type is isolated by a photocoupler. In addition, LED for output indication is driven by internal power supply.
- Transistor output is made up of NPN open collector output (sink [-common]) system and NPN emitter follower output (source [+common]) system.



# **Products list**

## $\diamond$ CPU module

| Mastal              |                             |           | Specifications      |             |                   | Description      |
|---------------------|-----------------------------|-----------|---------------------|-------------|-------------------|------------------|
| Model               | Rated voltage               |           |                     |             |                   | Description page |
| ◆ FX5U CPU modules  |                             |           |                     |             |                   |                  |
| FX5U-32MR/ES        |                             |           |                     |             | Relay             | 44               |
| FX5U-32MT/ES        | ]                           | 16 points |                     | 16 points   | Transistor/sink   | 44               |
| FX5U-32MT/ESS       |                             |           |                     |             | Transistor/source | 44               |
| FX5U-64MR/ES        |                             |           |                     |             | Relay             | 44               |
| FX5U-64MT/ES        | 100 to 240 V AC<br>50/60 Hz | 32 points | 24 V DC sink/source | 32 points   | Transistor/sink   | 44               |
| FX5U-64MT/ESS       | 100/00/12                   |           |                     |             | Transistor/source | 44               |
| FX5U-80MR/ES        | ]                           |           | ]                   |             | Relay             | 44               |
| FX5U-80MT/ES        | ]                           | 40 points |                     | 40 points   | Transistor/sink   | 44               |
| FX5U-80MT/ESS       |                             |           |                     |             | Transistor/source | 44               |
| FX5U-32MR/DS        |                             |           |                     |             | Relay             | 45               |
| FX5U-32MT/DS        | ]                           | 16 points |                     | 16 points   | Transistor/sink   | 45               |
| FX5U-32MT/DSS       |                             |           |                     |             | Transistor/source | 45               |
| FX5U-64MR/DS        | ]                           |           |                     |             | Relay             | 45               |
| FX5U-64MT/DS        | 24 V DC                     | 32 points | 24 V DC sink/source | 32 points   | Transistor/sink   | 45               |
| FX5U-64MT/DSS       |                             |           |                     |             | Transistor/source | 45               |
| FX5U-80MR/DS        | ]                           |           |                     |             | Relay             | 45               |
| FX5U-80MT/DS        |                             | 40 points |                     | 40 points   | Transistor/sink   | 45               |
| FX5U-80MT/DSS       |                             |           |                     |             | Transistor/source | 45               |
| ◆ FX5UC CPU modules |                             |           |                     |             |                   |                  |
| FX5UC-32MT/D        |                             |           | 24 V DC sink        |             | Transistor/sink   | 51               |
| FX5UC-32MT/DSS      | ]                           | 16 points |                     | 16 points   | Transistor/source | 51               |
| FX5UC-32MT/DS-TS    |                             | To points | 24 V DC sink/source | TO POINS    | Transistor/sink   | 51               |
| FX5UC-32MT/DSS-TS   |                             |           |                     |             | Transistor/source | 51               |
| FX5UC-32MR/DS-TS    | 24 V DC                     | 16 points | 24 V DC sink/source | 16 points   | Relay             | 51               |
| FX5UC-64MT/D        |                             | 20 pointo | 24 V DC sink        | - 32 points | Transistor/sink   | 51               |
| FX5UC-64MT/DSS      |                             | 32 points | 24 V DC sink/source | 52 points   | Transistor/source | 51               |
| FX5UC-96MT/D        | ]                           | 48 points | 24 V DC sink        | 48 points   | Transistor/sink   | 51               |
| FX5UC-96MT/DSS      |                             | 40 POILIE | 24 V DC sink/source | 40 points   | Transistor/source | 51               |

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## $\Diamond$ I/O module

|  |                          |           |                     |           |                     | Description      |
|--|--------------------------|-----------|---------------------|-----------|---------------------|------------------|
|  | Rated voltage            |           | Input               |           | Output              | Description page |
| Extension cable ty                       | /pe                      |           |                     |           |                     |                  |
| Input module                             |                          |           |                     |           |                     |                  |
| X5-8EX/ES                                | Supplied from CPU module | 8 points  | 24 V DC sink/source | -         | -                   | 58               |
| X5-16EX/ES                               | Supplied from CPO module | 16 points | 24 V DC SINK/SOURCE | -         | -                   | 58               |
| Output module                            |                          |           |                     |           |                     |                  |
| X5-8EYR/ES                               |                          |           |                     |           | Relay               | 58               |
| X5-8EYT/ES                               |                          | -         | -                   | 8 points  | Transistor/sink     | 58               |
| X5-8EYT/ESS                              | Supplied from CPU module |           |                     |           | Transistor/source   | 58               |
| X5-16EYR/ES                              |                          |           |                     |           | Relay               | 58               |
| X5-16EYT/ES                              |                          | -         | -                   | 16 points | Transistor/sink     | 58               |
| X5-16EYT/ESS                             |                          |           |                     |           | Transistor/source   | 58               |
| Input/output module                      | :                        |           |                     |           |                     |                  |
| X5-16ER/ES                               |                          |           |                     |           | Relay               | 58               |
| X5-16ET/ES                               | Supplied from CPU module | 8 points  | 24 V DC sink/source | 8 points  | Transistor/sink     | 58               |
| X5-16ET/ESS                              |                          |           |                     |           | Transistor/source   | 58               |
| <ul> <li>High-speed pulse inj</li> </ul> | put/output module        |           |                     |           |                     |                  |
| X5-16ET/ES-H                             | Supplied from CPU module | 8 points  | 24 V DC sink/source | 8 points  | Transistor/sink     | 59               |
| K5-16ET/ESS-H                            |                          |           |                     |           | Transistor/source   | 59               |
| Powered input/output                     | ut module                |           |                     |           |                     |                  |
| K5-32ER/ES                               | 100 to 240 V AC          |           |                     |           | Relay               | 57               |
| K5-32ET/ES                               | 50/60 Hz                 | 16 points | 24 V DC sink/source | 16 points | Transistor/sink     | 57               |
| K5-32ET/ESS                              |                          |           |                     |           | Transistor/source   | 57               |
| X5-32ER/DS                               |                          |           |                     |           | Relay               | 57               |
| X5-32ET/DS                               | 24 V DC                  | 16 points | 24 V DC sink/source | 16 points | Transistor/sink     | 57               |
| X5-32ET/DSS                              |                          |           |                     |           | Transistor/source   | 57               |
| Extension connec                         | tor type                 |           |                     |           |                     |                  |
| Input module                             |                          |           |                     |           |                     |                  |
| X5-C16EX/D                               |                          | 16 points | 24 V DC sink        |           | -                   | 59               |
| K5-C16EX/DS                              |                          |           | 24 V DC sink/source |           |                     | 59               |
| X5-C32EX/D<br>X5-C32EX/DS                | Supplied from CPU module | 22 pointo | 24 V DC sink        |           |                     | 59               |
| X5-C32EX/DS<br>X5-C32EX/DS-TS            |                          | 32 points | 24 V DC sink/source | -         | -                   | 59               |
|  |                          |           |                     |           |                     | 29               |
| Output module     X5-C16EYT/D            |                          |           |                     |           | Transistor/sink     | 59               |
| X5-C16EYT/DSS                            |                          | -         | -                   | 16 points | Transistor/sink     | 59               |
| X5-C16EYR/D-TS                           |                          | _         |                     | 16 points | Relay               | 59               |
| K5-C32EYT/D                              | Supplied from CPU module |           |                     | TO POILIS | Transistor/sink     | 59               |
| K5-C32EYT/DSS                            |                          |           |                     |           | Transistor/sink     | 59               |
| (5-C32EYT/D-TS                           |                          | -         | -                   | 32 points | Transistor/source   | 59               |
| (5-C32EYT/DSS-TS                         |                          |           |                     |           | Transistor/sink     | 59               |
| Input/output module                      |                          |           |                     |           | ridi ISISTOL/SOUICE | 09               |
| X5-C32ET/D                               |                          |           | 24 V DC sink        |           | Transistor/sink     | 59               |
| X5-C32ET/DSS                             |                          |           |                     |           | Transistor/sink     | 59               |
| X5-C32ET/DS-TS                           | Supplied from CPU module | 16 points | 24 V DC sink/source | 16 points | Transistor/source   | 59               |
| A0 002E1/D0-10                           |                          |           |                     |           | Transistor/source   | 59               |

## ♦ Expansion boards & Expansion adapter

| Model          | Specifications  | Description page |
|----------------|---|------------------|
| FX5-232-BD     | For RS-232C communication                                       | 108              |
| FX5-485-BD     | For RS-485 communication  | 108              |
| FX5-422-BD-GOT | For GOT connection RS-422 communication                         | 109              |
| FX5-232ADP     | For RS-232C communication                                       | 109              |
| FX5-485ADP     | For RS-485 communication  | 110              |
| FX5-4AD-ADP    | 4 ch analog input adapter                                       | 67               |
| FX5-4AD-PT-ADP | 4 ch temperature sensor (resistance temperature detector) input | 72               |
| FX5-4AD-TC-ADP | 4 ch temperature sensor (thermocouple) input                    | 73               |
| FX5-4DA-ADP    | 4 ch analog output adapter                                      | 67               |

## $\diamond$ FX5 extension power supply module, bus conversion module, connector conversion module

| Model        | Specifications   | Description page |
|--------------|--|------------------|
| FX5-1PSU-5V  | FX5U (AC power supply type) extension power supply                               | 124              |
| FX5-C1PS-5V  | FX5U (DC power supply type)/ FX5UC extension power supply                        | 125              |
| FX5-CNV-BUS  | Bus conversion FX5 (extension cable type) $\rightarrow$ FX3                      | 124              |
| FX5-CNV-BUSC | Bus conversion FX5 (extension connector type) - FX3                              | 124              |
| FX5-CNV-IF   | Connector conversion FX5 (extension cable type) → FX5 (extension connector type) | 125              |
| FX5-CNV-IFC  | Connector conversion FX5 (extension connector type) → FX5 (extension cable type) | 125              |

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## ◇ FX5 intelligent function module

| Model       | Specifications  | Description page |
|-------------|---|------------------|
| FX5-4AD     | 4 ch analog input   | 68               |
| FX5-4DA     | 4 ch analog output  | 69               |
| FX5-8AD     | 8 ch multi input  | 68               |
| FX5-4LC     | 4 ch temperature control                                    | 75               |
| FX5-20PG-P  | 2-axis pulse train positioning (transistor output)          | 87               |
| FX5-20PG-D  | 2-axis pulse train positioning (differential driver output) | 87               |
| FX5-40SSC-S | Simple motion 4-axis control                                | 89               |
| FX5-80SSC-S | Simple motion 8-axis control                                | 89               |
| FX5-ENET    | Ethernet module   | 101              |
| FX5-ENET/IP | EtherNet/IP module  | 102              |
| FX5-CCL-MS  | CC-Link system master/intelligent device station            | 97               |
| FX5-CCLIEF  | Intelligent device station for CC-Link IE Field network     | 96               |
| FX5-ASL-M   | AnyWireASLINK system master module                          | 104              |
| FX5-DP-M    | PROFIBUS-DP master module                                   | 107              |

## **○ FX3 extension power supply module**

| EX31-1PS11-5V EX3 extension power supply 125 | Model        |                            | Description page |
|--|--------------|----------------------------|------------------|
|  | FX3U-1PSU-5V | FX3 extension power supply | 125              |

## ◇ FX3 intelligent function module

| Model         | Specifications                    | Description page |
|---------------|-----------------------------------|------------------|
| FX3U-4AD      | 4 ch analog input                 | 69               |
| FX3U-4DA      | 4 ch analog output                | 70               |
| FX3U-4LC      | 4 ch temperature control          | 76               |
| FX3U-1PG      | Positioning pulse output 200 kpps | 88               |
| FX3U-2HC      | 2 ch 200 kHz high-speed counter   | 80               |
| FX3U-16CCL-M  | Master for CC-Link V2             | 98               |
| FX3U-64CCL    | Interface for CC-Link V2          | 99               |
| FX3U-128ASL-M | Master for AnyWireALSINK system   | 105              |
| FX3U-32DP     | PROFIBUS-DP slave                 | 107              |

#### ♦ Software package

| Туре                        | Model             | Specifications   |     |
|-----------------------------|-------------------|--|-----|
| MELSOFT iQ Works (DVD-ROM)  | SW2DND-IQWK-E*1   | FA engineering software (English version)*2  | 119 |
| MELSOFT GX Works3 (DVD-ROM) | SW1DND-GXW3-E     | PLC engineering software*2 (English version bundled product: GX Works 2, with GX Developer included) | 120 |
| MX Component                | SW4DNC-ACT-E      | ActiveX library for communication  | 120 |
| MX Sheet                    | SW2DNC-SHEET-E    | Microsoft® Excel® communication support tool   | 120 |
| MX Works                    | SW2DNC-SHEETSET-E | A set of MX Component and MX Sheet   | 120 |

\*1: If you have a conventional model (SW1DN□-IQWK-E), you cannot update. Please purchase an upgraded version separately.

For details, please contact our sales representative. \*2: For the corresponding models of each software, please refer to the manual of each product.

## ♦ Communication cable

| FX-232CAB-1     3 m     9-pin D-sub (female) ↔ 9-pin D-sub (female) (for DOS/V, etc.) | 116 |
|---|-----|

## ◇ Input/output cable

| Model           |       | Specifications  |     |  |  |
|-----------------|-------|---|-----|--|--|
| FX-16E-150CAB   | 1.5 m |   | 128 |  |  |
| FX-16E-300CAB   | 3.0 m | For connection between terminal module and FX5 PLC<br>(Flat cable with connectors at both ends)             | 128 |  |  |
| FX-16E-500CAB   | 5.0 m | ( nat cable with connectors at both ends)   | 128 |  |  |
| FX-16E-500CAB-S | 5.0 m | Loose wire with connector on one end  | 128 |  |  |
| FX-16E-150CAB-R | 1.5 m |   | 128 |  |  |
| FX-16E-300CAB-R | 3.0 m | For connection between terminal module and FX5 PLC<br>(Multi-core round cable with connectors at both ends) | 128 |  |  |
| FX-16E-500CAB-R | 5.0 m | (Multi-core round cable with connectors at both ends)   | 128 |  |  |

## ◇ Input/output connector

| Model           | Specifications  | Description page |
|-----------------|---|------------------|
| FX2C-I/O-CON    | 20-pin connector and 10 pressure connectors for flat cable  | 128              |
| FX2C-I/O-CON-S  | 20-pin connector and 5 sets of housing for loose wire and crimp contact (for 0.3 mm <sup>2</sup> )      | 128              |
| FX2C-I/O-CON-SA | 20-pin connector and 5 sets of housing for loose wire and crimp contact (for 0.5 mm <sup>2</sup> )      | 128              |
| A6CON1          | 40-pin connector, soldered type for external device connection (straight protrusion)                    | 128              |
| A6CON2          | 40-pin connector, crimped type for external device connection (straight protrusion)                     | 128              |
| A6CON4          | 40-pin connector, soldered type for external device connection (both straight/inclined protrusion type) | 128              |
| FX-I/O-CON2-S   | 40-pin connector, 2 sets for discrete wire, AWG22 (0.3 mm <sup>2</sup> )                                | 128              |
| FX-I/O-CON2-SA  | 40-pin connector, 2 sets for discrete wire, AWG20 (0.5 mm <sup>2</sup> )                                | 128              |

## $\diamond$ Terminal module

| Model              | Specifications  | Description page |
|--------------------|---|------------------|
| FX-16E-TB          | 16 input or output points   | 127              |
| FX-32E-TB          | 32 input or output points   | 127              |
| FX-16E-TB/UL       | 16 input or output points   | 127              |
| FX-32E-TB/UL       | 32 input or output points   | 127              |
| FX-16EYR-TB        | 16 relay output points 2 A/1 point (8 A/4 points)                           | 127              |
| FX-16EYS-TB        | 16 triac output points, 0.3 A/1 point (0.8 A/4 points)                      | 127              |
| FX-16EYT-TB        | 16 transistor output points, 0.5 A/1 point (0.8 A/4 points) (sink output)   | 127              |
| FX-16EYR-ES-TB/UL  | 16 relay output points 2 A/1 point (8 A/4 points)                           | 127              |
| FX-16EYS-ES-TB/UL  | 16 triac output points, 0.3 A/1 point (0.8 A/4 points)                      | 127              |
| FX-16EYT-ES-TB/UL  | 16 transistor output points, 0.5 A/1 point (0.8 A/4 points) (sink output)   | 127              |
| FX-16EYT-ESS-TB/UL | 16 transistor output points, 0.5 A/1 point (0.8 A/4 points) (source output) | 127              |

#### $\diamond$ Power cable

| Model         | Specifications  | Description page |
|---------------|---|------------------|
| FX2NC-100MPCB | FX5UC CPU module, for 24 V DC power supply  | 129              |
| FX2NC-100BPCB | Extension module (extension connector type), for 24 V DC input power supply                   | 129              |
| FX2NC-10BPCB1 | Extension module (extension connector type), for 24 V DC input power supply connection wiring | 129              |

## ♦ Extended cable/connector conversion adapter

| Model    |   |   |     |
|----------|---|---|-----|
| FX5-30EC | 30 cm   | For the extension of FX5 extension module | 126 |
| FX5-65EC | 65 cm   | For the extension of FAS extension module | 126 |
|          | For the connection between an extended extension cable and an FX5 input/output module (extension cable type), a high-speed pulse input/<br>output module, or an FX5 intelligent function module |   | 126 |

## $\bigcirc$ SD memory card & battery

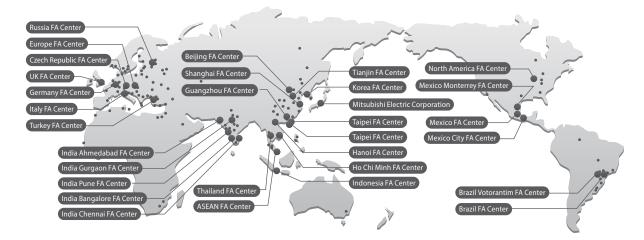
| Model         | Specifications           |     |
|---------------|--------------------------|-----|
| NZ1MEM-2GBSD  | SD memory card (2 GB)    | 123 |
| NZ1MEM-4GBSD  | SDHC memory card (4 GB)  | 123 |
| NZ1MEM-8GBSD  | SDHC memory card (8 GB)  | 123 |
| NZ1MEM-16GBSD | SDHC memory card (16 GB) | 123 |
| FX3U-32BL     | Battery                  | 123 |

**Products list** 

memo

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